

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	INTELLECTUAL VENTURES ASSETS 16 LLC	10/22/2015
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	Property Type	Number
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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures Assets 16 LLC, a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Quest Patent Research Corporation, a Delaware company with an address at 411 Theodore Fremd Ave., Suite 206S, Rye, New York 10580 (“*Assignee*”), all of Assignor's right, title, and interest in and to the following (collectively, the “*Assigned Patent Rights*”):

- (a) the patents and patent applications listed in the table below (the “*Patents*”);

Patent or application no.	Country	Filing Date	Title of Patent and Inventors
7100061 (09/484516)	US	8/29/2006 (1/18/2000)	Adaptive power control Andrew Read; Godfrey P. D'Souza; H. Peter Anvin; Keith Klayman; Marc Fleischmann; Sameer Halepete; Thomas Lawrence; Zongjian Chen
7596708 (11/411309)	US	9/29/2009 (4/25/2006)	Adaptive power control Andrew Read; Godfrey P. D'Souza; Keith Klayman; Sameer Halepete; Zongjian Chen; H. Peter Anvin; Marc Fleischmann; Thomas Lawrence
8566627 (12/502685)	US	10/22/2013 (7/14/2009)	Adaptive power control Godfrey P. D'Souza; Thomas Lawrence; Keith Klayman; Marc Fleischmann; Sameer Halepete; Zongjian Chen; Andrew Read; H. Peter Anvin
8806247 (13/725901)	US	8/12/2014 (12/21/2012)	ADAPTIVE POWER CONTROL Sameer Halepete; H. Peter Alvin; Zongjian Chen; Godfrey P. D'Souza; Marc Fleischmann; Keith Klayman; Thomas Lawrence; Andrew Read
7118273 (10/411955)	US	10/10/2006 (4/10/2003)	System for on-chip temperature measurement in integrated circuits William N. Schnaitter
7108420 (10/961311)	US	9/19/2006 (10/7/2004)	System for on-chip temperature measurement in integrated circuits William N. Schnaitter
11/524526	US	9/19/2006	System for on-chip temperature measurement in integrated circuits William N. Schnaitter
13/243976	US	9/23/2011	System for on-chip temperature measurement in integrated circuits William N. Schnaitter

5978876 (08/834242)	US	11/2/1999 (4/14/1997)	System and method for controlling communications between subsystems Paul E. Greaves
5650666 (08/562125)	US	7/22/1997 (11/22/1995)	Method and apparatus for preventing cracks in semiconductor die Marc Hartranft; Pat Zicoello
5846874 (08/810494)	US	12/8/1998 (2/28/1997)	Method and apparatus for preventing cracks in semiconductor die Marc Hartranft; Pat Zicoello

(b) any reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents and any patents or patent applications which correspond to or claim priority to any of the foregoing, and all foreign counterparts of the foregoing;

(c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and

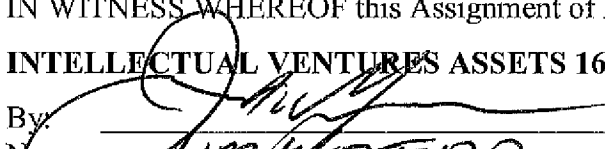
(d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain monetary damages and any other remedies of any kind for past, current and future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, take all reasonable steps necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights

IN WITNESS WHEREOF this Assignment of Patent Rights is executed on 10/22, 2015.

INTELLECTUAL VENTURES ASSETS 16 LLC:

By: 
Name: JIM WESTFIELD
Title: AUTHORIZED PERSON

ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures Assets 16 LLC, a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 ("**Assignor**"), does hereby sell, assign, transfer, and convey unto Quest Patent Research Corporation, a Delaware company with an address at 411 Theodore Fremd Ave., Suite 206S, Rye, New York 10580 ("**Assignee**"), its right, title, and interest in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the "**Certain Assets**");

Patent or (application no.)	Country	Filing Date	Title of Patent and Inventors
7100061C1 (95/000243)	US	6/13/2007	Adaptive power control Andrew Read; Godfrey P. D'Souza; H. Peter Anvin; Keith Klayman; Marc Fleischmann; Sameer Halepete; Thomas Lawrence; Zongjian Chen
(PCT/US2001/001684)	US	1/16/2001	Adaptive power control Andrew Read; Godfrey P. D'Souza; H. Peter Anvin; Keith Klayman; Marc Fleischmann; Sameer Halepete; Thomas Lawrence; Zongjian Chen
(10/824702)	US	4/14/2004	System for on-chip temperature measurement in integrated circuits Paul E. Greaves

Assignor assigns to Assignee all of its rights to the inventions, invention disclosures, and discoveries in the assets listed above, together, with its rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to make available to Assignee all records regarding the Certain Assets.

The terms and conditions of this Assignment of Rights in Certain Assets will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

DATED this 22 day of OCTOBER 2015.

ASSIGNOR:

INTELLECTUAL VENTURES ASSETS 16 LLC

By: 

Name: JIM WEISFELD

Title: AUTHORIZED PERSON