

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3631220

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
YAN XUN XUE	11/20/2015
HAMZA YILMAZ	11/21/2015
YUEH-SE HO	11/20/2015
JUN LU	11/20/2015
ZHIQIANG NIU	11/20/2015
GUO FENG LIANG	11/23/2015
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State/Country:	CALIFORNIA
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Property Type	Number
Application Number:	14950997
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NAME OF SUBMITTER:	CHEN-CHI LIN
SIGNATURE:	/Chen-Chi Lin/
DATE SIGNED:	11/24/2015
Total Attachments: 5	
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To:

US Patent & Trademark Office
Assignment Recordation Services
P.O. Box 1450
Alexandria, VA 22313-1450, USA

Subject: Assignment Letter

Dear Sir/madam:

With this letter, we hereby assign the following subject US utility patent application entitled:

**A WAFER PROCESS FOR MOLDED CHIP SCALE PACKAGE (MCSP) WITH
THICK BACKSIDE METALLIZATION**

Inventors: Yan Xun Xue, Hamza Yilmaz, Yuch-Se Ho, Jun Lu, Zhiqiang Niu, Guo Feng
Lian

to the following receiving party:

Name: Alpha and Omega Semiconductor Incorporated

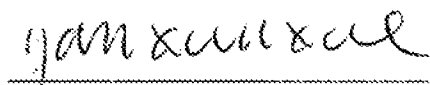
Officer name: Mike F. Chang

Title: CEO

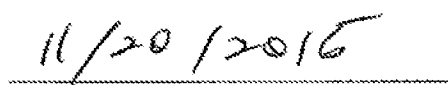
Street address: 475 Oakmead Pkwy, Sunnyvale, CA 94085, USA

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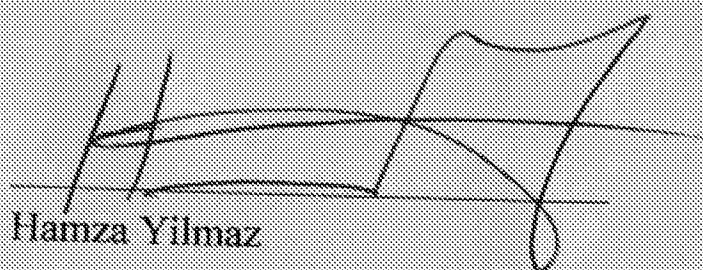
Signature and Date of Conveying Party:



Yan Xun Xue



Date



Hamza Yilmaz

Nov. 21, 2015

Date

Yueh- Se Ho

Date

Jun Lu

Date

Zhiqiang Niu

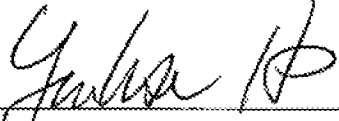
Date

Guo Feng Lian

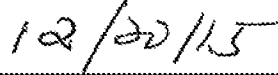
Date

Hamza Yilmaz

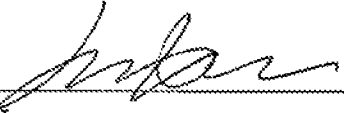
Date



Yuch-Se Ho



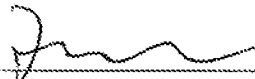
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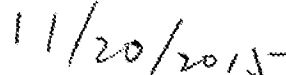
Jun Lu



Date



Zhiqiang Niu



Date

Guo Feng Lian

Date

Hamza Yilmaz

Date

Yueh- Se Ho

Date

Jun Lu

Date

Zhiqiang Niu

Date



Guo Feng Lian

11.23.2015

Date