

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PARIJAT BISWAS	12/23/2015
SHYAM DATTA	01/12/2016
SUBHRAJYOTI CHAKRABORTY	12/18/2015
MINAKSHI CHAKRAVORTY	12/23/2015
RECEIVING PARTY DATA	
Name:	SYNOPSYS, INC.
Street Address:	690 EAST MIDDLEFIELD ROAD
City:	MOUNTAIN VIEW
State/Country:	CALIFORNIA
Postal Code:	94043
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	14882414
CORRESPONDENCE DATA	
Fax Number:	(650)938-5200
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	6503357607
Email:	aeidinger@fenwick.com
Correspondent Name:	RAJIV P. PATEL
Address Line 1:	FENWICK & WEST LLP
Address Line 2:	801 CALIFORNIA STREET
Address Line 4:	MOUNTAIN VIEW, CALIFORNIA 94041
ATTORNEY DOCKET NUMBER:	22524-31031/US
NAME OF SUBMITTER:	RAJIV P. PATEL, REG. NO. 39,327
SIGNATURE:	/Rajiv P. Patel/
DATE SIGNED:	01/20/2016
Total Attachments: 8	
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source=31031_US_Executed_Assignment#page8.tif

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred to as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **Synopsys, Inc.**, a Delaware Corporation, having a place of business at 690 East Middlefield Road, Mountain View, CA 94043 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the following provisional or non-provisional application filed under 35 U.S.C. § 111, design application filed under 35 U.S.C. § 171, international application filed according to the Patent Cooperation Treaty (PCT), or U.S. national phase application filed under 35 U.S.C. § 371 ("APPLICATION"):

- Application No. 14/882,414, entitled "**Signal Reconstruction in Sequential Logic Circuitry**," filed on October 13, 2015.

2. The entire worldwide right, title, and interest in and to:
(a) the APPLICATION; (b) all applications claiming priority from the APPLICATION; (c) all provisional, utility, divisional, continuation, substitute, renewal, reissue, and other applications related thereto that have been or may be filed in the United States or elsewhere in the world; (d) all patents (including reissues and re-examinations) that may be granted on the applications set forth in (a), (b), and (c) above; and (e) all right of priority in the APPLICATION and in any underlying provisional or foreign application, together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

Title:	Signal Reconstruction in Sequential Logic Circuitry		
Filed:	October 13, 2015	Attorney	22524-31031/US
		Docket #:	
Application #:	14/882,414	2609US01	P2015,0735 US E

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This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

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- The APPLICATION was made or authorized to be made by INVENTOR.
- INVENTOR believes that INVENTOR is the original inventor or an original joint inventor of a claimed invention in the APPLICATION.

INVENTOR hereby acknowledges that any willful false statement made in this document is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

Name and Signature

Date of Signature

Parijat Biswas

Name and Signature

Date of Signature

Shyam Datta

Name and Signature

Date of Signature

Subhrajyoti Chakraborty
Subhrajyoti Chakraborty

18/12/15

Name and Signature

Date of Signature

Minakshi Chakravorty

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Name and Signature

Parijat Biswas

Parijat Biswas

Date of Signature

December 23, 2015

Name and Signature

Shyam Datta

Date of Signature

Name and Signature

Subhrajyoti Chakraborty

Date of Signature

Name and Signature

Minakshi Chakravorty

Date of Signature

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