

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SHINICHI WATANUKI	02/12/2016
ATSURO INADA	02/11/2016
RECEIVING PARTY DATA	
Name:	RENESAS ELECTRONICS CORPORATION
Street Address:	2-24, TOYOSU 3-CHOME
City:	KOUTOU-KU, TOKYO
State/Country:	JAPAN
Postal Code:	135-0061
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15062153
CORRESPONDENCE DATA	
Fax Number:	(703)956-7647
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	(703) 956-7650
Email:	ctexidor@sgriplaw.com, docketing@sgriplaw.com
Correspondent Name:	MITCHELL W. SHAPIRO
Address Line 1:	SHAPIRO, GABOR AND ROSENBERGER, PLLC
Address Line 2:	8607 WESTWOOD CENTER DRIVE, SUITE 320
Address Line 4:	TYSONS CORNER, VIRGINIA 22182
ATTORNEY DOCKET NUMBER:	XA-13078
NAME OF SUBMITTER:	CHERRISE TEXIDOR
SIGNATURE:	/Cherrise Texidor/
DATE SIGNED:	03/07/2016
Total Attachments: 2	
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source=XA-13078 Assignment#page2.tif	

ASSIGNMENT
(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Electronics Corporation, a corporation organized under the laws of Japan, located at 2-24, Toyosu 3-chome, Koutou-ku, Tokyo 135-0061, Japan receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Electronics Corporation, its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted,

to be held and enjoyed by said Renesas Electronics Corporation, its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Electronics Corporation.

Signed on the date(s) indicated aside our signatures:

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) <u>Shinichi Watanuki</u> (Shinichi WATANUKI)	<u>February 12, 2016</u>
2) _____ (Atsuro INADA)	_____
3) _____	_____
4) _____	_____
5) _____	_____
6) _____	_____

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And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Electronics Corporation.

Signed on the date(s) indicated aside our signatures:

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) _____ (Shinichi WATANUKI)	_____
2) <u>Atsuro Inada</u> _____ (Atsuro INADA)	<u>February 11, 2016</u>
3) _____	_____
4) _____	_____
5) _____	_____
6) _____	_____