PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT3774049

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
CREDIT SUISSE (F/K/A CREDIT SUISSE FIRST BOSTON)	03/17/2008

RECEIVING PARTY DATA

Name:	AMI SEMICONDUCTOR, INC.
Street Address:	2300 BUCKSKIN ROAD
City:	POCATELLO
State/Country:	IDAHO
Postal Code:	83201

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	10214056

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	AMI01-504
NAME OF SUBMITTER:	KELLY A. HALL
SIGNATURE:	/Kelly A. Hall/
DATE SIGNED:	03/08/2016

Total Attachments: 9
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EXECUTION VERSION

U.S. PATENTS RELEASE OF SECURITY INTEREST

THIS U.S. PATENTS RELEASE dated as of March 17, 2008 by CREDIT SUISSE (formerly known as Credit Suisse First Boston), acting through its Cayman Islands Branch as collateral agent (in such capacity, the "*Collateral Agent*") for the Secured Parties. Terms used herein and not otherwise defined shall have the meanings assigned to such terms in the Existing Credit Agreement referred to below.

A. Reference is made to the Credit Agreement dated as of April 1, 2005 (as amended, supplemented or otherwise modified prior to the date hereof, the "*Existing Credit Agreement*"), among AMI Semiconductor, Inc. ("*AMI*"), AMIS Holdings, Inc. ("*Holdings*"), the lenders from time to time party thereto and Credit Suisse (formerly known as Credit Suisse First Boston), as Administrative Agent and Collateral Agent.

B. Reference is made to the Guarantee and Collateral Agreement dated as of April 1, 2005 (as amended, supplemented or otherwise modified from time to time prior to the date hereof, the "*Collateral Agreement*"), among AMI, Holdings, the Subsidiaries of AMI party thereto (together with AMI and Holdings, the "*Grantors*") and the Collateral Agent. Pursuant to the Collateral Agreement, the Grantors granted a security interest to the Collateral Agent, for the benefit of the Secured Parties, in, among other things, the patents and patent applications of the Grantors set forth on Schedule I hereto (the "*Patents*"), which security interest was recorded with the United States Patent & Trademark Office at Reel/Frame 16290/206.

C. In connection with the termination of the Existing Credit Agreement, the Grantors have informed the Collateral Agent of their desire to obtain the release of all right, title and interest of the Secured Parties in and to the Patents.

Accordingly, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Collateral Agent hereby absolutely, unconditionally and irrevocably releases all right, title and interest (including, without limitation, security interests) of the Collateral Agent and the Secured Parties in and to the Patents. The Collateral Agent hereby assigns, transfers and sets over to Grantors all right, title and interest that the Collateral Agent and the Secured Parties may have in or to the Patents under the Collateral Agreement.

The Collateral Agent agrees to perform all further acts and execute and deliver, at AMI's expense, all further documents and/or instruments that may be reasonably necessary to carry out the provisions of this U.S. Patents Release. To the extent that any other filings with any other governmental authority have been made with respect to any of the Patents, the Collateral Agent will execute and deliver, at AMI's expense, a reasonable release or other instrument that will terminate any such filing and/or release any interests conveyed therein. Any execution and delivery of documents or instruments, including this U.S. Patents Release, shall be without recourse to or warranty by the Collateral Agent.

This U.S. Patents Release shall be governed by and construed in accordance with the law of the State of New York and shall be binding upon the Collateral Agent's representatives, successors, assigns and transferees.

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IN WITNESS WHEREOF, the Collateral Agent has duly executed this U.S. Patents Release as of the day and year first above written.

CREDIT SUISSE, CAYMAN ISLANDS BRANCH, as Collateral Agent,

by _____

Name: ROBERT HETU
Title: MANAGING DIRECTOR

by _____

Name: JAMES NEIRA
Title: ASSOCIATE

Schedule I

See attached

AMIS Active US Patents

Title	Class	Issued Date	Patent Number	Notes
Combinational Logic Structure Using Pass Transistors	Utility	11-Nov-86	4622548	
GaAs Differential Line Receiver with Positive Feedback	Utility	13-Jan-87	4636654	2
Apparatus and Method of Solder Coating Integrated Circuit Leads	Utility	4-Apr-87	4657172	
Quad-State Control Signal Input Circuit	Utility	14-Jul-87	4680485	2
Method of Making Tri-Well CMOS By Self-Aligned Process	Utility	6-Oct-87	4697332	2
Small Propagation Delay Measurement for Digital Logic	Utility	8-Dec-87	4712061	2
Temperature Compensated Complementary Metal-Insulator-Semiconductor-Oscillator	Utility	22-Dec-87	4714901	2
State Sequence Dependent Read Only Memory	Utility	29-Dec-87	4716566	
Uniform Intensity Led Driver Circuit	Utility	5-Jan-88	4717868	
Apparatus and Method of Solder Coating Integrated Circuit Leads	Utility	19-Jan-88	4720034	
Programmable Reference Voltage Generator for a Read Only Memory	Utility	28-Jun-88	4754167	
Metal Foil Semiconductor Interconnection and Method	Utility	12-Jul-88	4756080	
Thin Oxide Fuse	Utility	12-Jul-88	4757359	
CMOS Programmable Logic Array Using Nor Gates for Clocking	Utility	16-Aug-88	4764691	
ECL to GaaS Logic Conversion Circuit with Positive Feedback	Utility	6-Dec-88	4789788	2
Temperature Compensating Driver for a Liquid Crystal Display	Utility	28-Feb-89	4807972	
Integrated Circuit Filter with Reduced Die Area	Utility	1-Aug-89	4853759	
Cascade FET Logic Circuits	Utility	31-Oct-89	4877976	2
Asynchronous Digital Arbiter	Utility	16-Jan-90	4894565	
Thin-Film Electrical Connections for Integrated Circuits	Utility	26-Feb-91	4996584	2
Circuit for measuring the level of an electrical signal and including offset correction means, and application thereof to amplifiers having automatic gain control	Utility	21-Jul-92	5132609	1
Highly Stable High-Voltage Buffer Using CMOS Technology	Utility	8-Dec-92	5170078	2
CMOS Self-Adjusting Bias Generator for High Voltage Drivers	Utility	12-Jan-93	5179297	2
Frequency Converter Utilizing a Feedback Control Loop	Utility	28-May-96	5521556	
Digitally-Tuned Oscillator Including a Self-Calibrating RC Oscillator Circuit	Utility	3-Sep-96	5552748	
Low Power RC Oscillator Using a Low Voltage Bias Circuit	Utility	17-Dec-96	5585765	
Circuit for Detecting The Absence of An External Component	Utility	31-Dec-96	5589802	
Self-Calibrating RC Oscillator	Utility	14-Jan-97	5594388	
Timing Circuit with Rapid Initialization On Power-Up	Utility	1-Apr-97	5617062	
Circuit for Externally Overdriving An Internal Clock	Utility	10-Jun-97	5638029	
Linear Tunable Gm-C Integrator	Utility	26-Aug-97	5661432	1

Multiple Stage Tracking Filter Using a Self-Calibrating RC Oscillator Circuit	Utility	2-Sep-97	5663675	
Self Adjusting Sense Amplifier Clock Delay Circuit	Utility	28-Oct-97	5882353	5
Manufacturing Method for ROM Array with Minimal Band-to-Band Tunneling	Utility	4-Nov-97	5883925	5
Operating Method for ROM Array Which Minimizes Band-to-Band Tunneling	Utility	17-Nov-98	5838048	5
3v/5v Input Buffer	Utility	17-Nov-98	5838168	
Strobed Wordline Driver for Fast Memories	Utility	13-Jul-99	5923609	
Apparatus for and method of programming a digital hearing aid	Utility	5-Sep-00	6115478	6
One Bit Digital Phase Shift Keyed Carrier Recovery and Demodulator Circuit	Utility	19-Dec-00	6163208	7
Reference Voltage Generator with Monitoring and Start Up Means	Utility	20-Mar-01	6204653	1
Filterbank structure and method for filtering and separating an information signal into different bands, particularly for audio signal in hearing aids	Utility	22-May-01	6236731	6
Apparatus for and method of filtering in an digital hearing aid, including an application specific integrated circuit and a programmable digital signal processor		29-May-01	6240192	6
Differential-Mode Charge Transfer Amplifier		19-Jun-01	6249181	
Method for Detecting and Characterizing Plasma-Etch Induced Damage in An Integrated Circuit		24-Jul-01	6265729	
Electrical Diagnostic Technique for Silicon Plasma Etch Induced Damage Characterization		7-Aug-01	6271539	
Spread-Spectrum Modulation Method and Circuit for Clock Generator Phase-Locked Loop		25-Sep-01	6294936	
Regulator for Sine Wave Generator and Sine Wave Generator Unit Including Such a Regulator		15-Jan-02	6339317	1
Circuits and Methods for Providing a Bandgap Voltage Reference Using Composite Resistors in Series		29-Jan-02	6342781	
Method and apparatus for feedback reduction in acoustic systems, particularly in hearing aids		12-Feb-02	6347148	6
Circuits and Methods for Providing a Current Reference with a Controlled Temperature Coefficient Using a Series Composite Resistor		26-Feb-02	6351111	
Systems and Methods for Enhancing Charge Transfer Amplifier Gain		12-Mar-02	6356148	
Transducer Interface Arrangement Including a Sigma-Delta Modulator with Offset Correction and with Gain Setting		4-Jun-02	6400295	1
Delay Lock Loop with Wide Frequency Range Capability		20-Aug-02	6437616	
Circuits and Methods for Initializing Memory Cells		11-Feb-03	6519177	
Integrated Sine Wave Generating Circuit		25-Feb-03	6525592	3
Method for Tungsten Chemical Vapor Deposition On a Semiconductor Substrate		8-Apr-03	6544889	3
Voltage Generating Circuit		29-Apr-03	6556069	1
Reference-Free Charge Transfer Amplifier		20-May-03	6566943	
Analog to Digital Converters Based On Transconveyance Amplifiers		12-Aug-03	6606049	
Filterbank Structure and Method for Filtering and Separating an Information Signal Into Different Bands, Particularly for Audio Signals in Hearing Aids		12-Aug-03	6606391	6
Multi Stage Circuits for Providing a Bandgap Voltage Reference Less Dependent On or Independent of a Resistor Ratio		2-Sep-03	6614209	
Method for Processing Conductive Layer Structures and Devices Including Such Conductive Layer Structures		30-Sep-03	6628358	1
Bandgap Voltage Reference Using Differential Pairs to Perform Temperature Curvature Compensation		4-Nov-03	6642699	
Electromagnetic Compatible Regulator		30-Dec-03	6670842	1
Runtime Programmable Reed Solomon Decoder		9-Mar-04	6704901	

Layout Configurable Electrostatic Discharge Device for Integrated Circuits		16-Mar-04	6707110	4
Low Voltage Enhanced Output Impedance Current Mirror		16-Mar-04	6707286	
Structures and Methods for Direct Conversion From Radio Frequency Modulated Signals to Baseband Signals		23-Mar-04	6711397	
Charge pump device with reduced ripple and spurious low frequency electromagnetic signals		6-Apr-04	6717829	1
Absolute Value Amplitude Baseband Detector		1-Jun-04	6744309	
Differential NOR Memory Cell Having Two Floating Gate Transistors		29-Jul-04	6766325	
Stable Floating Gate Voltage Reference Using Interconnected Current-to-Voltage and Voltage-to-Current Converters		27-Jul-04	6768371	
Use of Irregularly Shaped Conductive Filler Features to Improve Planarization of the Conductive Layer While Reducing Parasitic Capacitance Introduced by the Filler Features		21-Sep-04	6794691	
Static Random Access Memory (SRAM) Without Precharge Circuitry		9-Nov-04	6816401	
Switched Capacitor Voltage Reference Circuits Using Transconductance Circuit to Generate Reference Voltage		16-Nov-04	6819163	
Stimulated Quick Start Oscillator		16-Nov-04	6819195	
Symmetric and Complementary Differential Amplifier		23-Nov-04	6822513	
Method for Making Interconnect Structures		28-Dec-04	6835644	1
Dual Differential-Input Amplifier Having Wide Input Range		18-Jan-05	6844781	
Double-sided extended drain field effect transistor, and integrated overvoltage and reverse voltage protection	Utility	15-Mar-05	6867640	
Distributed Memory and Logic Circuits	Utility	22-Mar-05	6870393	

NOTES:
1) Original assignment to Alcatel --Now assigned to AMI Semiconductor Belgium BVBA

2) Original assignment to Gould--Now assigned to AMI Semiconductor, Inc
3) Assigned to AMI Semiconductor Belgium BVBA
4) Original assignment to Alcatel --Now assigned to AMI Semiconductor Belgium BVBA, Joint Patent with IMEC
5) Joint patent with Waferscale Integration Inc.
6) Original assignment to Dspfactory, Ltd.--Now assigned to AMI Semiconductor, Inc.
7) Original assignment to GA-Tek --Now assigned to AMI Semiconductor, Inc.

US Patent Applications

Title	Class	Filing Date	Application Number	Notes
Method and apparatus for noise reduction, particularly in hearing aids	Utility	16-Apr-98	09/060,825	4
Listening Device	Utility	23-Oct-01	10/023,109	4
Software Implemented Loudness Normalization for a Digital Hearing Aid	Utility	7-Nov-01	09/985,976	4
Directional Audio Signal Processing Using an Oversampled Filterbank	Utility	7-Aug-02	10/214,350	4
Sub-band Adaptive Signal Processing in an Oversampled Filterbank	Utility	7-Aug-02	10/214,057	4
Sound Intelligibility Enhancement Using a Psychoacoustic Model and an Oversampled Filterbank	Utility	7-Aug-02	10/214,056	4
Integrated Overvoltage and Reverse Voltage Protection Circuit	Utility	13-Sep-02	10/243,749	
Method and System for Real Time Audio Synthesis	Utility	22-Oct-02	10/277,598	4
Method and System for Real Time Speech Recognition	Utility	22-Oct-02	10/277,454	4
Multiplex Transmission System with In-Circuit Addressing	Utility	13-Dec-02	10/319,347	2
Method and Device Generating Integrated Circuit Test Programs	Utility	24-Jan-03	10/351,204	1
Multiplex Bus System with Duty Cycle Correction	Utility	7-Feb-03	10/360,436	2
Methods for Sidewall Protection of Metal Interconnect for Unlanded Vias Using Physical Vapor Deposition	Utility	24-Feb-03	10/373,911	
Protection Circuit Protecting Against Voltage Irregularities	Utility	9-Apr-03	10/410,420	2
Distributed Memory and Logic Circuits	Utility	24-Apr-03	10/422,137	
Up-Conversion of a Down-Converted Baseband Signal in a Direct Conversion Architecture Without the Baseband Signal Passing	Utility	29-Apr-03	10/426,225	
Direct Conversion Receiver for Amplitude Modulated Signals Using Linear/Log Filtering	Utility	29-Apr-03	10/426,383	
Direct Conversion Receiver with Direct Current Offset Correction Circuitry	Utility	6-May-03	10/430,656	
An Adaptive Diversity Receiver Architecture	Utility	6-May-03	10/430,455	
Multi-Fault Protected High Side Switch with Current Sense	Utility	20-May-03	10/442,630	
Method and Circuit for Bi-Directional Current Measurement	Utility	20-May-03	10/442,618	
Delay Lock Loop with Fixed Angle De-Skew, Quick Start and Low Jitter	Utility	24-Jun-03	10/602,195	
Double-sided extended drain field effect transistor, and integrated overvoltage and reverse voltage protection circuit that uses the same	Utility	1-Jul-03	10/611,714	
Digitally Controlled Impedance Driver Matching for Wide Voltage Swings at Input/Output Node and Having Programmable Step Size	Utility	8-Aug-03	10/637,840	

Method and System for Processing Subband Signals Using Adaptive Filters	Utility	18-Aug-03	10/642847	4
Voltage Reference Circuit Using Schottky Diodes For Low Voltage Applications	Utility	2-Feb-04	10/770,223	
Device and method for detecting rotor speed of a multiple phase motor with bipolar device	Utility	22-Mar-04	10/805,972	2
Method and System for Acoustic Shock Protection	Utility	31-Mar-04	10/815,891	4
Pade' Approximant Based Compensation for Integrated Sensor Modules and the Like	Utility	13-May-04	10/845,681	
Structured ASIC Device with Configurable Die Size and Selectable Embedded Functions	Utility	4-Jun-04	10/860,694	
Reactive Sensor Modules Using Pade Approximant Based Compensation and Providing Module-Sourced Excitation	Utility	16-Jun-04	10/870,314	5
Methods For Manufacturing a Soft Error and Defect Resistant Pre-Metal Dielectric Layer	Utility	25-Jun-04	10/877,482	
Die Identification Systems and Methods	Utility	30-Jun-04	10/883,316	3
Low-Voltage Differential Signal (LVDS) Transmitter with High Signal Integrity	Utility	17-Aug-04	10/920,009	
High Voltage, Low-Offset Operational Amplifier with Rail-to-Rail Input Range in a Standard Digital CMOS Process	Utility	17-Aug-04	10/919,757	
Ambient Light Compensation Circuit for Photodiode Receiver Applications	Utility	24-Aug-04	10/925,263	
Dynamic Phase Alignment of a Clock and Data Signal Using an Adjustable Clock Delay	Utility	3-Sep-04	10/933,742	
Control of Current in an Inductance with Pulse Width Modulation at Controlled Frequency	Utility	19-Oct-04	10/899,653	2
Antenna Integrated with Retrieval Component in a Hearing Aid	Utility	28-Oct-04	10/975,914	
Radio Frequency Envelope Detector	Utility	29-Oct-04	10/977,295	
Double-sided extended drain field effect transistor	Utility	13-Dec-04	11/010,892	

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1) Original assignment to Alcatel --Now assigned to AMI Semiconductor Belgium BVBA
2) Assigned to AMI Semiconductor Belgium BVBA
3) Joint application of AMI Semiconductor, Inc. and Indigo Systems, Inc.
4) Original assignment to Dspfactory, Ltd.--Now assigned to AMI Semiconductor, Inc.
5) Joint application with Matsushita Electric Works, Ltd.