

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT3774193

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST	
<b>CONVEYING PARTY DATA</b>		
<b>Name</b>		<b>Execution Date</b>
LATTICE SEMICONDUCTOR CORPORATION		11/12/2015
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	JEFFERIES FINANCE LLC	
<b>Street Address:</b>	520 MADISON AVENUE	
<b>City:</b>	NEW YORK	
<b>State/Country:</b>	NEW YORK	
<b>Postal Code:</b>	10022	
<b>PROPERTY NUMBERS Total: 19</b>		
<b>Property Type</b>	<b>Number</b>	
Application Number:	14868985	
Application Number:	14824717	
Application Number:	14812220	
Application Number:	14797206	
Application Number:	14793788	
Application Number:	62212686	
Application Number:	62220744	
Application Number:	62218623	
Application Number:	62217383	
Application Number:	62194733	
Application Number:	62208041	
Application Number:	62209999	
Application Number:	62217180	
Application Number:	62235599	
Application Number:	62198326	
Application Number:	62218674	
Application Number:	62206191	
Application Number:	62189929	
Application Number:	62209371	

**CORRESPONDENCE DATA****Fax Number:** (212)969-2900

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

**Phone:** 2129693000**Email:** trademark@proskauer.com, ypan@proskauer.com**Correspondent Name:** ADAM D. SIEGARTEL**Address Line 1:** PROSKAUER ROSE LLP**Address Line 2:** ELEVEN TIMES SQUARE**Address Line 4:** NEW YORK, NEW YORK 10036-8299

<b>ATTORNEY DOCKET NUMBER:</b>	40767-132
<b>NAME OF SUBMITTER:</b>	MICHAEL GALLAGHER
<b>SIGNATURE:</b>	/Michael Gallagher/
<b>DATE SIGNED:</b>	03/08/2016

**Total Attachments: 4**

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GRANT OF SECURITY INTEREST  
IN UNITED STATES PATENTS

November 12, 2015

FOR GOOD AND VALUABLE CONSIDERATION, receipt and sufficiency of which are hereby acknowledged, **Lattice Semiconductor Corporation** ("Grantor") with principal offices at 111 SW Fifth Avenue, Suite 700, Portland, Oregon 97204, hereby grants to **Jefferies Finance LLC**, as Collateral Agent, with principal offices at 520 Madison Avenue, New York, New York 10022 (the "Grantee"), for the ratable benefit of the Secured Creditors, a security interest in (i) all of the Grantor's rights, title and interest in, to and under the United States Patents set forth on Schedule A attached hereto, in each case together with (ii) all Proceeds in respect thereof.

This Grant is made to secure the satisfactory performance and payment of all the Secured Obligations of the Grantor pursuant to that certain Guaranty and Collateral Agreement among the Grantor, the other grantors from time to time party thereto and the Grantee, dated as of March 10, 2015 (as amended, restated, amended and restated, modified and/or supplemented from time to time, the "Guaranty and Collateral Agreement"). Capitalized terms used but not defined herein have the definitions specified in the Guaranty and Collateral Agreement.


This Grant has been granted in conjunction with the security interest granted to the Grantee under the Guaranty and Collateral Agreement. The rights and remedies of the Grantee with respect to the security interest granted herein are as set forth in the Guaranty and Collateral Agreement, all terms and provisions of which are incorporated herein by reference. In the event that any provisions of this Grant are deemed to conflict with the Guaranty and Collateral Agreement, the provisions of the Guaranty and Collateral Agreement shall govern.

This Grant shall be construed in accordance with and governed by the laws of the State of New York.


[Signature Pages Follow]

IN WITNESS WHEREOF, the undersigned have executed this Grant as of the day and year first above written.

**LATTICE SEMICONDUCTOR CORPORATION, Grantor**

By:   
Name: Byron Milstead  
Title: Corporate VP & General Counsel

**JEFFERIES FINANCE LLC, as Collateral Agent  
and Grantee**

By:   
Name: J. Paul McDonnell  
Title: Managing Director

[Signature Page to Form of Grant of Security Interest in United States Patents]

**PATENT**  
**REEL: 037925 FRAME: 0026**

**SCHEDULE A**

App. No.	Patent No.	Jurisdiction	Title	Status	Record Owner
14/868,985		US	Dynamic Control of Pixel Color Formats Used to Encode Color Video Streams	Filed	Lattice Semiconductor Corporation
14/824,717		US	Reversible receptacle connector	Filed	Lattice Semiconductor Corporation
14/812,220		US	Elements to counter transmitter circuit performance limitations	Filed	Lattice Semiconductor Corporation
14/797,206		US	High-resolution oscillator having wide frequency range	Filed	Lattice Semiconductor Corporation
14/793,788		US	Crowbar current elimination	Filed	Lattice Semiconductor Corporation
62/212,686		US	Multi-time programmable non-volatile memory cell	Filed	Lattice Semiconductor Corporation
62/220,744		US	System and Method of Low Power Voice Recognition	Filed	Lattice Semiconductor Corporation
62/218,623		US	CMOS Buffer with Input Hysteresis Using Back-Bias	Filed	Lattice Semiconductor Corporation
62/217,383		US	Synthesis Systems and Methods and Circuitry Designs Resulting Therefrom	Filed	Lattice Semiconductor Corporation
62/194,733		US	Low-Speed Bus Time Stamp Methods Circuitry and Systems	Filed	Lattice Semiconductor Corporation
62/208,041		US	Low-Speed Bus Time Stamp Methods Circuitry and Systems	Filed	Lattice Semiconductor Corporation
62/209,999		US	Low-Speed Bus Time Stamp Methods Circuitry and Systems	Filed	Lattice Semiconductor Corporation
62/217,180		US	Low-Speed Bus Time Stamp Methods Circuitry and Systems	Filed	Lattice Semiconductor Corporation
62/235,599		US	Compressed video playback with raw data assist	Filed	Lattice Semiconductor Corporation
62/198,326		US	Wireless Control of Drone with Distance Ranging and Channel Sensing	Filed	Lattice Semiconductor Corporation
62/218,674		US	Slice and Tile based Transmission Priority for Compressed Video over Wireless Channels	Filed	Lattice Semiconductor Corporation

**PATENT**

**REEL: 037925 FRAME: 0027**

62/206,191		US	Error Correction for Compressed Audio Stream	Filed	Lattice Semiconductor Corporation
62/189,929		US	Systems And Methods Of Inteference Mitigation For Concurrent Links In Beamformed Communication	Filed	Lattice Semiconductor Corporation
62/209,371		US	One Time Programmable Cell Having Low Read Voltage	Filed	Lattice Semiconductor Corporation