PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT3787841

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
QUICKSILVER TECHNOLOGY, INC.	10/13/2005

RECEIVING PARTY DATA

Name:	TECHFARM VENTURES MANAGEMENT, LLC
Street Address:	2275 EAST BAYSHORE ROAD
City:	PALO ALTO
State/Country:	CALIFORNIA
Postal Code:	94303

PROPERTY NUMBERS Total: 2

Property Type	Number
Application Number:	14082691
Application Number:	14714993

CORRESPONDENCE DATA

Fax Number: (312)977-4405

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: ldstewart@nixonpeabody.com
Correspondent Name: WAYNE L. TANG
Address Line 1: 70 WEST MADISON STREET. SUITE 3500
Address Line 2: NIXON PEABODY LLP
Address Line 4: CHICAGO, ILLINOIS 60602

ATTORNEY DOCKET NUMBER:	066664-007USC2-USC3
NAME OF SUBMITTER:	WAYNE L. TANG
SIGNATURE:	/Wayne L. Tang--Reg. No. 36028/
DATE SIGNED:	03/17/2016

Total Attachments: 16
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CERTIFICATE OF TURNOVER OF COLLATERAL IN FULL SATISFACTION OF OBLIGATIONS

[CALIFORNIA COMMERCIAL CODE § 9620]

On or about June 1, 2004, Quicksilver Technology, Inc. ("Quicksilver" or "Debtor") issued secured promissory notes in the aggregate principal amount of $1,525,000.00 to TechFarm Ventures (Q), L.P., TechFarm Ventures, L.P., Sigma Partners 6, L.P., Sigma Associates 6, L.P., Sigma Investors 6, L.P., Selby Venture Partners II, L.P., Emerging Alliance Fund, L.P., and Portview Communications Partners, L.P. (collectively, the "Lenders"), pursuant to certain loan documents including Secured Convertible Promissory Notes, a Note Purchase Agreement and a Security Agreement (collectively, the "June Loan Documents").

On or about August 2, 2004 and again on or about November 2, 2004, Debtor issued additional secured promissory notes in subsequent loan closings to certain of the Lenders, in the principal amounts of $508,000.00 and $125,000.00 respectively. The principal amount of the notes issued to each of the Lenders is described in Schedule I of the Note Purchase Agreement and in each of the related Secured Convertible Promissory Notes. These documents, together with the June Loan Documents, are hereinafter collectively referred to as the "Loan Documents").

On or about June 1, 2004, Quicksilver together with the Lenders entered into the aforementioned Security Agreement whereby collateral agent TechFarm Ventures Management, LLC ("Collateral Agent"), on behalf of the Lenders, was granted a security interest in certain property of Quicksilver (the "Subject Assets"). A description of the Subject Assets is attached hereto as *Exhibit A*.

In accordance with the Loan Documents, the amount of Quicksilver's obligations to Lenders includes principal in the amount of $2,133,000.00. In addition, unpaid interest in an amount of not less than $165,570.00 has accrued on the outstanding principal balance under the Secured Convertible Promissory Notes (collectively, the "Notes"). In sum, the aggregate of all outstanding principal under the Notes together with accrued and unpaid interest, fees and costs thereon is in an amount of not less than $2,298,570.00. This amount, together with any and all other amounts that may be due and owing to Lenders pursuant to the Loan Documents shall hereinafter be collectively referred to the "Quicksilver Obligation."

Debtor is in default under each of the Notes for, among other reasons, its failure to make payments when due.

To satisfy the Quicksilver Obligation, Quicksilver has agreed to transfer any and all of its interest in the Secured Assets to the Collateral Agent, except for certain Cash-On-Hand as defined below, and the Lenders, in turn, have agreed to accept the Secured Assets (excepting the Cash-On-Hand being retained by Quicksilver) by and through the Collateral Agent as full satisfaction of the Quicksilver Obligation.

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NOW THEREFORE, THE PARTIES AGREE AS FOLLOWS:

Pursuant to § 9620 of the California Commercial Code, and in full satisfaction of the Quicksilver Obligation, Quicksilver does hereby transfer, assign, license and deliver (collectively, "transfer") to TechFarm Ventures Management, LLC, as Collateral Agent under the Security Agreement, all of the rights, title and interest of Debtor to and in all of the Subject Assets of Debtor except as set forth below, which Subject Assets are described in *Exhibit A* attached hereto, which is incorporated herein by reference. Notwithstanding the foregoing, Quicksilver shall not transfer, and may retain possession of, pay retainers with, or use in its reasonable judgment for purposes of winding up the company's affairs, certain of the funds it holds in bank accounts in the amount of not more than $40,000.00 (the "Cash-On-Hand"). The parties acknowledge the Quicksilver Obligation is in an amount of not less than TWO MILLION TWO HUNDRED NINETY-EIGHT THOUSAND FIVE HUNDRED SEVENTY DOLLARS ($2,298,570.00).

The term "transfer" shall herein be deemed to include the assignment, licensing and delivery of Subject Assets, which shall further include but not be limited to the licensing of the Collateral Agent to use Subject Assets wherever required.

Any portion of the Cash-On-Hand held by Quicksilver at such time that a wind down and dissolution of the company has been completed, or remaining from retainers paid to counsel, shall be transferred (or caused to be transferred) to Collateral Agent at that time.

AS BETWEEN DEBTOR AND COLLATERAL AGENT, THE SUBJECT ASSETS ARE TRANSFERRED AND LICENSED "AS IS," "WHERE IS," AND "IF IN," WITH ALL FAULTS. EXCEPT AS EXPRESSLY HEREIN PROVIDED, DEBTOR SPECIFICALLY DISCLAIMS ALL WARRANTIES, WRITTEN OR ORAL, STATUTORY, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, NONINFRINGEMENT, POSSESSION, QUIET ENJOYMENT OR TITLE, OR ANY WARRANTIES ARISING FROM A COURSE OF DEALING, TRADE USAGE OR TRADE PRACTICE.

Debtor acknowledges that it has not relied upon any representations of Collateral Agent or Lenders, except as may specifically be provided herein, in entering into this transfer of Subject Assets. Collateral Agent and Lenders agree that they are accepting the Subject Assets in full satisfaction of the Quicksilver Obligation.

As between Debtor and Collateral Agent, in addition to any other consideration, Collateral Agent shall be liable for costs associated with the transfer of the Subject Assets, including all sales or use taxes, to the extent any may exist or be assessed, and shall hold Debtor harmless from costs relating thereto, if any.

Quicksilver agrees to cooperate with Collateral Agent in completing any documents that, in Collateral Agent's sole opinion, are necessary to give effect to this agreement.

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All aspects of this agreement, including construction, validity and performance of this agreement, shall be governed by, and construed and enforced in accordance with, the laws of the State of California. The parties agree that the state or federal courts in the County of Santa Clara, State of California shall have exclusive jurisdiction with respect to any dispute arising under this agreement.

This agreement may be executed in any number of counterparts, each of which shall be an original, but all of which together shall be deemed to constitute one instrument.

This agreement is effective as October 13, 2005.

QUICKSILVER TECHNOLOGY, INC.

By: _____
Name: Bryan Wang
Its: CFO

TECHFARM VENTURES MANAGEMENT, LLC
As Collateral Agent

By: _____
Name: Gordon Campbell
Title: Senior Managing Member

LENDERS:

TECHFARM VENTURES, L.P.
By: TechFarm Ventures Management, LLC
Its: General Partner

By: _____
Name: Gordon Campbell
Title: Senior Managing Member

[Signatures are continued on the next page.]

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REEL: 038011 FRAME: 0272

TECHFARM VENTURES (Q), L.P.
By: TechFarm Ventures Management, LLC
Its: General Partner

By:_____
Name: Gordon Campbell
Title: Senior Managing Member

SIGMA ASSOCIATES 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA INVESTORS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA PARTNERS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

By:_____
Name:_____
Title:_____

SELBY VENTURE PARTNERS II, L.P.

By:_____
Name: Robert C. Marshall
Title: Managing Partner

[Signatures are continued on the next page.]

PATENT
REEL: 038011 FRAME: 0273

TECHFARM VENTURES (Q), L.P.
By: TechFarm Ventures Management, LLC
Its: General Partner

By:_____
Name: Gordon Campbell
Title: Senior Managing Member

SIGMA ASSOCIATES 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA INVESTORS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA PARTNERS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

By: _____
Name: LAURENCE G. FINE
Title: Mang. Director

SELBY VENTURE PARTNERS II, L.P.

By:_____
Name: Robert C. Marshall
Title: Managing Partner

[Signatures are continued on the next page.]

PATENT
REEL: 038011 FRAME: 0274

TECHFARM VENTURES (Q), L.P.
By: TechFarm Ventures Management, LLC
Its: General Partner

By:_____
Name: Gordon Campbell
Title: Senior Managing Member

SIGMA ASSOCIATES 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA INVESTORS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

SIGMA PARTNERS 6, L.P.
By its General Partner:
SIGMA MANAGEMENT 6, L.L.C.

By:_____
Name:_____
Title:_____

SELBY VENTURE PARTNERS II, L.P.

By:_____
Name: Robert C. Marshall
Title: Managing Partner

[Signatures are continued on the next page.]

PATENT
REEL: 038011 FRAME: 0275

EMERGING ALLIANCE FUND, L.P.

By: VENTURETECH ALLIANCE, L.L.C.
Its General Partner

By: _____
Name: Ronald C. Norris
Title: Managing Member

PORTVIEW COMMUNICATIONS PARTNERS, L.P.

By: _____
Name: Shawna Morehouse and Scott Somerville
Title: Authorized Signatories
For and on Behalf of CIBC Bank and Trust Company (Cayman) Limited in its Capacity as Administrator of Portview Communications Management LLC, the General Partner of Portview Communications Partners LP

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EMERGING ALLIANCE FUND, L.P.

By: VENTURETECH ALLIANCE, L.L.C.
Its General Partner

By:_____
Name: Ronald C. Norris
Title: Managing Member

PORTVIEW COMMUNICATIONS PARTNERS, L.P.

By:_____
Name: ~~Shawna Marchouse and Scott Somerville~~ Ronan Guilfoyle
Title: Authorized Signatories
~~For and on Behalf of CIBC Bank and Trust Company (Cayman) Limited in~~ its Capacity as Administrator of Portview Communications Management LLC, the General Partner of Portview Communications Partners LP
Admiral Administration Ltd.

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EXHIBIT A TO CERTIFICATE OF TURNOVER OF COLLATERAL IN FULL SATISFACTION OF OBLIGATIONS

The Subject Assets are as follows:

All right, title, interest, claims and demands of Company in and to the following property:

(i) All goods and equipment now owned or hereafter acquired, including, without limitation, all laboratory equipment, computer equipment, office equipment, machinery, fixtures, vehicles, and any interest in any of the foregoing, and all attachments, accessories, accessions, replacements, substitutions, additions, and improvements to any of the foregoing, wherever located;

(ii) All inventory now owned or hereafter acquired, including, without limitation, all merchandise, raw materials, parts, supplies, packing and shipping materials, work in process and finished products including such inventory as is temporarily out of Company's custody or possession or in transit and including any returns upon any accounts or other proceeds, including insurance proceeds, resulting from the sale or disposition of any of the foregoing and any documents of title representing any of the above, and Company's books relating to any of the foregoing;

(iii) All contract rights, general intangibles, health care insurance receivables, payment intangibles and commercial tort claims, now owned or hereafter acquired, including, without limitation, all patents, patent rights (and applications and registrations therefor), trademarks and service marks (and applications and registrations therefor), inventions, copyrights, mask works (and applications and registrations therefor), trade names, trade styles, software and computer programs, trade secrets, methods, processes, know how, drawings, specifications, descriptions, and all memoranda, notes, and records with respect to any research and development, goodwill, license agreements, franchise agreements, blueprints, drawings, purchase orders, customer lists, route lists, infringements, claims, computer programs, computer disks, computer tapes, literature, reports, catalogs, design rights, income tax refunds, payments of insurance and rights to payment of any kind and whether in tangible or intangible form or contained on magnetic media readable by machine together with all such magnetic media;

(iv) All now existing and hereafter arising accounts, contract rights, royalties, license rights and all other forms of obligations owing to Company arising out of the sale or lease of goods, the licensing of technology or the rendering of services by Company (subject, in each case, to the contractual rights of third parties to require funds received by Company to be expended in a particular manner), whether or not earned by performance, and any and all credit insurance, guaranties, and other security therefor, as well as all merchandise returned to or reclaimed by Company and Company's books relating to any of the foregoing;

(v) All documents, cash, deposit accounts, letters of credit, letter of credit rights, supporting obligations, certificates of deposit, instruments, chattel paper, electronic chattel paper, tangible chattel paper and investment property, including, without limitation, all securities, whether certificated or uncertificated, security entitlements, securities accounts, commodity contracts and commodity accounts, and all financial assets held in any securities account or otherwise, wherever located, now owned or hereafter acquired and Company's books relating to the foregoing; and

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(vi) Any and all claims, rights and interests in any of the above and all substitutions for, additions and accessions to and proceeds thereof, including, without limitation, insurance, condemnation, requisition or similar payments and the proceeds thereof.

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SCHEDULE A
TO SECURITY AGREEMENT

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None

PATENTS

Title	Date Issued	Patent No.
Method and System for Reconfigurable Channel Coding	May 10, 2003	6577678
Adaptive and Reconfigurable Integrated Circuitry for Multimode Rake Reception For Dynamic Search and Multipath Resolution	Sept 9, 2003	6618434
High Performance Memory Efficient Variable-Length Coding Decoder	July 1, 2003	6587057
Method, System And Language Structure For Programming Reconfigurable Hardware	May 4, 2004	6732354

PATENT APPLICATIONS

See attached list
"Patent Applications Filed"

TRADEMARKS

None

TRADEMARK APPLICATIONS

None

MASK WORKS

None

[LICENSES OF PATENTS, TRADEMARKS, COPYRIGHTS OR MASK WORKS]
(other than non-exclusive licenses to end-users)

See attached list

"Patent Licenses (exclusive)"

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PATENT
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QuickSilver Technology, Inc.

Patent License Agreements

7/29/2005

Company Name	Type of Agreement	Patent Number	Patent Title	Comments/Status
Giga Operations, Inc.	Exclusive	5,857,109	Programmable logic device for real time video processing.	Patents assigned to and owned by QuickSilver
	Exclusive	5,652,875	Implementation of a selected instruction set CPU in programmable hardware.	"
	Exclusive	5,603,043	System for compiling algorithmic language source code for implementation in programmable hardware.	"
Virginia Tech	Exclusive	5,828,858	Worm-hole Run-time Reconfigurable Processor Field Programmable Gate Array (FPGA)	Signed agreement in place.

APPLICATIONS FILED WITH USPTO

		Filing Date	Serial Number
QST-001 US	ADAPTIVE INTEGRATED CIRCUITRY WITH HETEROGENEOUS AND RECONFIGURABLE MATRICES OF DIVERSE AND ADAPTIVE COMPUTATIONAL UNITS HAVING FIXED, APPLICATION SPECIFIC COMPUTATIONAL ELEMENTS	22-Mar-01	09/815,122
QST-014 US	Method and System for Scheduling In An Adaptable Computing Engine	31-May-01	09/872,397
QST-015 US	Method and System For An Interconnection Network To Support Communications Among A Plurality Of Heterogeneous Processing Elements	03-Jul-01	09/898,350
QST-012 US	METHOD AND SYSTEM FOR DIGITAL SIGNAL PROCESSING IN AN ADAPTIVE COMPUTING ENGINE	25-Jul-01	09/916,141
QST-013 US	Method and System For Encoding Instructions For A VLIW That Reduces Instruction Memory Requirements	25-Jul-01	09/916,142
QST-006 US	Predictive Resource Allocation	09-Oct-01	09/974,521
QST-030 US	Method and System for Minimizing Power Consumption in Embedded Systems With Clock Enable Control	28-Nov-01	09/996,094
QST-027 US	Leasing or Renting of Silicon Chips to Product Design Engineers and OEMs	28-Nov-01	09/998,006
QST-002 US	'System and Method For Configuration Of Adaptive Integrated Circuitry Having Fixed, Application Specific Computational Elements'	30-Nov-01	09/997,530
QST-003 US	"Apparatus, Method and System For Configuration and Operation Of Adaptive Integrated Circuitry Having Fixed, Application Specific Computational Elements"	30-Nov-01	09/997,987
QST-018 US	Combining Silicon (Hardware) with Software (Application) by System Level Sales	05-Dec-01	10/010,018
QST-022 US	METHOD AND SYSTEM FOR increasing availability and proximity of base stations for cellular communications via mobile base stations	06-Dec-01	10/010,596
QST-033 US	Water Marking of Hardware Bit Files	06-Dec-01	10/020,149
QST-020 US	System For Adapting Device Standards After Manufacture	10-Dec-01	10/013,825
QST-045 US	Method and System For Managing HW Resources To Implement System Functions Using An Adaptive Computing Architecture	12-Dec-01	10/015,544
QST-046 US	Applying Unused Hardware Resources to Traditional Algorithms	12-Dec-01	10/015,530
QST-051 US	A method to detect 8 scrambling codes in a group using a single code generator in stage 3 of the 3GPP WCDMA downlink synchronization algorithm	12-Dec-01	10/015,537
QST-052 US	A low I/O bandwidth method to detect 8 scrambling codes in a group using a single code generator in stage 3 of the 3GPP WCDMA downlink synchronization algorithm.	12-Dec-01	10/015,531
QST-042 US	Computer Processor Architecture Selectively Using Finite-State-Machine for Control Code Execution	13-Dec-01	10/022,778
QST-050 US	A Universal Adaptive Hardware Unit	21-Dec-01	10/029,502
QST-007 US	APPARATUS, METHOD AND SYSTEM FOR GENERATING A UNIQUE HARDWARE CONFIGURATION INSEPARABLE FROM CORRESPONDINGLY UNIQUE CONTENT	27-Dec-01	10/034,053
QST-008 US	Appendix and Method For Adaptive Multimedia Reception and Transmission	04-Jan-02	10/040,100
QST-053 US	METHOD AND SYSTEM FOR IMPLEMENTING A SYSTEM ACQUISITION FUNCTION FOR USE WITH A COMMUNICATION DEVICE	04-Feb-02	10/067,495
QST-060 US	Method and System For Data Flow Control of Execution Nodes Of An ACE	05-Mar-02	10/092,559
QST-044 US	Hardware Implementation Of The Secure Hash Standard	06-Mar-02	10/093,158
QST-083 US	A scripting language for decoding and encoding typed structured data such as ASN.1.	29-Apr-02	10/136,055
QST-023 US	Caching Features for a telephone on a Network - adaptive downloading	29-Apr-02	10/135,906
QST-079 US	Visually Cued Integrated Circuit Package	06-May-02	10/140,235
QST-010 PR US	Method and System for Optimizing Operations of an ACE	13-May-02	60/378,068
QST-047 PR US	METHOD AND SYSTEM FOR ACHIEVING INDIVIDUALIZED PROTECTED SPACE IN AN OPERATING SYSTEM	13-May-02	60/380,877
QST-067 US	Method for ordering instructions for scheduling by a modulo scheduler for processors with a large number of function units and reconfigurable data paths	15-May-02	10/146,857
QST-068 US	Method for estimating cost when placing instructions within a modulo scheduler when scheduling for processors with a large number of function units or reconfigurable data paths.	31-May-02	10/160,666
QST-054 PR US	ACM Infrastructure Allows Fast Time-to-Market SOCs	12-Jun-02	60/388,249
QST-072 PR US	RAN - RECONFIGURABLE ARITHMETIC NODE	25-Jun-02	60/391,874
QST-084 US	Multitasking Model to Support both Hard and Soft Realtime tasks	03-Jul-02	10/189,791
QST-062 US	Block Floating Point Accumulator	09-Jul-02	10/192,391
QST-048 US	Network Distribution Model (combined with QST-085 US + A method for securely distributing and downloading new hardware configurations)	18-Jul-02	10/198,823

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APPLICATIONS FILED WITH USPTO

		FILING DATE	SERIAL NUMBER
QST-049 US	Revenue Distribution Model	18-Jul-02	10/199,900
QST-076 US	Reconfigurable Security Processor	26-Jul-02	10/205,824
QST-070 US	Watermarking Binary Computer Code	16-Aug-02	10/223,205
QST-071 US	Two Versions of a method for Watermarking Binary Computer Code are described	16-Aug-02	10/223,256
QST-059 US	Grammar to define the loading and prerequisite resource requirements for tasks running on a network of heterogeneous processors	29-Aug-02	10/233,175
QST-016-1P US	Method and system for an interconnection network to support communications among a plurality of heterogeneous processing elements	10-Sep-02	10/241,511
QST-035 PR US	PSN – Programmable Scalar Node (RISC)	30-Sep-02	60/415,320
QST-090 US	A Low-Complexity Scheme in Color Conversion and Down-Sampling of Image Codecs	02-Oct-02	10/263,941
QST-084 US	RETARGETABLE COMPILER FOR MULTIPLE AND DIFFERENT HARDWARE PLATFORMS	04-Oct-02	10/264,465
QST-066 US	Device Log of applications by type and frequency of usage	10-Oct-02	10/265,872
QST-083 US	An operating system service to record and collect computer/processor resource usage and utilization.	10-Oct-02	10/268,434
QST-041 PR US	Reconfigurable Bit-Manipulation Node	11-Oct-02	60/418,019
QST-087 PR US	RFN – Reconfigurable Filter Node	24-Oct-02	60/421,543
QST-017 US	METHOD AND SYSTEM FOR PROVIDING A DEVICE WHICH CAN BE ADAPTED ON AN ONGOING BASIS	25-Oct-02	10/280,496
QST-088 PR US	RXN- Run-time reconfigurable, multi-precision data-flow machine	26-Oct-02	60/422,063
QST-043 US	A Mechanism for Securely Storing ACE Code Off Chip	01-Nov-02	10/286,533
QST-088 PR US	Arithmetic Node Including General Digital Signal Processing Functions For An ACM	01-Nov-02	60/423,010
QST-005 US	APPARATUS, METHOD AND SYSTEM FOR DEVELOPING AND SCHEDULING ADAPTIVE RECONFIGURABLE HARDWARE AND SOFTWARE	07-Nov-02	10/289,039
QST-008 US	PROFILING OF SOFTWARE AND CIRCUIT DESIGNS UTILIZING DATA OPERATION ANALYSES FOR ADAPTIVE AND RECONFIGURABLE COMPUTING	07-Nov-02	10/289,640
QST-051-1P US (improvement)	A method to detect 8 scrambling codes in a group using a single code generator in stage 3 of the 3GPP WCDMA downlink synchronization algorithm	14-Nov-02	10/295,632
QST-051A-1P US (improvement)	A low I/O bandwidth method to detect 8 scrambling codes in a group using a single code generator in stage 3 of the 3GPP WCDMA downlink synchronization algorithm.	14-Nov-02	10/295,692
QST-096 PR US	XMC – NEW NODE	22-Nov-02	60/428,046
QST-100 PR US	IOC – new node	22-Nov-02	60/428,591
QST-091 US	Method and System For Providing An Excitation-Pattern Based Audio Coding Scheme	10-Jan-03	10/340,060
QST-069 US	RAPTOR PATENT/ M. Node/ correct instructions / flexible automatic memory connections / fixed fpga widgets	14-Jan-03	10/342,888
QST-077 US	Hardware Resource Management Support	31-Jan-03	10/356,671
QST-095 CV US	Arithmetic Node Including General Digital Signal Processing Functions For An ACM	13-Feb-03	10/367,188
QST-001-1P US	ADAPTIVE INTEGRATED CIRCUITRY WITH HETEROGENEOUS AND RECONFIGURABLE MATRICES OF DIVERSE AND ADAPTIVE COMPUTATIONAL UNITS HAVING FIXED, APPLICATION SPECIFIC COMPUTATIONAL ELEMENTS	07-Mar-03	10/384,486
QST-097 CV US	Reconfigurable Filter Node For An Adaptive Computing Machine	11-Mar-03	10/386,996
QST-011-1C US (incl. rejected claims)	Method and System for Reconfigurable Channel Coding	26-Mar-03	10/402,691
QST-010 CV US	Method and System for Optimizing Operations of an ACE	13-May-03	10/437,800
QST-047 CV US	Capability BASED OS	13-May-03	10/437,856
QST-072-1 CV US	RAN – RECONFIGURABLE ARITHMETIC NODE	21-May-03	10/443,596
QST-072-2 CV US	HTM – Hardware Task Manager	21-May-03	10/443,601
QST-072-3 CV US	Uniform Interface For A Functional Node in an ACM	21-May-03	10/443,554
QST-054 CV US	ACM Infrastructure Allows Fast Time-to-Market SOCs	12-Jun-03	10/459,859
QST-101 US	ACE PATENT IN THE DIGITAL IMAGERY SPACE	25-Jun-03	10/606,091
QST-105 US	Frequency-Domain Scaled Scheme for Discrete Cosine Transform (DCT)	01-Jul-03	10/612,202
QST-112 PR US	An Error Diffusion Scheme with Low-Complexity	03-Jul-03	60/484,706
QST-088-1 CV US	RXN- Run-time reconfigurable, multi-precision data-flow machine	23-Jul-03	10/626,833
QST-088-2 CV US	RXN- Run-time reconfigurable, multi-precision data-flow machine	23-Jul-03	10/626,479
QST-088-3 CV US	RXN- Run-time reconfigurable, multi-precision data-flow machine	24-Jul-03	10/628,083
QST-088-4 CV US	RXN- Run-time reconfigurable, multi-precision data-flow machine	24-Jul-03	10/628,036

PATENT
REEL: 038011 FRAME: 0283

APPLICATIONS FILED WITH USPTO

[illegible]	[illegible]	FILING DATE	SERIAL NUMBER
QST-102 US	Control-flow methodologies in ACM	14-Aug-03	10/641,976
QST-091 US	System for communication between a host and a heterogeneous multiprocessor environment using efficient asynchronous FIFO's	19-Aug-03	10/644,621
QST-114 US	PROGRAMMING LANGUAGE CONSTRUCTS SUPPORTING STATIC AND DYNAMIC CONFIGURATION OF AN ADAPTIVE COMPUTING DEVICE	21-Aug-03	10/646,269
QST-089 CV US	PSN – Programmable Scalar Node (RISC)	29-Sep-03	10/673,678
QST-041 CV US	RBN - Reconfigurable Bit Manipulation Node (originally: Patent for Bit Node, Arithmetic Node)	10-Oct-03	10/683563
QST-096 CV US	XMC NODE plus New Aspect of XMC being incorporated	20-Nov-03	10/718,921
QST-100 CV US	Input/Output Controller Node in An ACE	23-Nov-03	10/719,409
QST-117 PR US	Adaptive Computing Silverstream Design Method, System and Software	22-Jan-04	60/538,276
QST-089-1P US	PSN – Programmable Scalar Node (RISC)	26-Jan-04	10/765,556
QST-115 US	DBN Viterbi	18-Feb-04	10/784,484
QST-119 PR US	NEW NODE - DVN - Motion Estimation Node	10-May-04	TBD

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ASSIGNMENT TO TECHFARM VENTURES MANAGEMENT, LLC
FROM QUICKSILVER TECHNOLOGY, INC.

ADDITIONAL PATENT NUMBERS
AND PATENT APPLICATION NUMBERS

Patents:

2. 6,986,021;
3. 6,618,434;
4. 6,968,454;
5. 6,732,354;
6. 6,577,678;
7. 6,874,079;
8. 7,046,635;
9. 7,088,825;

Patent Applications:

10. 10/384,486;
11. 10/990,800;
12. 09/997,530;
13. 11/241,009;
14. 10/289,639;
15. 10/040,100;
16. 11/282,460;
17. 10/289,640;
18. 10/437,800;
19. 10/402,691;
20. 09/872,397;
21. 09/898,350;
22. 10/280,496;
23. 10/013,825;
24. 10/135,905;
25. 10/022,776;
26. 10/093,156;
27. 10/015,544;
28. 10/015,530;
29. 10/437,855;
30. 10/015,537;
31. 10/295,632;
32. 10/295,692;
33. 11/498,647;
34. 10/067,496;
35. 10/233,175;
36. 10/092,859;
37. 10/268,872;
38. 10/443,501;
39. 10/626,833;
40. 10/719,921;
41. 10/606,031; and
42. 10/645,269.