PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3819113

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
SEQUENCE:	1

CONVEYING PARTY DATA

Name	Execution Date
SKYMEDI CORPORATION	03/01/2016

RECEIVING PARTY DATA

Name:	AUSPITEK (SHENZHEN) INC.	
Street Address:	ROOM 201, BLOCK A, NO.1, QIANWAN ROAD 1	
Internal Address:	QIANHAISHEN PORT COOPERATIVE DISTRICT	
City:	SHENZHEN	
State/Country:	CHINA	
Postal Code:	518052	

PROPERTY NUMBERS Total: 20

Property Type	Number
Patent Number:	8074013
Patent Number:	8082386
Patent Number:	8239733
Patent Number:	8332728
Patent Number:	8355285
Patent Number:	8427219
Patent Number:	8429497
Patent Number:	8472246
Patent Number:	8503233
Patent Number:	8547077
Patent Number:	8566562
Patent Number:	8707135
Patent Number:	8730725
Patent Number:	8738083
Patent Number:	8855799
Patent Number:	8867272
Patent Number:	8892968
Patent Number:	8923071

PATENT REEL: 038211 FRAME: 0263

503772468

Property Type	Number
Patent Number:	8938561
Patent Number:	8995196

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: jeff.lee@piconetsinc.com

Correspondent Name: JEFF C. LEE

Address Line 1: 12F-1, NO.5, TAIYUAN 1ST STREET,

Address Line 4: CHUPEI CITY, TAIWAN 30288

NAME OF SUBMITTER:	JEFF C. LEE
SIGNATURE:	/CK Lee/
DATE SIGNED:	04/07/2016
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 4

source=SKY-MNLCP_20160407_124814#page1.tif source=SKY-MNLCP_20160407_124814#page2.tif source=MNLCP-AUSPI_20160407_124717#page1.tif source=MNLCP-AUSPI_20160407_124717#page2.tif

> PATENT REEL: 038211 FRAME: 0264

ASSIGNMENT OF RIGHTS - PATENT ISSUED

Skymedi Corporation ("Assignor") is owner of U.S. patents which are as the list on Exhibit (the "Patent").

MonocleCap Limited ("Assignee") desires to acquire rights in and to the Patent.

Therefore, for valuable consideration, the receipt of which is acknowledged. Assignor assigns to Assignee 100% of his right, title and interest in the invention and Patent to Assignee for the entire term of the Patent and any reissues or extensions and for the entire terms of any patents, reissues or extensions that may issue from foreign applications, divisions, continuations in whole or part or substitute applications filed claiming the benefit of the Patent. The right, title and interest conveyed in this Assignment is to be held and enjoyed by Assignee and Assignee's successors as fully and exclusively as it would have been held and enjoyed by Assignor had this assignment not been made.

Assignor further agrees to: (a) cooperate with Assignee in the protection of the patent rights and prosecution and protection of foreign counterparts; (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer; and (c) perform such other acts as Assignee lawfully may request to obtain or maintain the Patent and any and all applications and registrations for the

invention in any and all countries.

Date: 20/6,3./

PATENT REEL: 038211 FRAME: 0265

Exhibit

Patent No.	Issue Date	Title
US8074013	December 6, 2011	Uniform coding system for a flash memory
US8082386	December 20, 2011	Method of performing wear leveling with variable threshold
US8239733	August 7, 2012	Memory device with protection capability and method of accessing data therein
US8332728	December 11, 2012	Method and apparatus of generating a soft value for a memory device
US8355285	January 15, 2013	Method and system for adaptively finding reference voltages for reading data from a MLC flash memory
US8427219	April 23, 2013	Clock generator and a method of generating a clock signal
US8429497	April 23, 2013	Method and system of dynamic data storage for error correction in a memory device
US8472246	June 25, 2013	Method of programming a multi-bit per cell non-volatile memory
US8503233	August 6, 2013	Method of twice programming a non-volatile flash memory with a sequence
US8547077	October 1, 2013	Voltage regulator with adaptive miller compensation
US8566562	October 22, 2013	Method for sequentially writing data with an offset to a non-volatile memory
US8707135	April 22, 2014	Method and system of dynamic data storage for error correction in a memory device
US8730725	May 20, 2014	Method of programming/reading a non-volatile memory with a sequence
US8738083	May 27, 2014	Operating method, apparatus, and memory module integrated with wireless communication component
US8855799	October 7, 2014	Automated mass production method and system thereof
US8867272	October 21, 2014	Method of accessing a non-volatile memory
US8892968	November 18, 2014	Bit-level memory controller and a method thereof
US8923071	December 30, 2014	Method of programming a multi-bit per cell non-volatile memory
US8938561	January 20, 2015	Time-sharing buffer access system
US8995196	March 31, 2015	Method of sorting a multi-bit per cell non-volatile memory and a multi-mode configuration method

PATENT REEL: 038211 FRAME: 0266

ASSIGNMENT OF RIGHTS - PATENT ISSUED

MonocleCap Limited ("Assignor", a Hong Kong company) is owner of U.S. patents which are as the list on Exhibit (the "Patent").

AUSPITEK (SHENZHEN) INC. ("Assignee", a China company) desires to acquire rights in and to the Patent.

Therefore, for valuable consideration, the receipt of which is acknowledged,
Assignor assigns to Assignee 100% of his right, title and interest in the invention and
Patent to Assignee for the entire term of the Patent and any reissues or extensions and
for the entire terms of any patents, reissues or extensions that may issue from foreign
applications, divisions, continuations in whole or part or substitute applications filed
claiming the benefit of the Patent. The right, title and interest conveyed in this
Assignment is to be held and enjoyed by Assignee and Assignee's successors as fully
and exclusively as it would have been held and enjoyed by Assignor had this assignment
not been made.

Assignor further agrees to: (a) cooperate with Assignee in the protection of the patent rights and prosecution and protection of foreign counterparts; (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer; and (c) perform such other acts as Assignee lawfully may request to obtain or maintain the Patent and any and all applications and registrations for the invention in any and all countries.

Date: 67 116

Assignor

Exhibit

Patent No.	Issue Date	Title
US8074013	December 6, 2011	Uniform coding system for a flash memory
US8082386	December 20, 2011	Method of performing wear leveling with variable threshold
US8239733	August 7, 2012	Memory device with protection capability and method of accessing data therein
US8332728	December 11, 2012	Method and apparatus of generating a soft value for a memory device
US8355285	January 15, 2013	Method and system for adaptively finding reference voltages for reading data from a MLC flash memory
US8427219	April 23, 2013	Clock generator and a method of generating a clock signal
US8429497	April 23, 2013	Method and system of dynamic data storage for error correction in a memory device
US8472246	June 25, 2013	Method of programming a multi-bit per cell non-volatile memory
US8503233	August 6, 2013	Method of twice programming a non-volatile flash memory with a sequence
US8547077	October 1, 2013	Voltage regulator with adaptive miller compensation
US8566562	October 22, 2013	Method for sequentially writing data with an offset to a non-volatile memory
US8707135	April 22, 2014	Method and system of dynamic data storage for error correction in a memory device
US8730725	May 20, 2014	Method of programming/reading a non-volatile memory with a sequence
US8738083	May 27, 2014	Operating method, apparatus, and memory module integrated with wireless communication component
US8855799	October 7, 2014	Automated mass production method and system thereof
US8867272	October 21, 2014	Method of accessing a non-volatile memory
US8892968	November 18, 2014	Bit-level memory controller and a method thereof
US8923071	December 30, 2014	Method of programming a multi-bit per cell non-volatile memory
US8938561	January 20, 2015	Time-sharing buffer access system
US8995196	March 31, 2015	Method of sorting a multi-bit per cell non-volatile memory and a multi-mode configuration method

PATENT REEL: 038211 FRAME: 0268

RECORDED: 04/07/2016