503774929 04/08/2016

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

EPAS ID: PAT3821574

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	ASSIGNMENT		

CONVEYING PARTY DATA

Name	Execution Date
KHEE YONG LIM	02/04/2016
JAE HAN CHA	02/04/2016
CHIA CHING YEO	02/04/2016
KIOK BOONE ELGIN QUEK	02/04/2016

RECEIVING PARTY DATA

Name: GLOBALFOUNDRIES SINGAPORE PTE. LTD.			
Street Address:	60 WOODLANDS INDUSTRIAL PARK D STREET 2		
City:	SINGAPORE		
State/Country:	SINGAPORE		
Postal Code:	738406		

PROPERTY NUMBERS Total: 1

Property Type	Number			
Application Number:	15093888			

CORRESPONDENCE DATA

Fax Number: (480)385-5061

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 480-385-5060

Email: docketing@ifllaw.com

INGRASSIA FISHER & LORENZ, P.C. (GF) **Correspondent Name:**

Address Line 1: 7010 E. COCHISE RD.

Address Line 4: SCOTTSDALE, ARIZONA 85253

ATTORNEY DOCKET NUMBER:	126.0097 (HSG032)
NAME OF SUBMITTER:	RYAN T. FORTIN
SIGNATURE:	/Ryan T. Fortin/
DATE SIGNED:	04/08/2016

Total Attachments: 4

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COMBINED DECLARATION and ASSIGNMENT (37) CFR 1.63) FOR UTILITY OR DESIGNATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title of Invention	METHODS FOR FABRICATING INTEGRATED CIRCUITS WITH LOW, MEDIUM, AND/OR HIGH VOLTAGE TRANSISTORS ON AN EXTREMELY THIN SILICON-ON-INSULATOR SUBSTRATE				
**************************************	DECLARATION				
As the below	named inventor, I hereby declare that:				
This declaration					
	United States application or PCT international application number filed on				
The above-identified application was made or authorized to be made by me.					
I believe that	I am the original inventor or an original joint inventor of a claimed invention in the application.				
	owledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by onment of not more than five (5) years, or both.				
	ed and understand the contents of the application and am aware of the duty to disclose to the US Patent and ffice all information known to me to be material to patentability as defined in 37 CFR § 1.56.				
	ASSIGNMENT				
For good and valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby sell, assign, transfer, to GLOBALFOUNDRIES Singapore Pte. Ltd. having a business address of 60 Woodlands Industrial Park D Street 2, Singapore 738406 its successors and assigns, absolutely and forever, my entire right, title and interest in and to the invention listed above, together with the Application, any and all Patents that may issue in the United States and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-inparts, divisionals or extensions thereof that may be issued or granted, and all right, title and interest to the inventions contained in said Patents and Applications, for the United States and all foreign countries, and all the rights and privileges relating thereto including but not limited to the priority rights arising from said Applications, the right to recover and take all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, present and future infringement, and the right to apply for, take and maintain patents on said inventions. If any portion of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof shall nevertheless remain in full force and effect.					
LEGAL NAM	1E OF INVENTOR				
Inventor:	Khee Yong Lim Date: Feb, 4, 2016				
Signature	ankliegers.				

Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

84625460v1

GF SG Ref. No.: HSG032

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN VI APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title of Invention	METHODS FOR FABRICATING INTEGRATED CIRCUITS WITH LOW, MEDIUM, AND/OR HIGH VOLTAGE TRANSISTORS ON AN EXTREMELY THIN SILICON-ON-INSULATOR SUBSTRATE			
	DECLARATION			
As the below	named inventor, I bereby declare that:			
This declaration The attached application, or is directed to:				
	United States application or PCT international application number filed on			
The above-ide	entified application was made or authorized to be made by me.			
I believe that	I am the original inventor or an original joint inventor of a claimed invention in the application.			
	owledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by comment of not more than five (5) years, or both.			
	ed and understand the contents of the application and am aware of the duty to disclose to the US Patent and fice all information known to me to be material to patentability as defined in 37 CFR § 1.56.			
	ASSIGNMENT			
For good and valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby sell, assign, transfer, to GLOBALFOUNDRIES Singapore Pte. Ltd. having a business address of 60 Woodlands Industrial Park D Street 2, Singapore 738406 its successors and assigns, absolutely and forever, my entire right, title and interest in and to the invention listed above, together with the Application, any and all Patents that may issue in the United States and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-inparts, divisionals or extensions thereof that may be issued or granted, and all right, title and interest to the inventions contained in said Patents and Applications, for the United States and all foreign countries, and all the rights and privileges relating thereto including but not limited to the priority rights arising from said Applications, the right to recover and take all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, present and future infringement, and the right to apply for, take and maintain patents on said inventions.				
	of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof cless remain in full force and effect.			
RANGUUKAUNAN manananan mananan				
LEGAL NAM	E OF INVENTOR			
Inventor:	Jac Han Cha Date: 4th feb, 24/6			
Signature:				
Note: An applic	eation data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form.			

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Or SU Kel, No.: HSG032

APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title of Invention	METHODS FOR FABRICATING INTEGRATED CIRCUITS WITH LOW MEDIUM, AND/OR HIGH VOLTAGE TRANSISTORS ON AN EXTREMELY THIN SILICON-ON-INSULATOR SUBSTRATE					
	DECLARATION					
As the below	named inventor, I hereby declare that:					
This declaration The attached application, or is directed to:						
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The above-ide	entified application was made or authorized to be made by me.					
I believe that	I am the original inventor or an original joint inventor of a claimed invention in the application.					
	owledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by onment of not more than five (5) years, or both.					
	ed and understand the contents of the application and am aware of the duty to disclose to the US Patent and flice all information known to me to be material to patentability as defined in 37 CFR § 1.56.					
	ASSIGNMENT					
sell, assign, a Industrial Pari interest in and United States parts, division contained in privileges related recover and tapresent and further present and further portion of the present and further present	valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby transfer, to GLOBALFOUNDRIES Singapore Pte. Ltd. having a business address of 60 Woodlands & D Street 2, Singapore 738406 its successors and assigns, absolutely and forever, my entire right, title and d to the invention listed above, together with the Application, any and all Patents that may issue in the and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-intended or extensions thereof that may be issued or granted, and all right, title and interest to the inventions said Patents and Applications, for the United States and all foreign countries, and all the rights and atting thereto including but not limited to the priority rights arising from said Applications, the right to ake all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, ture infringement, and the right to apply for, take and maintain patents on said inventions. In of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof eless remain in full force and effect.					

Note: An application data sheet (PTO/SB/14 or equivalent), including naming the entire inventive entity, must accompany this form. Use an additional form for each additional inventor.

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LEGAL NAME OF INVENTOR

Inventor: Chia Ching Yeo

Signature:

GF SG Ref. No.: HSG032

COMBINED DECLARATION and ASSIGNMENT (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

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Title of Invention	METHODS FOR FABRICATING INTEGRATED CIRCUITS WITH LOW, MEDIUM, AND/OR HIGH VOLTAGE TRANSISTORS ON AN EXTREMELY THIN SILICON-ON-INSULATOR SUBSTRATE					
		Ε	ECLARATION			
As the below	named invent	or, I hereby declare that:				
This declaration						
		United States application	n or PCT internati	onal application	n number	filed on
The above-ide	entified applic	ation was made or author	ized to be made b	y me.		
I believe that l	am the origi	nal inventor or an origina	l joint inventor of	a claimed inve	ntion in the applicat	ion.
		any willful false statemen more than five (5) years,		laration is puni	shable under 18 U.S	S.C. 1001 by
		tand the contents of the a nation known to me to be				
			ASSIGNMENT			
For good and valuable consideration, the receipt, sufficiency and adequacy of which are hereby acknowledged, I hereby sell, assign, transfer, to GLOBALFOUNDRIES Singapore Pte. Ltd. having a business address of 60 Woodlands Industrial Park D Street 2, Singapore 738406 its successors and assigns, absolutely and forever, my entire right, title and interest in and to the invention listed above, together with the Application, any and all Patents that may issue in the United States and/or any foreign countries, and any reissues, re-examinations, renewals, continuations, continuation-in-parts, divisionals or extensions thereof that may be issued or granted, and all right, title and interest to the inventions contained in said Patents and Applications, for the United States and all foreign countries, and all the rights and privileges relating thereto including but not limited to the priority rights arising from said Applications, the right to recover and take all such proceedings as may be necessary for the recovery of damages or otherwise in respect of past, present and future infringement, and the right to apply for, take and maintain patents on said inventions.						
If any portion of this Combined Declaration or Assignment is held unenforceable, then remaining portions hereof shall nevertheless remain in full force and effect.						
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LEGAL NAM	E OF INVEN	ITOR				
Inventor:	Kiok Boone	Elgin Quek		Date:	Feb 4, 201	<u> </u>
Signature:	<u> </u>	eleki-		7h		
		et (PTO/SB/14 or equivalent), including naming	the entire invent	tive entity, must accon	apany this form.

PATENT REEL: 038225 FRAME: 0604

RECORDED: 04/08/2016