PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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EPAS ID: PAT3831331

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
EAST WEST BANK	04/12/2016

RECEIVING PARTY DATA

Name:	MAXWELL TECHNOLOGIES, INC.
Street Address:	3888 CALLE FORTUNADA
City:	SAN DIEGO
State/Country:	CALIFORNIA
Postal Code:	92123

PROPERTY NUMBERS Total: 34

Property Type	Number
Patent Number:	7467326
Patent Number:	7382043
Patent Number:	6583432
Patent Number:	6963125
Patent Number:	6613978
Patent Number:	6455864
Patent Number:	6368899
Patent Number:	6720493
Patent Number:	6261508
Patent Number:	6064555
Patent Number:	8930753
Patent Number:	8661446
Patent Number:	8018739
Patent Number:	7890799
Patent Number:	7613948
Patent Number:	7696610
Patent Number:	7683186
Patent Number:	8032889
Patent Number:	7415630
Patent Number:	7437599

Property Type	Number
Patent Number:	7475326
Patent Number:	7148084
Patent Number:	7191516
Patent Number:	5880403
Patent Number:	5889316
Patent Number:	5825042
Patent Number:	5635754
Patent Number:	6262362
Application Number:	14188583
Application Number:	61407770
Application Number:	08077731
Application Number:	60483210
Application Number:	60451041
Application Number:	08791256

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	381874-23
NAME OF SUBMITTER:	TROY ZANDER
SIGNATURE:	/s/ Troy Zander
DATE SIGNED:	04/14/2016

Total Attachments: 3
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EXECUTION VERSION

REASSIGNMENT AND RELEASE OF SECURITY INTEREST

This Reassignment and Release of Security Interest (the "**Release**") is executed as of April 12, 2016, by EAST WEST BANK, a California corporation ("**Assignor**"), for the benefit of MAXWELL TECHNOLOGIES, INC., a Delaware corporation ("**Assignee**").

RECITALS

A. WHEREAS, Assignee granted the Assignor a security interest in the patents described on **Exhibit A** (together, the "**Designated Patents**") pursuant to the Intellectual Property Security Agreement, dated as of July 3, 2015 (the "**IP Security Agreement**"); and

B. WHEREAS, the IP Security Agreement was recorded with the United States Patent and Trademark Office on July 6, 2015, at Reel/Frame 036064/0636;

AGREEMENT

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor hereby releases and discharges fully its security interest in the Designated Patents and reassigns to Assignee, without warranty or recourse, all interest of Assignor in and to the Designated Patents.

Assignor hereby authorizes Assignee or Assignee's agents or designees to (i) record this Release with the United States Patent and Trademark Office and any foreign intellectual property registrars, or (ii) otherwise file or record this Release as necessary to evidence Assignor's release and discharge of its security interest in the Designated Patents. Assignor further agrees to execute and deliver to Assignee all further documents and instruments, and to do all further acts which Assignee (or its agents or designees) reasonably requests, at Assignee's cost and expense, in order to confirm this Release and the Assignee's right, title, and interest in the Designated Patents.

ASSIGNOR:

EAST WEST BANK

By: _____
Name: Alexis Coyle
Title: Managing Director

Address:

555 Montgomery Street, 9th Floor
San Francisco, CA 94111
Attention: Alexis Coyle

EXHIBIT A

Patents

Description	Patent/App. No.	Issue/ File Date
Method and apparatus for managing and controlling power consumption and heat generation in computer systems	14188583	2/24/14
System, Method and Apparatus for Error Correction in Multi-Processor Systems	8930753	1/6/15
Method and apparatus for managing and controlling power consumption and heat generation in computer systems	8661446	2/25/14
Cache coherency using checksums	61407770	10/28/10
Apparatus for shielding integrated circuit devices	8018739	9/13/11
Self-correcting computer	7890799	2/15/11
Cache coherency during resynchronization of self-correcting computer	7613948	11/3/09
Method for shielding integrated circuit devices	7696610	4/13/10
Apparatus and method for cold sparing in multi-board computer systems	7683186	3/2/10
Method and apparatus for managing and controlling power consumption and heat generation in computer systems	8032889	10/4/11
Cache coherency during resynchronization on self-correcting computer	7415630	8/19/08
System and method for effectively implementing an immunity mode in an electronic device	7437599	10/14/08
Error detection and correction method and system for memory devices	7475326	1/6/09
Radiation shielding of integrated circuits and multi-chip modules in ceramic metal packages	7148084	12/12/06
Method for shielding integrated circuit devices	7191516	3/20/07
Error detection and correction method and system for memory devices	60483210	6/27/03
Self-correcting computer	7467326	12/16/08
Self-correcting computer	60451041	2/28/03
Method and apparatus for shielding an integrated from radiation	7382043	6/3/08
Methods and compositions for ionizing radiation shielding	6583432	6/24/03
Electronic device packaging	6963125	11/8/05

Radiation shielding of three dimensional multi-chip modules	6613978	9/2/03
Methods and compositions for ionizing radiation shielding	6455864	9/24/02
Electronic device packaging	6368899	4/9/02
Radiation shielding of integrated circuits and multi-chip modules in ceramic metal packages	6720493	4/13/04
Method for making a shielding composition	6261508	7/17/01
Radiation shielding of three dimensional multi-chip modules	6262362	7/17/01
Radiation induced single event latchup protection and recovery of integrated circuits	6064555	5/16/00
Methods and compositions for ionizing radiation shielding	08791256	1/30/97
Radiation shielding of three dimensional multi-chip modules	5880403	3/9/99
Radiation shielding of plastic integrated circuits	5889316	3/30/99
Radiation shielding of plastic integrated circuits	5825042	10/20/98
Radiation shielding of integrated circuits and multi-chip modules in ceramic metal packages	5635754	6/3/97
Radiation shielding of plastic integrated circuits	08077731	6/18/93
Radiation shielding of three dimensional multi-chip modules	6262362	7/17/01