503812430 05/04/2016

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT3859078

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
YULIANG WANG	04/05/2016
DAEYOUNG CHOI	04/29/2016
ZENGLI LIU	04/05/2016
DAOJIE LI	04/05/2016
FEI AI	04/29/2016
JUN ZHOU	04/05/2016

RECEIVING PARTY DATA

Name:	BOE TECHNOLOGY GROUP CO., LTD.
Street Address:	NO. 10 JIUXIANQIAO RD.
Internal Address:	CHAOYANG DISTRICT
City:	BEIJING
State/Country:	CHINA
Postal Code:	100015
Name:	HEFEI XINSHENG OPTOELECTRONICS TECHNOLOGY CO., LTD.
Street Address:	XINZHAN INDUSTRIAL PARK, HEFEI
City:	ANHUI
State/Country:	CHINA
Postal Code:	230012

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	15146643

CORRESPONDENCE DATA

Fax Number: (212)589-4201

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 2028611687

Email: Patents@bakerlaw.com, kadkins@bakerlaw.com

Correspondent Name: BAKER & HOSTETLER LLP

Address Line 1: WASHINGTON SQUARE, SUITE 1100 1050 CONNECTICUT AVENUE, N.W. Address Line 2:

REEL: 038458 FRAME: 0674 503812430

PATENT

Address Line 4: WA	SHINGTON, D.C. 20036
ATTORNEY DOCKET NUMBER:	96544.244
NAME OF SUBMITTER:	EUGENE LIEBERSTEIN
SIGNATURE:	/EUGENE LIEBERSTEIN/
DATE SIGNED:	05/04/2016
	This document serves as an Oath/Declaration (37 CFR 1.63).
Total Attachments: 12	
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Title of Invention	THIN FILM TRANSISTOR, ARRAY SUBSTRATE, THEIR MANUFACTURING METHODS, AND DISPLAY DEVI					
As a below	named inventor, I hereby declare that:					
This declar	ration					
is directed	to: The attached application, or					
	United States application or PCT international application numberfiled on					
The above-i	dentified application was made or authorized to be made by me.					
I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.						
	knowledge that any willful false statement made in this declaration is under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) th.					

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, each undersigned inventor (hereinafter referred to as "ASSIGNOR"), do hereby:

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AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such inventions to the Assignee;

Docket No. X1400247B5-OIUS162166CN

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and properly herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such inventions; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such inventions and the history thereof; and generally do everything possible which the Assignee shall consider desirable for vesting title to such inventions in the Assignee, and for securing, maintaining and enforcing proper patent protection for such inventions;

TO BE BINDING on the heirs; assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

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PATENT

REEL: 038458 FRAME: 0677

Title of Invention	THIN FILM TRANSISTOR, ARRAY SUBSTRATE. THEIR MANUFACTURING METHODS, AND DISPLAY DEVICE
As a below	named inventor, I hereby declare that:
This decla	The attached application or
is directed	United States application or PCT international application numberfiled on
The above-	dentified application was made or authorized to be made by me.
I believe than	at I am the original inventor or an original joint inventor of a claimed invention cation.
	knowledge that any willful false statement made in this declaration is under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) th.

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I acknowledge the duty to disclose to the United States Patent Office all information known to me to be material to patentability as defined in 37 C.F.R. 1.36, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part.

LEGAL NAME OF INVENTOR
Invertor: Zengli LIU Date: Apy. 5 , 75 / 6
Signature: Zengli LIU:

Title of Invention	THIN FILM TRANSISTOR, ARRAY SUBSTRATE, THEIR MANUFACTURING METHODS, AND DISPLAY DEVICE
As a below	named inventor, I hereby declare that:
This decla	Post day of
is directed	to: The attached application, or
	United States application or PCT international application numberfiled on
The above-	dentified application was made or authorized to be made by me.
l believe tha in the applic	It I am the original inventor or an original joint inventor of a claimed invention ation.
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LEGAL NAME OF	- INVE	NTOR										
nventor: <u>Deglie l</u>	i Si	Date:_	 (ex	 ,	22	16	 					
Signature:	oii.	/	Ĭ.									

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LEGAL NAME OF INVENTOR

Inventor: Jun ZHOU

Date: <u>APY, 5, 70/6</u>

Signature: Tun 2400

i.

PATENT

REEL: 038458 FRAME: 0683

Title of Invention	THIN FILM TRANSISTOR, ARRAY SUBSTRATE, THEIR MANUFACTURING METHODS, AND DISPLAY DEVICE
As a below	named inventor, I hereby declare that:
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j	LEGAL NAME OF INVENTOR					
	inventor: <u>Daeyoung CHOI</u>	Date: 127	12016			
	Signature: Vacyoung	CHOL			,	

Title of Invention	THIN FILM TRANSISTOR, ARRAY SUBSTRATE, THEIR MANUFACTURING METHODS, AND DISPLAY DEVICE						
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	LEGAL NAME OF INVENTOR	
	Inventor: Fel Al Date: APV. 29, 2016	•
	Signature: FeiAZ	andrascapes an accompany property and