

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3862800

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST	
CONVEYING PARTY DATA		
Name		Execution Date
THE BANK OF NEW YORK MELLON TRUST COMPANY, N.A.		03/10/2014
RECEIVING PARTY DATA		
Name:	CONEXANT SYSTEMS, INC.	
Street Address:	1901 MAIN STREET	
Internal Address:	SUITE 300	
City:	IRVINE	
State/Country:	CALIFORNIA	
Postal Code:	92614	
Name:	CONEXANT, INC.	
Street Address:	1901 MAIN STREET	
Internal Address:	SUITE 300	
City:	IRVINE	
State/Country:	CALIFORNIA	
Postal Code:	92614	
Name:	CONEXANT SYSTEMS WORLDWIDE, INC.	
Street Address:	1901 MAIN STREET	
Internal Address:	SUITE 300	
City:	IRVINE	
State/Country:	CALIFORNIA	
Postal Code:	92614	
Name:	BROOKTREE BROADBAND HOLDING, INC.	
Street Address:	1901 MAIN STREET	
Internal Address:	SUITE 300	
City:	IRVINE	
State/Country:	CALIFORNIA	
Postal Code:	92614	
PROPERTY NUMBERS Total: 547		
Property Type	Number	
Patent Number:	5550869	

PATENT

Property Type	Number
Patent Number:	5570390
Patent Number:	5557632
Patent Number:	5583887
Patent Number:	5574737
Patent Number:	5572537
Patent Number:	5381095
Patent Number:	5631923
Patent Number:	5559799
Patent Number:	5625643
Patent Number:	5444712
Patent Number:	5422924
Patent Number:	5473754
Patent Number:	5648993
Patent Number:	5379077
Patent Number:	5463336
Patent Number:	5463633
Patent Number:	5541990
Patent Number:	5440162
Patent Number:	5655132
Patent Number:	5541961
Patent Number:	5513216
Patent Number:	5541598
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Patent Number:	5715437
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Patent Number:	5627396
Patent Number:	5620933
Patent Number:	5650953
Patent Number:	5563908
Patent Number:	5537437
Patent Number:	5598434
Patent Number:	5612996
Patent Number:	5646990
Patent Number:	6021160
Patent Number:	6154490
Patent Number:	5717904
Patent Number:	5987064

Property Type	Number
Patent Number:	5896576
Patent Number:	5852561
Patent Number:	5781132
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Patent Number:	5959510
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Patent Number:	7535920
Patent Number:	7724637
Patent Number:	7733878
Patent Number:	7835265
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Patent Number:	8554203
Application Number:	10705909
Application Number:	11139370
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Application Number:	11438011
Application Number:	11713393
Application Number:	11930715
Application Number:	11947946
Application Number:	11954579
Application Number:	11956756
Application Number:	12051475
Application Number:	12106260
Application Number:	12152753
Application Number:	12165590
Application Number:	12371406
Application Number:	12469647

Property Type	Number
Application Number:	12497241
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Application Number:	12833606
Application Number:	12839771
Application Number:	12852353
Application Number:	12872733
Application Number:	12878796
Application Number:	12925828
Application Number:	12927549
Application Number:	12931240
Application Number:	12963443
Application Number:	13010586
Application Number:	13037091
Application Number:	13040121
Application Number:	13052784
Application Number:	13065620
Application Number:	13066731
Application Number:	13092006
Application Number:	13092743
Application Number:	13149713
Application Number:	13149725
Application Number:	13149735
Application Number:	13149747
Application Number:	13166600
Application Number:	13168605
Application Number:	13183288
Application Number:	13184231
Application Number:	13214123
Application Number:	13230633
Application Number:	13242078
Application Number:	13269358
Application Number:	13270012
Application Number:	13273122
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Property Type	Number
Application Number:	13284833
Application Number:	13285305
Application Number:	13289926
Application Number:	13312879
Application Number:	13312897
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Application Number:	13344510
Application Number:	13368308
Application Number:	13426469
Application Number:	13431662
Application Number:	13431889
Application Number:	13443849
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Application Number:	13540466
Application Number:	13548045
Application Number:	13552523
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Application Number:	13560439
Application Number:	13618443
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Application Number:	13693569
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Application Number:	13752058
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Application Number:	13766003
Application Number:	13767759
Application Number:	13774923
Application Number:	13783074
Application Number:	13790967
Application Number:	13840302
Application Number:	13851906
Application Number:	13856393
Application Number:	13860477
Application Number:	13887230
Application Number:	13892064

Property Type	Number
Application Number:	13919973
Application Number:	13935413
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Application Number:	13950242
Application Number:	14040429
Application Number:	14062840
Application Number:	61711672
Application Number:	61737590
Application Number:	61772441
Application Number:	61772451
Application Number:	61825452
Application Number:	61826906
Application Number:	61833388
Application Number:	61898038

CORRESPONDENCE DATA

Fax Number: (214)651-5940

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 949-202-3000

Email: tammy.lightman@haynesboone.com

Correspondent Name: HAYNES AND BOONE, LLP

Address Line 1: 2323 VICTORY AVENUE

Address Line 2: SUITE 300

Address Line 4: DALLAS, TEXAS 75219

ATTORNEY DOCKET NUMBER: 70548.1

NAME OF SUBMITTER: DENNIS GALLAGHER

SIGNATURE: /Dennis Gallagher/

DATE SIGNED: 05/06/2016

Total Attachments: 21

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RELEASE OF SECURITY INTEREST

This release of security interest is made and executed by The Bank of New York Mellon Trust Company, N.A., in its capacity as collateral trustee (the "**Collateral Trustee**") for certain secured parties, in favor of Conexant Systems, Inc., a Delaware corporation, Conexant, Inc., a Delaware corporation, Conexant Systems Worldwide, Inc., a Delaware Corporation, and Brooktree Broadband Holding, Inc., a Delaware Corporation (collectively, the "**Grantors**").

WHEREAS, the Collateral Trustee and the Grantors entered into that certain Blanket Pledge and Security Agreement, dated as of March 10, 2010 (as amended, supplemented or otherwise modified from time to time, the "**Security Agreement**").

WHEREAS, the Collateral Trustee and the Grantor entered into certain patent security agreements in accordance with the terms of the Security Agreement, including that certain Patent Security Agreement, dated as of March 10, 2010, as amended, supplemented or otherwise modified from time to time (collectively, the "**Patent Security Agreements**").

WHEREAS, pursuant to the Security Agreement and the Patent Security Agreements, the Grantors granted to the Collateral Trustee, for the benefit of the Secured Parties (as such term is defined in the Security Agreement), a security interest in and continuing lien on all of such Grantor's right, title and interest in, to and under the Patent Collateral (as such term is defined in the Patent Security Agreement), including but not limited to each patent listed on **Schedule A** attached hereto (the "**Relevant Patents**").

WHEREAS, on February 28, 2013, the Grantors filed voluntary petitions for relief under chapter 11 of the Bankruptcy Code with the United States Bankruptcy Court for the District of Delaware.

WHEREAS, on May 13, 2013, the Grantors filed the *Plan Supplement to the Second Modified Joint Plan of Reorganization of Conexant Systems, Inc. and Its Debtor Affiliates Pursuant to Chapter 11 of the Bankruptcy Code*, and on May 24, 2013, the Grantors filed the *First Supplement to the Plan Supplement to the Second Modified Joint Plan of Reorganization of Conexant Systems, Inc. and Its Debtor Affiliates Pursuant to Chapter 11 of the Bankruptcy Code* (the "**Plan**").

WHEREAS, on June 6, 2013 the bankruptcy court entered its *Findings of Facts, Conclusions of Law, and Order Confirming the Second Modified Joint Plan of Reorganization of Conexant Systems, Inc. and Its Debtor Affiliates Pursuant to Chapter 11 of the Bankruptcy Code* (the "**Confirmation Order**"), which became effective on July 12, 2013 (the "**Effective Date**").

WHEREAS, Article VIII.C of the Plan, titled "Release of Liens", provides that as of the Effective Date "all mortgages, deeds of trust, Liens, pledges or other security

interests against any property of the Estates shall be fully released and discharged, and all right, title and interest of any holder in such mortgages, deeds, Liens, pledges or other security interests shall revert to the Reorganized Debtor and its successors and assigns."

WHEREAS, the parties desire to execute this Release of Security Interest to confirm the release of Collateral Trustee' right, title and interest in, to and under the Relevant Patents, including all right, title and interest granted to the Collateral Trustee pursuant to the Security Agreement and Patent Security Agreements.

NOW, THEREFORE, the Collateral Trustee hereby (i) confirms that all of its right, title and interest in, to and under the Relevant Patents (including all security interests in and continuing liens on the Relevant Patents) were irrevocably and unconditionally released under the Confirmation Order and Plan as of the Effective Date; and (ii) to the extent necessary, hereby irrevocably and unconditionally releases all of its right, title and interest in the Relevant Patents.

Collateral Trustee hereby authorizes the Grantors, and/or the Grantors authorized representatives to record this Release of Security Interest in the U.S. Patent and Trademark Office and other appropriate filing offices, in order to memorialize the release of any security interest by the Collateral Agent in the Relevant Patents.

THIS RELEASE OF LIEN SHALL BE CONSTRUED IN ACCORDANCE WITH AND GOVERNED BY THE LAWS OF THE STATE OF NEW YORK APPLICABLE TO CONTRACTS MADE AND TO BE PERFORMED WHOLLY WITHIN THE STATE OF NEW YORK.

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IN WITNESS WHEREOF, the Collateral Trustee has executed this Release of Security Interest on this 10th day of March, 2014.

THE BANK OF NEW YORK MELLON
TRUST COMPANY, N.A., as Collateral
Trustee

By: Sharon McGrath

Name: Sharon McGrath

Title: Vice President

SCHEDULE A TO RELEASE OF LIEN

The Relevant Patents

#	#	File Date	Issued Date	Title
5,550,869	07/998,300	30-Dec-1992	27-Aug-1996	Demodulator for Consumer Uses PSK Demodulator (In Canada)
5,570,390	08/009,103	26-Jan-1993	29-Oct-1996	TIMING GENERATION DEVICE AND DATA TRANSMISSION DEVICE HAVING THE TIMING GENERATION DEVICE
5,557,632	08/029,238	10-Mar-1993	17-Sep-1996	DEMODULATION AND ROLL-OFF FILTER APPARATUS
5,583,887	08/031,621	15-Mar-1993	10-Dec-1996	TRANSMISSION SIGNAL PROCESSING APPARATUS
5,574,737	08/053,803	29-Apr-1993	12-Nov-1996	MODULATOR-DEMODULATOR DEVICE CAPABLE OF DETECTING AN UNSYNCHRONIZED FRAME STATE
5,572,537	08/053,804	29-Apr-1993	05-Nov-1996	MODULATOR-DEMODULATOR DEVICE CAPABLE OF DETECTING AN UNSYNCHRONIZED FRAME STATE
5,381,095	08/080,907	21-Jun-1993	10-Jan-1995	Method of Estimating Location and Orientation of Magnetic Dipoles Using Extended Kalman Filtering and Schweppe Likelihood Ratio Detection
5,631,923	08/089,190	12-Jul-1993	20-May-1997	TWO-WIRE FULL-DUPLEX MODEM
5,559,799	08/108,536	19-Aug-1993	24-Sep-1996	MODULATOR AND DEMODULATOR APPARATUS AS WELL AS MODULATION AND DEMODULATION METHOD
5,625,643	08/108,547	19-Aug-1993	29-Apr-1997	MODULATOR AND DEMODULATOR APPARATUS
5,444,712	08/113,113	30-Aug-1993	22-Aug-1995	Communication Mode Identification Technique
5,422,924	08/130,802	06-Dec-1993	06-Jun-1995	Gain and Offset Control for Charge Based Signals
5,473,754	08/155,891	23-Nov-1993	05-Dec-1995	Branch Decision Encoding Scheme
5,648,993	08/160,917	03-Dec-1993	15-Jul-1997	METHOD AND APPARATUS FOR SYNCHRONIZING TRANSMISSION OF MODEM TRANS- MISSION BY CONTROLLING A MEASURED PHASE DIFFERENCE BETWEEN AN INTERNAL TIMING SIGNAL AND A TRANSMISSION TIMING SIGNAL
5,379,077	08/164,263	09-Dec-1993	03-Jan-1995	System for and Method of, Operating Upon NTSC and PAL Signals
5,463,336	08/186,939	27-Jan-1994	31-Oct-1995	Supply Sensing Power-On Reset Circuit
5,463,633	08/199,437	22-Feb-1994	31-Oct-1995	Use of AT Commands to Test Modem with HP3065
5,541,990	08/270,538	05-Jul-1994	30-Jul-1996	Compensated Hybrid
5,440,162	08/280,417	26-Jul-1994	08-Aug-1995	ESD Protection for Submicron CMOS Circuits
5,655,132	08/287,017	08-Aug-1994	05-Aug-1997	Register File with Multi-Tasking Support
5,541,961	08/290,253	15-Aug-1994	30-Jul-1996	Digitally Controlled High Resolution Hybrid Phase Synthesizer
5,513,216	08/322,877	13-Oct-1994	30-Apr-1996	Hybrid Equalizer Arrangement for Use in Data Communications Equipment
5,541,598	08/333,973	03-Nov-1994	30-Jul-1996	Current Cell for Converting a Binary Value to an Analog Value
5,732,279	08/337,924	10-Nov-1994	24-Mar-1998	System and Method for Command Processing or Emulation in a Computer System Using Interrupts, Such as Emulation of DMA Commands Using Burst Mode Data Transfer for Sound or the Like
5,715,437	08/337,939	10-Nov-1994	03-Feb-1998	System for, and Method of, Processing in Hardware Commands Received from Software Without Polling of the Hardware by the Software
5,596,284	08/338,012	10-Nov-1994	21-Jan-1997	System for, and Method of, Minimizing Noise in an Integrated Circuit Chip

5,598,433	08/441,014	01-Feb-1993	28-Jan-1997	AUTOMATIC EQUALIZER AND DATA MODE CONVERGENCE METHOD
5,627,396	08/443,456	18-May-1995	06-May-1997	Micromachined Relay and Method of Forming the Relay
5,620,933	08/445,139	19-May-1995	15-Apr-1997	Micromachined Relay and Method of Forming the Relay
5,650,953	08/468,186	06-Jun-1995	22-Jul-1997	RECIPROCAL NUMBER ARITHMETIC OPERATING METHOD AND CIRCUIT WHICH ARE USED IN MODEM
5,563,908	08/472,291	07-Jun-1995	08-Oct-1996	MODULATOR AND DEMODULATOR APPARATUS
5,537,437	08/505,479	19-Aug-1993	16-Jul-1996	INITIALIZATION EQUALIZATION FOR MODULATION AND DEMODULATION USING SPECIAL TRAINING PATTERN
5,598,434	08/524,835	07-Sep-1995	28-Jan-1997	AUTOMATIC EQUALIZER WITH A BRANCHED INPUT FOR IMPROVED ACCURACY
5,612,996	08/531,992	21-Sep-1995	18-Mar-1997	Loop Gain Processing System for Speakerphone Applications
5,646,990	08/531,993	21-Sep-1995	08-Jul-1997	Efficient Speakerphone Anti-Howling System
6,021,160	08/539,067	04-Oct-1995	01-Feb-2000	TRAINING METHOD FOR NON-NYQUIST TRANSMISSION SYSTEM AND TRAINING DATA TRANSMISSION APPARATUS FOR NON-NYQUIST TRANSMISSION SYSTEM
6,154,490	08/543,056	13-Oct-1995	28-Nov-2000	SIGNAL PROCESSING APPARATUS
5,717,904	08/543,236	13-Oct-1995	10-Feb-1998	Apparatus and Methods for Automatically Controlling Block Writes
5,987,064	08/547,614	24-Oct-1995	16-Nov-1999	EYE PATTERN DISPLAY METHOD, EYE PATTERN DISPLAY APPARATUS, AND COMMUNICATIONS APPARATUS
5,896,576	08/568,049	06-Dec-1995	20-Apr-1999	Audio Mute for Digital Cordless Telephone
5,852,561	08/568,840	07-Dec-1995	22-Dec-1998	Selective Configuration of Operating Programs Stored in an Application Specific Integrated Circuit
5,781,132	08/587,755	17-Jan-1996	14-Jul-1998	Error Correcting Decoder
5,703,905	08/602,944	16-Feb-1996	30-Dec-1997	Multi-Channel Timing Recovery System
5,881,108	08/605,404	22-Feb-1996	09-Mar-1999	Adaptive Pre-Equalizer for Use in Data Communications Equipment
5,686,847	08/616,421	15-Mar-1996	11-Nov-1997	Reduced Sensitivity Power-On Reset Circuitry
5,689,513	08/624,055	29-Mar-1996	18-Nov-1997	DATA TRANSMISSION SYSTEM HAVING A BACKUP TESTING FACILITY
5,748,686	08/628,220	04-Apr-1996	05-May-1998	System and Method Producing Improved Frame Synchronization in a Digital Communication System
5,825,818	08/629,382	08-Apr-1996	20-Oct-1998	APPARATUS AND METHOD OF RECOVERING A TIMING SIGNAL IN A TRANSMISSION APPARATUS
5,867,560	08/653,185	24-May-1996	02-Feb-1999	Remote Hang Up Detector
6,121,087	08/665,783	18-Jun-1996	19-Sep-2000	Integrated Circuit Device with Embedded Flash Memory and Method for Manufacturing Same
5,742,679	08/699,269	19-Aug-1996	21-Apr-1998	Optimized Simultaneous Audio and Data Transmission Using QADM with Phase Randomization
5,764,074	08/715,234	18-Sep-1996	09-Jun-1998	System for, and Method of, Minimizing Noise in an Integrated Circuit Chip
5,940,610	08/720,891	03-Oct-1996	17-Aug-1999	Using Prioritized Interrupt Callback Routines to Process Different Types of Multimedia Information
5,870,438	08/721,149	26-Sep-1996	09-Feb-1999	Fast Resynchronization System for High-Speed Data Transmission
5,880,980	08/724,168	30-Sep-1996	09-Mar-1999	Distributed Decimation Sample Rate Conversion
5,909,384	08/726,187	04-Oct-1996	01-Jun-1999	System for Dynamically Adapting the Length of a Filter
5,654,862	08/732,752	18-Oct-1996	05-Aug-1997	Method and Apparatus for Coupling Multiple Independent On-

				Chip Vdd Busses to an ESD Core Clamp
6,002,724	08/748,346	14-Nov-1996	14-Dec-1999	TRANSMISSION APPARATUS AND CONTROL METHOD THEREFOR
5,834,953	08/756,203	25-Nov-1996	10-Nov-1998	High Speed Current Sense Amplifier
5,909,426	08/828,482	31-Mar-1997	01-Jun-1999	Orthogonal LMS Algorithms for Fast Line Echo Canceler Training
6,044,109	08/833,260	04-Apr-1997	28-Mar-2000	MODEM SIGNAL TRANSMITTER, MODEM SIGNAL TRANSMITTING METHOD, MODEM SIGNAL RECEIVER, MODEM SIGNAL RECEIVING METHOD, MODEM SIGNAL TRANSMITTING/RECEIVING SYSTEM AND MODEM SIGNAL TRANSMITTING/RECEIVING METHOD
5,916,301	08/838,899	11-Apr-1997	29-Jun-1999	Sample Rate Conversion for Synchronous Variable Rate
5,983,315	08/845,594	25-Apr-1997	09-Nov-1999	System and Method for Establishing Priorities in Transferring Data in Burst Counts From a Memory to a Plurality of Fifo Stages, Each Having a Low, Intermediate, and High Region
5,983,362	08/862,712	12-May-1995	09-Nov-1999	NON-INTERRUPTED OPERATION CONTROL APPARATUS FOR A MODULATOR-DEMODULATOR
6,229,812	08/872,530	11-Jun-1997	08-May-2001	SCHEDULING TECHNIQUES FOR DATA CELLS IN A DATA SWITCH
5,959,510	08/874,977	13-Jun-1997	28-Sep-1999	Second Order Filter with Low Sensitivity to Component Values and a Method for Designing the Same
5,974,478	08/888,910	07-Jul-1997	26-Oct-1999	System for Command Processing or Emulation in a Computer System, Such as Emulation of DMA Commands Using Burst Mode Data Transfer for Sound
5,922,052	08/912,547	18-Aug-1997	13-Jul-1999	Fast Ethernet Combination Chaining of Auto-Negotiations for Multiple Physical Layer Capability
5,996,104	08/924,146	05-Sep-1997	30-Nov-1999	System for Coding Signals
6,104,758	08/925,181	09-Sep-1997	15-Aug-2000	PROCESS AND SYSTEM FOR TRANSFERRING VECTOR SIGNAL WITH PRECODING FOR SIGNAL POWER REDUCTION
6,026,122	08/925,495	09-Sep-1997	15-Feb-2000	PROCESS AND SYSTEM FOR TRANSFERRING VECTOR SIGNAL WITH PRECODING FOR SIGNAL POWER REDUCTION
6,009,123	08/925,496	08-Sep-1997	28-Dec-1999	PROCESS AND SYSTEM FOR TRANSFERRING VECTOR SIGNAL WITH PRECODING FOR SIGNAL POWER REDUCTION
6,067,326	08/925,552	08-Sep-1997	23-May-2000	4X4 Dimensional Block Modulation Code for a PAM-5 Constellation
6,061,391	08/942,977	02-Oct-1997	09-May-2000	ERROR INDICATOR, AND DATA MODULATOR-DEMODULATOR AND INDICATING METHOD OF LIGHT EMITTER THEREIN
6,034,996	08/944,942	02-Oct-1997	07-Mar-2000	System and Method for Concatenating Reed-Solomon and Trellis Codes
6,282,246	08/956,553	23-Oct-1997	28-Aug-2001	FREQUENCY MODULATION METHOD AND MODEM UNIT EMPLOYING SUCH METHOD
6,069,893	08/959,056	28-Oct-1997	30-May-2000	ASYNCHRONOUS TRANSFER MODE SWITCHING ARCHITECTURES HAVING CONNECTION BUFFERS
6,097,763	08/962,046	31-Oct-1997	01-Aug-2000	MMSE Equalizer for DMT system with crosstalk
6,393,124	08/964,573	05-Nov-1997	21-May-2002	CPE Alert Signal Tone Detector
6,272,184	08/992,034	17-Dec-1997	07-Aug-2001	Non-Coherent Frequency Shift Keying Detection Scheme
6,243,425	09/005,645	12-Jan-1998	05-Jun-2001	Adaptive Precoding System & Method for Equalizing Communication Signal
6,023,493	09/009,228	20-Jan-1998	08-Feb-2000	Method and Apparatus for Synchronizing a Data Communication System to a Periodic Digital Impairment

6,327,666	09/014,155	27-Jan-1998	04-Dec-2001	System and Method for External Timing using a Complex Rotator
6,169,801	09/039,744	16-Mar-1998	02-Jan-2001	DIGITAL ISOLATION APPARATUS AND METHOD
5,949,218	09/045,276	20-Mar-1998	07-Sep-1999	Methods and Apparatus for Managing the Charging and Discharging of a Lithium Battery
6,198,723	09/060,228	14-Apr-1998	06-Mar-2001	Asynchronous Transfer Mode Traffic Shapers
6,349,098	09/062,301	17-Apr-1998	19-Feb-2002	METHOD AND APPARATUS FOR FORMING A VIRTUAL CIRCUIT
6,128,373	09/075,451	08-May-1998	03-Oct-2000	Programmable Data Access Arrangement Having Combined Command Signals
6,101,217	09/075,722	11-May-1998	08-Aug-2000	System and Method for Demodulating Digital Information from an Odd Constellation
6,100,834	09/079,965	15-May-1998	08-Aug-2000	Recursive Multi-Bit ADC with Predictor
6,100,937	09/087,736	29-May-1998	08-Aug-2000	Method and System for combining Multiple Images into a Single Higher-Quality Image
6,037,743	09/094,735	15-Jun-1998	14-Mar-2000	A Battery Charger and Power Source Employing an Environmental Energy Extractor and a Method Related Thereto
6,104,791	09/096,851	11-Jun-1998	15-Aug-2000	System and Method for Performing Telephone Line-in-use Detection, Extension Pick-up Detection, and Remote Hang-up Detection in a Modem
6,202,165	09/121,475	23-Jul-1998	13-Mar-2001	Photonic Clock Distribution Method and Apparatus for Electronic Systems
6,714,260	09/121,476	23-Jul-1998	30-Mar-2004	Monolithic Generation of RF for Wireless Transmission of Video
6,166,585	09/143,584	31-Aug-1998	26-Dec-2000	Methods and Apparatus for a High Efficiency Charge Pump
6,055,024	09/146,057	02-Sep-1998	25-Apr-2000	Adaptive Method for Y-C Separation in Video Signals
6,104,230	09/153,952	16-Sep-1998	15-Aug-2000	Electronic Inductor Circuit Using Cascoded Transistors
6,640,328	09/158,802	22-Sep-1998	28-Oct-2003	Method for Detecting Dropouts in Data Delivered Over a Bandwidth-Limited Bus
6,389,135	09/161,209	25-Sep-1998	14-May-2002	Method and Apparatus for Deriving Power From a Clock Signal Coupled Through Capacitors
6,414,990	09/162,493	29-Sep-1998	02-Jul-2002	Timing Recovery for a High Speed Digital Data Communication System Based on Adaptive Equalizer Impulse Response Characteristics
6,108,720	09/163,054	29-Sep-1998	22-Aug-2000	Echo Canceling Modem Which Dynamically Switches Sizes of Sample Buffer Between First Size for Quick Response Time and Second Size for Robustness to Interrupt Latency
6,421,757	09/163,776	30-Sep-1998	16-Jul-2002	Method and Apparatus for Controlling the Programming and Erasing of Flash Memory
6,381,266	09/164,155	30-Sep-1998	30-Apr-2002	Method and Apparatus for Identifying the Encoding Type of a Central Office Codec
6,263,429	09/164,256	30-Sep-1998	17-Jul-2001	Dynamic Microcode for Embedded Processors
6,442,178	09/164,329	01-Oct-1998	27-Aug-2002	System and Method for Data Alignment in a Communication System
6,434,233	09/164,530	30-Sep-1998	13-Aug-2002	Method and Apparatus for Canceling Periodic Interference Signals in a Digital Data Communication System
6,738,389	09/164,850	01-Oct-1998	18-May-2004	Circuit and Method for Performing Partial Parallel Data Transfer in a Communications System
6,535,522	09/164,886	01-Oct-1998	18-Mar-2003	Multiple Protocol Interface and Method for Use in a Communications System
6,192,498	09/164,921	01-Oct-1998	20-Feb-2001	System and Method for Generating Error Checking Data in a Communications System
6,427,179	09/164,969	01-Oct-1998	30-Jul-2002	System and Method for Protocol Conversion in a

				Communications System
6,163,845	09/165,781	02-Oct-1998	19-Dec-2000	Apparatus and Method for Power Management of Embedded Subsystems
6,064,267	09/167,093	05-Oct-1998	16-May-2000	Current Mirror Utilizing Amplifier to Match Operating Voltages of Input and Output Transconductance Devices
6,205,410	09/170,749	13-Oct-1998	20-Mar-2001	System and Method for Bit Loading with Optimal Margin Assignment
6,154,037	09/170,753	13-Oct-1998	28-Nov-2000	System and Method for Testing Distortion in Transformers
6,233,594	09/175,886	20-Oct-1998	15-May-2001	Decimation Filter Design for Over-Sampling Analog-to-Digital Converter OSADC
6,192,227	09/179,637	27-Oct-1998	20-Feb-2001	Method for Detecting Nonlinear Distortion Using Moment Invariants
6,449,664	09/192,616	16-Nov-1998	10-Sep-2002	Two-Dimensional Direct Memory Access in Image Processing Systems
6,288,794	09/192,617	16-Nov-1998	11-Sep-2001	Variable Sample Rate Converter Adaptable to Rational Ratios
6,314,475	09/192,627	16-Nov-1998	06-Nov-2001	Method and Apparatus for Monitoring, Controlling and Configuring Local Communication Devices
6,351,530	09/193,222	16-Nov-1998	26-Feb-2002	Modem Having a Digital High Voltage Isolation Barrier
6,330,597	09/193,304	17-Nov-1998	11-Dec-2001	Method and Apparatus for Monitoring, Controlling and Configuring Local Communication Devices
6,459,678	09/203,029	01-Dec-1998	01-Oct-2002	System and Method for Providing Near Optimal Bit Loading in a Discrete Multi-Tone Modulation System
6,052,037	09/209,235	10-Dec-1998	18-Apr-2000	MODULATION METHOD AND APPARATUS
6,516,004	09/209,801	11-Dec-1998	04-Feb-2003	HDLC Digital Data Transmission Protocol Controller
6,138,228	09/209,997	11-Dec-1998	24-Oct-2000	Protocol and Bus Link System Between Components of a Micro-Controller
5,949,819	09/213,961	17-Dec-1998	07-Sep-1999	Method and Apparatus for Training Linear Equalizers in a PCM Modem
6,192,109	09/219,914	23-Dec-1998	20-Feb-2001	Apparatus and Method for Improved DSL Communication
6,075,845	09/224,762	04-Jan-1999	13-Jun-2000	System and Method for Performing Telephone Line-in-use Detection, Extension Pick-up Detection, and Remote Hang-up Detection in a Modem
6,247,092	09/226,406	06-Jan-1999	12-Jun-2001	CONTROL OF COMMAND TRANSMISSION AND RECEPTION IN A DATA TRANSMISSION DEVICE AND DATA TRANSMISSION DEVICE THEREFOR
6,389,033	09/237,146	25-Jan-1999	14-May-2002	System and Method for Performing Signal Acceleration on an AC Link Bus
6,412,027	09/243,024	03-Feb-1999	25-Jun-2002	Direct Memory Access Controller Having On-Board Arbitration Circuitry
6,175,389	09/244,642	04-Feb-1999	16-Jan-2001	Comb Filtered Signal Separation
6,415,338	09/247,366	10-Feb-1999	02-Jul-2002	System for Writing a Data Value at a Starting Address to a Number of Consecutive Locations Equal to a Segment Length Identifier
6,453,365	09/247,407	10-Feb-1999	17-Sep-2002	Direct Memory Access Controller Having Decode Circuit for Compact Instruction Format
6,351,846	09/250,458	16-Feb-1999	26-Feb-2002	Information Server for Kernel and Passive Mode Systems
6,459,430	09/251,696	17-Feb-1999	01-Oct-2002	System and Method for Implementing Multi-Level Resolution Conversion Using Modified Linear Interpolation
6,172,556	09/261,981	04-Mar-1999	09-Jan-2001	FEEDBACK-CONTROLLED LOW VOLTAGE CURRENT SINK/SOURCE
6,300,824	09/265,480	09-Mar-1999	09-Oct-2001	Analog Offset Cancellation Method and Circuit and Amplifier Using Such Circuit

6,385,756	09/274,990	23-Mar-1999	07-May-2002	Selection of Functions Within an Integrated Circuit
6,449,057	09/282,949	01-Apr-1999	10-Sep-2002	Image Data Addressing for Bit Rotation Block within Digital Image Processors
6,456,389	09/282,956	01-Apr-1999	24-Sep-2002	Integrated Bit Rotation and Row Alignment Within Digital Image Printing Systems
6,636,328	09/283,876	01-Apr-1999	21-Oct-2003	Flexible Array Image Data Processing for Ink Jet Nozzle Data Control in Digital Image Printing Systems
7,265,873	09/290,854	13-Apr-1999	04-Sep-2007	Image Processor that Performs Edge Enhancement Using a Dynamic Threshold
6,211,719	09/294,131	19-Apr-1999	03-Apr-2001	Power Control Circuit for a Line Driver
6,424,541	09/295,562	21-Apr-1999	23-Jul-2002	Electronic Device Attachment Methods and Apparatus for Forming an Assembly
6,195,766	09/309,205	10-May-1999	27-Feb-2001	system and method for providing soft audio and soft modem copy protection for hardware interfaces and software code
6,647,114	09/312,218	14-May-1999	11-Nov-2003	Telephone Line Interface Circuit With Virtual Impedance
6,731,749	09/312,412	14-May-1999	04-May-2004	Telephone Line Interface Circuit With Intelligent Line Current Control
6,215,353	09/317,277	24-May-1999	10-Apr-2001	Stable Voltage Reference Circuit
6,370,671	09/335,975	18-Jun-1999	09-Apr-2002	Configurable Decoder and Method for Decoding a Reed-Solomon Codeword
6,412,090	09/336,626	18-Jun-1999	25-Jun-2002	Galois Field Computation System and Method
6,415,413	09/336,630	18-Jun-1999	02-Jul-2002	Configurable Reed-Solomon Controller and Method
6,556,622	09/339,353	23-Jun-1999	29-Apr-2003	Seamless Transition Between Communication Characteristics of Communication Devices
6,157,257	09/340,255	30-Jun-1999	05-Dec-2000	Low Power Folding Amplifier
6,445,734	09/343,873	30-Jan-1999	03-Sep-2002	Method of Validating Equalizer Training
7,046,382	09/345,269	30-Jun-1999	16-May-2006	v.34 hdx fallback to g3 facsimile for t.38 gateways
6,370,239	09/349,670	08-Jul-1999	09-Apr-2002	Standalone Telephone Answering Machine with Store and Forward Facsimile
6,243,414	09/360,252	23-Jul-1999	05-Jun-2001	METHOD AND APPARATUS FOR DATA TRANSMISSION
6,181,122	09/361,713	27-Jul-1999	30-Jan-2001	System and Method for Starting Voltage and Current Controlled Elements
6,118,265	09/361,801	27-Jul-1999	12-Sep-2000	Reference Voltage Stabilization System and Method for Fixing Reference Voltages, Independent of Sampling Rate
6,771,393	09/370,313	09-Aug-1999	03-Aug-2004	Transmission Rate Control for Continuous-Tone Facsimile
6,373,118	09/372,546	11-Aug-1999	16-Apr-2002	High-Value Integrated Circuit Resistor
6,490,637	09/379,994	24-Aug-1999	03-Dec-2002	Unique Serial Number Method for a System Device
6,407,611	09/382,298	24-Aug-1999	18-Jun-2002	System and Method for Providing Automatic Compensation of IC Design Parameters that Vary as a Result of Natural Process Variation
6,118,266	09/393,238	09-Sep-1999	12-Sep-2000	Low Voltage Reference With Power Supply Rejection Ratio
6,700,974	09/397,413	16-Sep-1999	02-Mar-2004	Switching Power Supply For Generating A Ringing Voltage
6,452,424	09/406,954	28-Sep-1999	17-Sep-2002	Method and Apparatus for Multiple Channel Signal Processing
6,278,387	09/407,465	28-Sep-1999	21-Aug-2001	Method of Slowing and Speeding the Playback of Recorded Audio Sound Without Changing the Pitch
6,650,698	09/408,367	29-Sep-1999	18-Nov-2003	Non-Linear Equalization for the Upstream Data Connection of 56K PCM Modems
6,388,467	09/408,455	30-Sep-1999	14-May-2002	High Voltage Tolerant Output Drive for Sustained Tri-State Signal Lines
6,768,720	09/409,360	30-Sep-1999	27-Jul-2004	Detection of Receive Event on Home Network to Verify Functional Media

6,359,972	09/409,368	30-Sep-1999	19-Mar-2002	Line in Use Detection
6,728,308	09/409,369	30-Sep-1999	27-Apr-2004	Variable Sample Rate Converter Adaptable to Rational Ratios
6,707,457	09/409,819	30-Sep-1999	16-Mar-2004	Microprocessor Extensions For Two-Dimensional Graphics Processing
6,532,511	09/409,820	30-Sep-1999	11-Mar-2003	Asynchronous Centralized Multi-Channel DMA Controller
6,590,938	09/409,823	30-Sep-1999	08-Jul-2003	DCT Domain Conversion of a Higher Definition Signal to Lower Definition Signal
6,550,046	09/411,417	01-Oct-1999	15-Apr-2003	Method for Automated Placement of Cells in an Integrated Circuit Layer
6,738,796	09/415,396	08-Oct-1999	18-May-2004	Optimization of Memory Requirements for Multi-Threaded Operating Systems
6,785,371	09/416,482	12-Oct-1999	31-Aug-2004	Signaling Mechanism for Modem Connection Holding and Reconnecting
6,434,633	09/432,141	02-Nov-1999	13-Aug-2002	Method and Apparatus For Facilitating AC-Link Communications Between A Controller and a Slow Peripheral of a Codec
6,529,975	09/432,434	02-Nov-1999	04-Mar-2003	Method and Apparatus for Addressing and Controlling Expansion Devices Through An AC-Link And A Codec
6,603,813	09/432,992	03-Nov-1999	05-Aug-2003	Video Signal Coding Systems and Processes Using Adaptive Quantization
6,898,243	09/439,920	12-Nov-1999	24-May-2005	Apparatus and Method for Down-Conversion Video De-Interlacing
6,236,273	09/442,583	18-Nov-1999	22-May-2001	High Efficiency Power Amplifier
6,564,045	09/461,647	14-Dec-1999	13-May-2003	A Broadband, Quadrature Frequency Doubler and Quadrupler
6,134,165	09/467,285	20-Dec-1999	17-Oct-2000	High Speed Sensing of Dual Port Static RAM Cell
6,581,164	09/476,849	03-Jan-2000	17-Jun-2003	System for Adjusting Clock Frequency Based Upon Amount of Unread Data Stored in Sequential Memory When Reading a New Line of Data Within a Field of Data
6,438,158	09/476,856	03-Jan-2000	20-Aug-2002	Method and Apparatus for Implementing Enhanced Multiple Modulus Conversion Techniques in a Signal Point Mapping Context
6,693,961	09/480,225	10-Jan-2000	17-Feb-2004	A System and Method of Video Frame Memory Reduction For Video Decoders
6,578,093	09/487,138	19-Jan-2000	10-Jun-2003	System for Loading a Saved Write Pointer into a Read Pointer of a Storage at Desired Synchronization Points within a Horizontal Video Line for Synchronizing Data
6,684,236	09/504,392	15-Feb-2000	27-Jan-2004	System of and Method For Efficiently Performing Computations Through Extended Booth Encoding Of the Operands Thereto
6,597,746	09/505,528	17-Feb-2000	22-Jul-2003	System and Method for Peak to Average Power Ratio Reduction
6,782,135	09/507,305	18-Feb-2000	24-Aug-2004	Apparatus and Method for Adaptive Digital Video Quantization
6,629,288	09/516,373	01-Mar-2000	30-Sep-2003	A Single Clock Cycle CRC Engine
6,268,815	09/517,979	03-Mar-2000	31-Jul-2001	High-Performance Switched-Capacitor Filter for Oversampling Sigma-Delta Digital to Analog Converters
6,711,207	09/523,637	13-Mar-2000	23-Mar-2004	System and Method for Reduced Power Consumption in Digital Subscriber Line Modems
6,662,321	09/532,346	21-Mar-2000	09-Dec-2003	Modem Using Phase Coherence to Adapt for Missing Transmission Samples
6,519,339	09/534,673	24-Mar-2000	11-Feb-2003	Method of Regulating Power Transfer Across an Isolation Barrier
6,307,408	09/543,201	05-Apr-2000	23-Oct-2001	Method and Apparatus for Powering Down a Line Driver
6,704,399	09/557,233	24-Apr-2000	09-Mar-2004	Quick Connect Parameter Exchange
6,301,341	09/573,040	17-May-2000	09-Oct-2001	System and Method for Performing Telephone Line-in-use

				Detection, Extension Pick-up Detection, and Remote Hang-up Detection in a Modem
6,763,470	09/575,489	19-May-2000	13-Jul-2004	System and Method for Dynamically Amplifying a Delayed Analog Signal Based on Amplitude Information Obtained From Its Digital Representation
6,297,759	09/578,980	25-May-2000	02-Oct-2001	Digital-to-Analog Converter with High-Speed Output
7,069,328	09/579,309	25-May-2000	27-Jun-2006	System and Method to Interface a Local Area Network with a Wide Area Network
6,874,041	09/588,830	07-Jun-2000	29-Mar-2005	Autoconfiguration of Communication Device Input or Output Terminal
6,968,020	09/588,992	06-Jun-2000	22-Nov-2005	Frequency Synthesis Scheme to Avoid Gaps and VCO Pulling in DBS Systems
6,731,726	09/592,707	13-Jun-2000	04-May-2004	Communication on Hold
6,642,859	09/611,294	07-Jul-2000	04-Nov-2003	Calibrating for Errors in a Plurality of Signal Sources Such as Current or Voltage Sources
6,526,109	09/615,627	05-Dec-2000	25-Feb-2003	Method and Apparatus for Hybrid Smart Center Loop for Clock Date Recovery
6,427,011	09/616,097	14-Jul-2000	30-Jul-2002	Modem Data Access Arrangement Without Diode Bridge
6,714,645	09/618,336	18-Jul-2000	30-Mar-2004	Modem DAA Without Hookswitch Relay
6,647,109	09/620,871	21-Jul-2000	11-Nov-2003	Network Telephony
6,647,100	09/626,057	26-Jul-2000	11-Nov-2003	Universal Serial Bus Data Pump Command Interpreter
6,975,585	09/626,435	27-Jul-2000	13-Dec-2005	Slotted Synchronous Frequency Division Multiplexing for Multi-Drop Networks
6,813,743	09/629,122	31-Jul-2000	02-Nov-2004	A Sliding Window Technique for Map Decoders
6,795,547	09/630,845	02-Aug-2000	21-Sep-2004	Full Duplex Speaker-Phone Providing Increased Loop Stability
7,035,323	09/632,029	02-Aug-2000	25-Apr-2006	System and Method for Combining Multiple Digital Subscriber Line Transceivers
6,865,337	09/634,265	08-Aug-2000	08-Mar-2005	System and Method for Detecting Modifications of Video Signals Designed to Prevent Copying by Traditional Video Tape Recorders
6,728,401	09/641,406	17-Aug-2000	27-Apr-2004	Red Eye Removal Using Digital Color Image Processing
6,697,873	09/643,567	22-Aug-2000	24-Feb-2004	High Speed MAC Address Search Engine
6,690,776	09/644,806	23-Aug-2000	10-Feb-2004	Communication On Hold
6,282,235	09/657,593	08-Sep-2000	28-Aug-2001	SIGNAL PROCESSING APPARATUS
6,353,347	09/663,280	15-Sep-2000	05-Mar-2002	Power Control Circuit for a Line Driver
6,518,604	09/667,364	21-Sep-2000	11-Feb-2003	Diode With Variable Width Metal Stripes for Improved Protection Against Electrostatic Discharge (ESD) Current Failure
6,766,383	09/672,160	27-Sep-2000	20-Jul-2004	Packet-Based Direct Memory Access
6,643,109	09/672,165	27-Sep-2000	04-Nov-2003	Fully Synthesizable and Highly Area Efficient Very Large Scale Integration (VLSI) Electrostatic Discharge (ESD) Protection Circuit
6,741,162	09/678,983	04-Oct-2000	25-May-2004	Power Line Networking Apparatus and Method
6,373,377	09/679,414	05-Oct-2000	16-Apr-2002	Power Supply with Digital Data Coupling for Power-Line Networking
6,651,187	09/683,752	11-Feb-2002	18-Nov-2003	System and Method for Determining Fault Path Behavior
6,961,779	09/683,762	11-Feb-2002	01-Nov-2005	Method for Robust Parsing of Multiple-Frame Protocol Messages Segmented on an Arbitrary Boundary
6,952,430	09/683,763	12-Feb-2002	04-Oct-2005	System and Method for Interfacing a Data Link Protocol Engine and Physical Layer
6,775,305	09/691,933	19-Oct-2000	10-Aug-2004	System and Method for Combining Multiple Physical Layer Transport Links

6,718,419	09/702,468	31-Oct-2000	06-Apr-2004	System and Method for Extending the Number of Addressable Physical Devices on a Data Bus
6,967,996	09/732,456	06-Dec-2000	22-Nov-2005	System and Method for Optimizing Digital Subscriber Line Performance between Transceivers from Multiple Manufacturers
6,427,210	09/734,522	11-Dec-2000	30-Jul-2002	Apparatus and Method for Power Management of Embedded Subsystems
6,473,870	09/734,848	11-Dec-2000	29-Oct-2002	System and Method for Providing Soft Audio and Soft Modem Copy Protection for Hardware Interfaces and Software Code
6,353,361	09/746,756	20-Dec-2000	05-Mar-2002	Fully-Differential Two-Stage Operation Amplifier with Gain-Boosting in the First Stage
6,343,851	09/746,965	21-Dec-2000	05-Feb-2002	K Replacement for Color Digital Image Printing
6,476,675	09/747,470	20-Dec-2000	05-Nov-2002	A Double-Cascode Two-Stage Operational Amplifier
7,002,916	09/753,797	02-Jan-2001	21-Feb-2006	ASYNCHRONOUS TRANSFER MODE TRAFFIC SHAPERS
6,868,095	09/756,570	08-Jan-2001	15-Mar-2005	Control Channel Implementation in a Packet Switched Communications Network
6,882,684	09/756,793	10-Jan-2001	19-Apr-2005	AUTOMATIC GAIN CONTROL CIRCUIT, DATA COMMUNICATION DEVICE AND AUTOMATIC GAIN CONTROL METHOD
6,400,782	09/758,536	10-Jan-2001	04-Jun-2002	A Method of Frequency Domain Filtering Employing a Real To Analytic Transform
6,757,398	09/765,741	19-Jan-2001	29-Jun-2004	System and Method for Sensing a Sound System
6,665,021	09/776,970	05-Feb-2001	16-Dec-2003	System and Process for Filtering Single Tone Signals
6,718,355	09/777,622	05-Feb-2001	06-Apr-2004	Systems and Methods for a Partial Sum Digital Fir Filter
7,002,978	09/780,054	09-Feb-2001	21-Feb-2006	SCHEDULING TECHNIQUES FOR A DATA CELL IN A DATA SWITCH
6,437,720	09/785,690	16-Feb-2001	20-Aug-2002	Code Independent Charge Transfer Scheme for Switched-Capacitor Digital-to-Analog Converter
6,990,529	09/792,152	22-Feb-2001	24-Jan-2006	Unified Algorithm for Frame Scheduling and Buffer Management in Differentiated Services Network
6,954,424	09/794,184	26-Feb-2001	11-Oct-2005	Credit Based Pacing Scheme for Heterogeneous Speed Frame Forwarding
7,142,514	09/808,652	15-Mar-2001	28-Nov-2006	Bandwidth Sharing Using Emulated Weighted Fair Queuing (EFWQ)
6,534,996	09/816,506	23-Mar-2001	18-Mar-2003	System and Method for Phone Line Characterization By Time Domain Reflectometry
6,664,841	09/816,621	23-Mar-2001	16-Dec-2003	Method and Apparatus for Conditioning an Analog Signal
6,856,607	09/817,827	26-Mar-2001	15-Feb-2005	System and Method for Detecting Valid Indicator Signals
7,120,683	09/824,951	03-Apr-2001	10-Oct-2006	Single Switch Image for a Stack of Switches
RE38,678	09/849,500	04-May-2001	28-Dec-2004	Method and Apparatus for Synchronizing a Data Communications System to a Periodic Digital Impairment
6,614,374	09/855,080	14-May-2001	02-Sep-2003	High-Performance Switched-Capacitor Filter for Oversampling Sigma-Delta Digital to Analog Converters
7,016,490	09/862,689	21-May-2001	21-Mar-2006	Circuit Board Capacitor Structure for Forming a High Voltage Isolation Barrier
6,593,828	09/862,919	22-May-2001	15-Jul-2003	System and Method for Filter Tuning
6,879,625	09/863,164	21-May-2001	12-Apr-2005	System and Method for Providing Cancellation of Interference in a Repeater Configuration with Remote Loop Powering
8,356,334	09/866,259	25-May-2001	15-Jan-2013	DATA NETWORK NODE HAVING ENHANCED SECURITY FEATURES
6,854,059	09/876,454	06-Jun-2001	08-Feb-2005	Method and Apparatus for Medium Access Control in Powerline Communication Network Systems
6,728,942	09/879,142	12-Jun-2001	27-Apr-2004	Method and Systems for Predictive MOSFET Layout Generation

				with Reduced Design Cycle
6,798,870	09/898,640	03-Jul-2001	28-Sep-2004	Non Real-Time Facsimile System for a Computing Device
6,775,722	09/899,426	05-Jul-2001	10-Aug-2004	Efficient Data Retrieval from Input Coupling Queues
7,061,976	09/910,289	19-Jul-2001	13-Jun-2006	Multiple Symbol Rate Burst Equalizer Training
7,016,371	09/912,283	24-Jul-2001	21-Mar-2006	Multi-Mode Adaptive Filter
6,999,416	09/935,533	23-Aug-2001	14-Feb-2006	Buffer Management for Support of Quality of Service Guarantees and Data Flow Control in Data Switching
6,588,002	09/941,883	28-Aug-2001	01-Jul-2003	Method and System for Predictive Layout Generation for Inductors With Reduced Design Cycle
6,950,461	09/961,839	24-Sep-2001	27-Sep-2005	MODEMS UTILIZING LOW DENSITY PARITY CHECK CODES
7,082,138	09/966,691	28-Sep-2001	25-Jul-2006	Internal Communication Protocol for Data Switching Equipment
6,952,458	09/969,323	02-Oct-2001	04-Oct-2005	Demapping System and Method
7,257,134	09/969,702	03-Oct-2001	14-Aug-2007	Method of Pacing the Synchronization of Routing Information in a Data Switching Environment
6,586,992	09/971,400	05-Oct-2001	01-Jul-2003	System and Method for Increasing Power Range in a Power Amplifier Using a Single Voltage Supply
7,068,592	09/997,662	29-Nov-2001	27-Jun-2006	System and Method for Increasing Payload Capacity by Clustering Unloaded Bins in a Data Transmission System
6,534,854	10/005,564	08-Nov-2001	18-Mar-2003	A Pin Grid Array Package With Controlled Impedance Pins
6,851,046	10/011,183	13-Nov-2001	01-Feb-2005	Jumping to a Recombine Target Address Which is Encoded in a Ternary Branch Instructions
7,006,623	10/016,194	02-Nov-2001	28-Feb-2006	Circuit for Reducing Voltage Peak in Interfacing With a Telephone Line
6,856,953	10/021,064	19-Dec-2001	15-Feb-2005	Method and System for Testing Algorithm Compliancy
6,839,887	10/029,476	24-Oct-2001	04-Jan-2005	Method and System for Predictive Multi-Component Circuit Layout Generation with Reduced Design Cycle
6,693,998	10/034,682	29-Dec-2001	17-Feb-2004	Error Correction and Compression Parameter Exchange During Modem Training Phase
7,116,788	10/054,359	17-Jan-2002	03-Oct-2006	Efficient Head Related Transfer Function Filter Generation
7,263,109	10/063,005	11-Mar-2002	28-Aug-2007	Clock Skew Compensation for a Jitter Buffer
6,667,561	10/063,036	13-Mar-2002	23-Dec-2003	Integrated Circuit Package Capable of Operating in Multiple Orientations
7,103,008	10/064,340	02-Jul-2002	05-Sep-2006	Communications System Using Rings Architecture
7,418,616	10/064,445	15-Jul-2002	26-Aug-2008	Improved synchronous data access
7,106,860	10/068,610	06-Feb-2002	12-Sep-2006	A System and Method for Executing Advanced Encryption Standard (AES) Algorithm
6,618,462	10/077,114	15-Feb-2002	09-Sep-2003	Digital Frequency Divider
7,042,875	10/090,865	04-Mar-2002	09-May-2006	System and Method for High-Speed Switching
7,206,323	10/093,324	06-Mar-2002	17-Apr-2007	INTERFACING 622.08 MHz LINE INTERFACE TO A 77.76 MHz SONET FRAMER
7,342,927	10/094,931	11-Mar-2002	11-Mar-2008	Systems and Methods for Transferring Various Data Types Across an ATM Network
6,711,245	10/099,582	15-Mar-2002	23-Mar-2004	Method and Apparatus for Processing Call Progress of a Serial Communication Device
7,330,544	10/137,601	29-Apr-2002	12-Feb-2008	Digital Isolation Barrier as Interface Bus for Modems
6,608,571	10/140,624	07-May-2002	19-Aug-2003	System and Method for Communicating Over a One-Wire Bus
6,538,512	10/198,848	18-Jul-2002	25-Mar-2003	A Double-Cascode Two-Stage Operational Amplifier
6,662,246	10/218,184	12-Aug-2002	09-Dec-2003	Two-Dimensional Direct Memory Access to Maximize Processing Resources in Image Processing Systems
8,504,479	10/230,054	29-Aug-2002	06-Aug-2013	Key Interface for Secure Object Manipulation

7,912,155	10/250,353	29-Oct-2003	22-Mar-2011	Demodulator Circuit
6,803,849	10/284,624	31-Oct-2002	12-Oct-2004	Solid State Inducting Device
7,835,265	10/284,856	31-Oct-2002	16-Nov-2010	High Availability Ethernet Backplane Architecture
7,246,266	10/303,317	21-Nov-2002	17-Jul-2007	Method and Apparatus for Fireware Restoration in Modems
7,397,809	10/319,304	13-Dec-2002	08-Jul-2008	Scheduling Method for Combined Unicast and Multicast Queuing
7,031,414	10/322,568	19-Dec-2002	18-Apr-2006	Combined Feedforward Filter for a Decision Feedback Equalizer
6,925,627	10/325,618	20-Dec-2002	02-Aug-2005	Method and Apparatus for Power Routing in an Integrated Circuit
7,260,066	10/326,352	20-Dec-2002	21-Aug-2007	Apparatus for Link Failure Detection on High Availability Ethernet Backplane
8,027,473	10/340,651	13-Jan-2003	27-Sep-2011	System and Method for Improved Data Protection in PONs
6,985,566	10/352,754	28-Jan-2003	10-Jan-2006	Point of Sale for High-Speed Communications
7,257,165	10/418,748	17-Apr-2003	14-Aug-2007	Method and Apparatus for Preamble Detection and Time Estimation in OFDM Communication Systems
7,450,604	10/422,014	21-Apr-2003	11-Nov-2008	Method And Apparatus For Establishing Circuit Connections Over Local Area Networks With Frequency Selective Impairments
7,724,637	10/422,070	21-Apr-2003	25-May-2010	Method and Appartus for Controlled Spectrum Multi-Carrier Modulation
7,522,638	10/430,278	07-May-2003	21-Apr-2009	Method and system for preventing data loss in a Real-Time Computer System
6,864,518	10/438,664	14-May-2003	08-Mar-2005	Bit cells having offset contacts in a memory array
7,336,683	10/464,361	17-Jun-2003	26-Feb-2008	Efficient communication system for reliable frame transmission over broad SNR ranges
7,257,162	10/600,163	19-Jun-2003	14-Aug-2007	Hypothetical Reference Decoder for Compressed Image and Video
6,842,509	10/607,244	25-Jun-2003	11-Jan-2005	Signaling Mechanism for Modem Connection Holding and Reconnecting
7,925,013	10/611,402	30-Jun-2003	12-Apr-2011	System for Data Encryption and Decryption of Digital Data Entering and Leaving Memory
7,733,878	10/614,109	08-Jul-2003	08-Jun-2010	System and Method for Packet Transmission From Fragmented Buffer
7,117,321	10/614,219	08-Jul-2003	03-Oct-2006	System and Method for Interleaving SDRAM Device Access Requests
7,535,920	10/614,339	08-Jul-2003	19-May-2009	Method and System for Optimizing UTOPIA CLAV Polling Arbitration
7,298,758	10/615,613	08-Jul-2003	20-Nov-2007	Method and System for Optimizing UTOPIA CLAV Polling Arbitration
6,770,493	10/619,451	16-Jul-2003	03-Aug-2004	Integrated Circuit Package Capable of Operating in Multiple Orientations
7,372,899	10/626,771	25-Jul-2003	13-May-2008	Reduced Symbol Rate Handshake Signaling in ADSL Systems
6,927,458	10/637,324	08-Aug-2003	09-Aug-2005	Ballasting MOSFETs Using Staggered, Segmented Diffusions
6,922,467	10/640,102	12-Aug-2003	26-Jul-2005	Quick Connect Parameter Exchange
8,028,298	10/642,309	18-Aug-2003	27-Sep-2011	System and Methods for Managing Shared Resources in a Computer System
7,346,118	10/644,023	20-Aug-2003	18-Mar-2008	Method and apparatus for generating a 64 state 4-D trellis code in DMT
7,532,670	10/655,698	05-Sep-2003	12-May-2009	Hypothetical Reference Decoder With Low Start-Up Delays for Compressed Image and Video
7,315,515	10/676,900	30-Sep-2003	01-Jan-2008	TCP Acceleration System
7,881,461	10/697,819	31-Oct-2003	01-Feb-2011	System and Apparatus for Conveying Bidirectional Data, Power

				and Timing Signals Using a Single Transformer
	10/705,909	13-Nov-2003		Method and System for Attaching a USB Network Adapter Supporting Both RNDIS and Non-RNDIS Capable Operating Systems
7,272,172	10/714,907	18-Nov-2003	18-Sep-2007	System and Method for Selectable Mask for LDSL
7,716,668	10/735,678	16-Dec-2003	11-May-2010	System and Method for Scheduling Thread Execution
7,135,366	10/738,256	17-Dec-2003	14-Nov-2006	Method for Fabricating a Lateral Metal-Insulator-Metal Capacitor
7,643,413	10/746,235	29-Dec-2003	05-Jan-2010	System and Method for Providing Quality of Service in Asynchronous Transfer Mode Cell Transmission
7,098,961	10/746,921	23-Dec-2003	29-Aug-2006	Horizontal Sync Pulse Detection Without Amplitude Assumption
7,088,275	10/749,570	31-Dec-2003	08-Aug-2006	Variable Clock Rate Analog-to-Digital Converter
7,068,202	10/749,571	31-Dec-2003	27-Jun-2006	Architecture for an Analog-to-Digital Converter
7,403,760	10/751,013	31-Dec-2003	22-Jul-2008	DC Offset Correction for Direct Conversion Receiver
7,227,585	10/776,075	11-Feb-2004	05-Jun-2007	Luminance and Chrominance Separation System
7,167,213	10/776,121	11-Feb-2004	23-Jan-2007	Comb Filter Design for Separating Luminance and Chrominance in Video Signals
7,760,719	10/881,226	30-Jun-2004	20-Jul-2010	Combined Pipelined Classification and Address Search Method and Apparatus for Switching Environments
7,974,272	10/901,873	29-Jul-2004	05-Jul-2011	Remote Control of a Switching Node in a Stack of Switching Nodes
7,813,263	10/903,437	30-Jul-2004	12-Oct-2010	Methods and Apparatus Providing Rapid End-to-End Failover in a Packet Switched Communications Network
7,376,075	10/949,861	24-Sep-2004	20-May-2008	Circular Constellations with Coherent Gain/Differential Phase and Pilots
7,746,606	10/990,641	16-Nov-2004	29-Jun-2010	ESD Protection for Integrated Circuits Having Ultra Thin Gate Oxides
7,759,967	11/031,891	07-Jan-2005	20-Jul-2010	General Purpose Pin Mapping For A General Purpose Application Specific Integrated Circuit (ASIC)
7,484,070	11/032,456	10-Jan-2005	27-Jan-2009	Selective Memory Block Remapping
7,447,375	11/032,458	10-Jan-2005	04-Nov-2008	Compensation For Exposure Time Variation When Scanning With CCD Sensors
7,535,606	11/032,478	10-Jan-2005	19-May-2009	CIS (contact image sensor) RYB (red-luminance-blue) Sampling
7,716,512	11/032,578	10-Jan-2005	11-May-2010	Real Time Clock Content Validation
7,180,823	11/032,604	10-Jan-2005	20-Feb-2007	Flexible SDRAM Clocking (MS-DLL)
7,401,880	11/032,605	10-Jan-2005	22-Jul-2008	Combination of A/D Inputs With Quadrature Detection To Give Higher Resolution Positioning
7,453,600	11/032,615	10-Jan-2005	18-Nov-2008	Text Region Identification And Enhancement During Color Copy
7,309,117	11/032,616	10-Jan-2005	18-Dec-2007	Multi-Valued Raster Elements As A Technique To Reduce The Memory Requirements And Computation Complexity Of Advanced Ink Jet Systems
7,441,850	11/032,633	10-Jan-2005	28-Oct-2008	Generic Ink Jet Head Fire Logic
7,667,870	11/032,635	10-Jan-2005	23-Feb-2010	White Region Identification To Accelerate Image Processing For Copies
7,675,127	11/107,206	15-Apr-2005	09-Mar-2010	MOSFET Having Increased Snap-Back Conduction Uniformity
7,386,040	11/117,252	28-Apr-2005	10-Jun-2008	System and Method for Optimizing Digital Subscriber Line Performance between Transceivers from Multiple Manufacturers
	11/139,370	26-May-2005		Method for the Fast Exploration of Bus-Based Communication Architectures at the Cycle-Count-Accurate-at-Transaction-Boundaries Abstraction

7,590,674	11/173,329	30-Jun-2005	15-Sep-2009	Method and Apparatus for Generating a Random Bit Stream
7,564,928	11/174,359	30-Jun-2005	21-Jul-2009	System and Method of Frequency Synthesis to Avoid Gaps and VCO Pulling in Direct Broadcast Satellite Systems
7,216,267	11/224,328	13-Sep-2005	08-May-2007	Systems and Methods for Multistage Signal Detection in MIMO Transmissions and Iterative Detection of Precoded OFDM
8,270,002	11/305,406	15-Dec-2005	18-Sep-2012	Printing Digital Images With Rotation
7,750,976	11/323,337	30-Dec-2005	06-Jul-2010	Automatic Video Detector
8,072,547	11/395,023	31-Mar-2006	06-Dec-2011	Comb Filter that Utilizes Host Memory
7,590,163	11/437,311	19-May-2006	15-Sep-2009	Spread Spectrum Clock Generation
	11/438,009	19-May-2006		Modem Call Forwarding and Three-Way Calling Features for VoIP Applications
	11/438,011	19-May-2006		Inbound Noise Reduction for VoIP Applications
7,586,644	11/512,905	30-Aug-2006	08-Sep-2009	Color Conversion System and Method
7,590,248	11/514,028	30-Aug-2006	15-Sep-2009	Head Related Transfer Function Filter Generation
7,254,134	11/534,760	25-Sep-2006	07-Aug-2007	Systems for Transferring Various Data Types Across An ATM Network
7,778,618	11/617,054	28-Dec-2006	17-Aug-2010	Systems and Methods for Reducing Broadband Image Noise Through a Programmable Bandwidth RF Variable Gain Attenuator
7,730,360	11/647,784	29-Dec-2006	01-Jun-2010	CDC-Compliant Embedded USB Controller Communication Device And System With Custom Features Support
7,876,977	11/703,068	06-Feb-2007	25-Jan-2011	Image Processor and Method of Image Rotation
	11/713,393	02-Mar-2007		Method and System For Remote Facsimile Transmission
7,474,357	11/731,660	30-Mar-2007	06-Jan-2009	Decoding Video Signals to Separate Luma and Chroma Components
7,496,135	11/849,618	04-Sep-2007	24-Feb-2009	System and Method for Selectable Mask for LDSL
7,701,301	11/853,394	11-Sep-2007	20-Apr-2010	Systems for Implementing a Temperature and Process Compensated Two-Stage Ring Oscillator
7,990,902	11/871,515	12-Oct-2007	02-Aug-2011	Facilitating Transmissions in a Plurality of Protocols
	11/930,715	31-Oct-2007		Set/Reset Mechanism
8,085,698	11/930,723	31-Oct-2007	27-Dec-2011	Piggyback Acknowledgement
8,155,139	11/931,172	31-Oct-2007	10-Apr-2012	Systems and Methods for Management of Wireless Clients
	11/947,946	30-Nov-2007		Systems and Methods for Coexistence of WLAN and Bluetooth Networks
	11/954,579	12-Dec-2007		Systems and Methods for Retrieving Buffered Data from an Access Point
	11/956,756	14-Dec-2007		Acoustic Substrate
7,701,374	12/037,272	26-Feb-2008	20-Apr-2010	Method and Apparatus for Automatic Optimal Sampling Phase Detection
8,165,154	12/046,391	11-Mar-2008	24-Apr-2012	Systems and Methods for Reliable Broadcast and Multicast Transmission Over a Wireless Local Area Network
8,089,908	12/046,946	12-Mar-2008	03-Jan-2012	Systems and Methods for Indicating Buffered Data at an Access Point Using a Traffic Indication Map Broadcast
8,170,002	12/047,021	12-Mar-2008	01-May-2012	Systems and Methods for Indicating Buffered Data at an Access Point with Efficient Beacon Handling
8,233,414	12/047,210	12-Mar-2008	31-Jul-2012	Systems and Methods for Indicating Buffered Data at an Access Point Using an Embedded Traffic Indication Map
	12/051,475	19-Mar-2008		Flexible Cadence Follower for Film on Video
8,022,490	12/054,169	24-Mar-2008	20-Sep-2011	Micro Electro-Mechanical Sensor (MEMS) Fabricated with Ribbon Wire Bonds
7,907,660	12/072,328	26-Feb-2008	15-Mar-2011	System and Method for Television Signal Demodulation and

				Decoding
8,072,993	12/106,252	18-Apr-2008	06-Dec-2011	Systems and Methods for Communicating to a Disassociated Station in a Protected Network
	12/106,260	18-Apr-2008		Systems and Methods for Multicast Retransmission Over a Secure Wireless LAN
8,144,676	12/106,878	21-Apr-2008	27-Mar-2012	Network Allocation
8,111,677	12/108,759	24-Apr-2008	07-Feb-2012	Systems and Methods of Combined Bluetooth and WLAN Signaling
8,077,647	12/110,888	28-Apr-2008	13-Dec-2011	Systems and Methods for Utilizing a Scanning Threshold to Reduce Power Consumption
8,335,663	12/126,345	23-May-2008	18-Dec-2012	Signal Acquisition System and Method
8,411,847	12/136,436	10-Jun-2008	02-Apr-2013	Acoustic Echo Canceller
7,907,557	12/138,923	13-Jun-2008	15-Mar-2011	Low Power Receiving
7,915,950	12/143,546	20-Jun-2008	29-Mar-2011	Method and Algorithm of High Precision On-Chip Global Biasing Using Integrated Resistor Calibration Circuits
7,733,252	12/145,133	24-Jun-2008	08-Jun-2010	Method and Apparatus for Delay and Combining Circuitry
	12/152,753	16-May-2008		Method and System for Dynamic Audio Stream Redirection
7,710,299	12/157,436	09-Jun-2008	04-May-2010	System and Method Providing Channel Multiplexing for Analog-to-Digital Conversion
	12/165,590	30-Jun-2008		Method and System for Removal of Clicks and Noise in a Redirected Audio Stream
7,983,306	12/178,179	23-Jul-2008	19-Jul-2011	Method and Apparatus for Channelizing Sampled Broadband Signal
7,852,868	12/265,391	05-Nov-2008	14-Dec-2010	Method And Apparatus For Establishing Circuit Connections Over Local Area Networks With Frequency Selective Impairments
8,270,896	12/298,512	23-Jun-2009	18-Sep-2012	Systems and Method for Frequency Based Satellite Channel Scanning
8,325,940	12/339,635	19-Dec-2008	04-Dec-2012	Power Management Controller for Drivers
8,274,772	12/341,753	22-Dec-2008	25-Sep-2012	Current Detection And Limiting Method and Apparatus
8,243,952	12/341,777	22-Dec-2008	14-Aug-2012	Microphone Array Calibration Method and Apparatus
8,213,809	12/358,971	23-Jan-2009	03-Jul-2012	Universal Systems and Methods for Determining an Incoming Carrier Frequency and Decoding an Incoming Signal
8,013,440	12/365,101	03-Feb-2009	06-Sep-2011	Enhanced Thermal Dissipation Ball Grid Array Package
	12/371,406	13-Feb-2009		Systems and Methods for Network Facsimile Transmissions
8,160,153	12/384,180	31-Mar-2009	17-Apr-2012	Hypothetical Reference Decoder With Low Start Up Delays for Compressed Image and Video
7,982,525	12/390,333	20-Feb-2009	19-Jul-2011	Systems and Methods for Driving High Power States Using Lower Voltage Processes
8,126,038	12/392,039	24-Feb-2009	28-Feb-2012	System and Method for Selectable Mask for LDSL
8,228,951	12/427,698	21-Apr-2009	24-Jul-2012	Method and System for Preventing Data Loss in a Real-Time Computer System
	12/469,647	20-May-2009		Systems and Methods for Low-Latency Encrypted Storage
8,514,532	12/487,528	18-Jun-2009	20-Aug-2013	Electrostatic Discharge Protection Circuit
7,920,078	12/488,322	19-Jun-2009	05-Apr-2011	Systems and Methods for Variable Rate Conversion
8,063,699	12/488,438	19-Jun-2009	22-Nov-2011	Anti-Pop Circuit
	12/497,241	02-Jul-2009		Systems and Methods of Improved Heat Dissipation with Variable Pitch Grid Array Packaging
8,363,082	12/539,526	11-Aug-2009	29-Jan-2013	Systems and Methods for Alignment of Laser Printers
8,355,570	12/540,330	12-Aug-2009	15-Jan-2013	Systems and Methods for Raster-to-Block Converter
7,952,508	12/553,004	02-Sep-2009	31-May-2011	Noise-Shaped Scrambler For Reduced Out-of-Band Common-

				Mode Interference
7,933,045	12/553,749	03-Sep-2009	26-Apr-2011	Color Conversion System and Method
8,402,073	12/559,053	14-Sep-2009	19-Mar-2013	Method and Apparatus for Generating A Random Bit Stream
8,044,690	12/574,663	06-Oct-2009	25-Oct-2011	System and Method for Clock-Synchronized Triangular Waveform Generation
8,411,877	12/578,477	13-Oct-2009	02-Apr-2013	Tuning and DAC Selection of High-Pass Filters For Audio Codecs
8,477,949	12/579,200	14-Oct-2009	02-Jul-2013	2.1 Crossover Equalization In PC Audio Applications
8,344,797	12/623,202	20-Nov-2009	01-Jan-2013	Systems and Methods for Offset Cancellation Method For DC-Coupled Audio Drivers
	12/684,829	08-Jan-2010		Systems and Methods for Echo Cancellation and Echo Suppression
8,064,101	12/696,301	29-Jan-2010	22-Nov-2011	White Region Identification To Accelerate Image Processing For Copies
	12/712,108	24-Feb-2010		Speaker Distortion Reduction System and Method
8,455,990	12/713,141	25-Feb-2010	04-Jun-2013	Systems and Methods of Tamper Proof Packaging of a Semiconductor Device
8,505,012	12/759,125	13-Apr-2010	06-Aug-2013	System and Method for Scheduling Threads Requesting Immediate CPU Resource in the Indexed Time Slot
8,301,828	12/792,592	02-Jun-2010	30-Oct-2012	Systems and Methods for Reliable Multi-Level Cell Flash Storage
	12/814,425	11-Jun-2010		Systems and Methods for Creating Immersion Surround Sound and Virtual Speakers Effects
8,517,487	12/820,932	22-Jun-2010	27-Aug-2013	Systems and Methods for Dielectric Heating of Ink in Inkjet Printers
	12/828,565	01-Jul-2010		Systems and Methods for Transducer Calibration and Tuning
	12/828,640	01-Jul-2010		High-Bandwidth Linear Current Mirror
8,526,635	12/828,716	03-Sep-2013	03-Sep-2013	Grounding Switch Method and Apparatus
	12/833,606	09-Jul-2010		Systems and Methods for Generating Phantom Base
	12/839,771	20-Jul-2010		Method and System for Automatically Detecting and Processing Halftone Regions in Scanned Documents
8,477,394	12/851,343	05-Aug-2010	02-Jul-2013	Systems and Methods for Color Defringing
	12/852,353	06-Aug-2010		Systems and Methods for Heat Dissipation Using Thermal Conduits
8,212,608	12/860,791	20-Aug-2010	03-Jul-2012	Apparatus and Method for a Smooth Powerup of a Reference Signal
	12/872,733	31-Aug-2010		Processor Nand Flash Boot System and Method
	12/878,796	09-Sep-2010		Adaptive High Dynamic Range Surface Capacitive Touchscreen Controller
	12/925,828	29-Oct-2010		Systems and Methods for Improved Heat Dissipation in Semiconductor Packages
	12/927,549	16-Nov-2010		Semiconductor Packages with Reduced Solder Voiding
	12/931,240	25-Jan-2011		Integrated LED in System-in-Package Module
8,542,721	12/931,862	11-Feb-2011	24-Sep-2013	System and Method for Television Signal Demodulation and Decoding
8,253,610	12/932,170	17-Feb-2011	28-Aug-2012	Systems and Methods for Variable Rate Conversion
8,436,640	12/941,703	08-Nov-2010	07-May-2013	Area Optimized Output Impedance Controlled Driver
	12/963,443	08-Dec-2010		Systems and Methods for Reducing Rub and Buzz Distortion in a Loudspeaker
	13/010,586	20-Jan-2011		Method and System for Floating Point Acceleration on Fixed Point Digital Signal Processors

7,995,867	13/011,669	21-Jan-2011	09-Aug-2011	Image Processor and Method of Image Rotation
8,289,358	13/035,790	25-Feb-2011	16-Oct-2012	Systems and Methods for Alignment of Laser Printers
	13/037,091	28-Feb-2011		Multi-Sample Reading in Sleep Mode for Passive Infrared Detectors and Other Analog Inputs
	13/040,121	03-Mar-2011		Systems and Methods for High Pass Filtering with Smart Saturation
8,410,839	13/041,065	04-Mar-2011	02-Apr-2013	Systems and Methods for Driving High Power States Using Lower Voltage Processes
8,421,518	13/044,378	09-Mar-2011	16-Apr-2013	High Speed Level Shifters and Method of Operation
	13/052,784	21-Mar-2011		Universal Reed-Solomon Decoder
	13/065,620	24-Mar-2011		Unpackaged and Packaged IC Stacked in a System-in-Package Module
	13/066,731	21-Apr-2011		Fully Differential Adaptive Bandwidth PLL with Differential Supply Regulation
	13/092,006	21-Apr-2011		Systems and Methods for Speaker Bar Sound Enhancement
	13/092,743	22-Apr-2011		Biphase Mark Code Decoder and Method of Operation
8,552,540	13/104,620	10-May-2011	08-Oct-2013	Wafer Level Package with Thermal Pad for Higher Power Dissipation
	13/149,713	31-May-2011		Regular Expression Selection for Configurable Application Data Steering
	13/149,725	31-May-2011		Database Hierarchical Inheritance
	13/149,735	31-May-2011		Asynchronous Process Notification
	13/149,747	31-May-2011		Persistent Data Storage
	13/166,600	22-Jun-2011		Audio Driver System and Method
	13/168,605	24-Jun-2011		Externally Wire Bondable Chip Scale Package in a System-in-Package Module
8,532,145	13/181,376	12-Jul-2011	10-Sep-2013	Method and Apparatus for Channelizing Sampled Broadband Signal
	13/183,288	14-Jul-2011		A Wave Shaping System to Prevent Electrical and Mechanical Saturation in Loud Speakers
	13/184,231	15-Jul-2011		Auto Driver System and Method
8,540,529	13/196,379	02-Aug-2011	24-Sep-2013	Shielded USB Connector Module with Molded Hood and LED Light Pipe
	13/214,123	19-Aug-2011		System and Method for Automatic Baud Rate Selection for Facsimile Transmit and Receive
	13/230,633	12-Sep-2011		Spot Plated Leadframe and IC Bond Pad Via Array Design for Copper Wire
8,377,731	13/236,532	19-Sep-2011	19-Feb-2013	Micro Electro-Mechanical Sensor (MEMS) Fabricated with Ribbon Wire Bonds
	13/242,078	23-Sep-2011		Sub-Micron CMOS VCO That Uses Delay-Based Calibration and Method of Operation
	13/269,358	07-Oct-2011		Systems and Methods for Reliable Broadcast and Multicast Transmission Over a Wireless Local Area Network
	13/270,012	10-Oct-2011		Copper Stud Bump Wafer Level Package
	13/273,122	13-Oct-2011		Apparatus and Method for Abstract Memory Addressing
	13/280,221	24-Oct-2011		Audio Programmable Gain Amplifier with Automatic Reconfiguration
	13/284,833	28-Oct-2011		Low Power High PSRR PVT Compensation Bandgap and Current Reference with Internal Resistor with Detection/Monitoring Circuits
	13/285,305	31-Oct-2011		Method and System for Waking on Input/Output Interrupts While Powered Down

	13/289,926	04-Nov-2011		Low Power Level Shifter with Output Swing Control
8,446,216	13/300,012	18-Nov-2011	21-May-2013	Anti-Pop Circuit
	13/312,879	06-Dec-2011		System and Method for Capacitive DC-DC Converter with Variable Input and Output Voltages
	13/312,897	06-Dec-2011		Multi-Standard Headset Support with Integrated Ground Switching
8,396,015	13/324,924	13-Dec-2011	12-Mar-2013	Scanning Threshold
	13/340,240	29-Dec-2011		Acoustic Substrate
	13/344,510	05-Jan-2012		Systems and Methods for Stereo Echo Cancellation
	13/368,308	07-Feb-2012		Systems and Methods of Combined Bluetooth and WLAN Signaling
	13/426,469	21-Mar-2012		Systems and Methods for Receiving Data at an Access Point
	13/431,662	27-Mar-2012		Nonlinear Echo Suppression
	13/431,889	27-Mar-2012		Network Allocation
	13/443,849	10-Apr-2012		Systems and Methods for Management of Wireless Clients
8,432,148	13/525,071	15-Jun-2012	30-Apr-2013	Apparatus and Method for a Smooth Powerup of a Reference Signal
	13/535,212	27-Jun-2012		Systems and Methods for Indicating Buffered Data at an Access Point Using an Embedded Traffic Indication Map
	13/540,466	02-Jul-2012		Universal Systems and Methods for Determining an Incoming Carrier Frequency and Decoding an Incoming Signal
	13/548,045	12-Jul-2012		System and Method for Automatic Recovery of Cloud Print Jobs After Printer Error
	13/552,523	18-Jul-2012		Systems and Methods for Variable Rate Conversion
8,532,147	13/554,861	20-Jul-2012	10-Sep-2013	Residential Gateway for Voice Over Internet Protocol Communications
	13/560,290	27-Jul-2012		Multiple Microphone Support for Earbud Headsets
	13/560,439	27-Jul-2012		System and Method to Print Documents and Images That Are Not Natively Supported By The Printer
8,554,203	13/607,556	07-Sep-2012	08-Oct-2013	Systems and Method for Frequency Based Satellite Channel Scanning
	13/618,443	14-Sep-2012		Analog Frontend for CCD/CIS Sensor
	13/625,641	24-Sep-2012		Current Detection And Limiting Method and Apparatus
	13/672,239	08-Nov-2012		Systems and Methods for Improving the Scan-Line Throughput of an Image Scanner Using a Modulated Light Source
	13/693,569	04-Dec-2012		Power Management Controller for Drivers
	13/719,491	19-Dec-2012		Low-Power Programmable Oscillator and Ramp Generator
	13/719,577	19-Dec-2012		Current-Mode Sample and Hold for Dead Time Control of Switched Mode Regulators
	13/752,058	28-Jan-2013		System and Method for Dynamic Range Compensation of Distortion
	13/755,575	31-Jan-2013		Low Power Current Comparator for Switched Mode Regulators
	13/765,007	12-Feb-2013		Speakers and Room Virtualization Using Headphones
	13/766,003	13-Feb-2013		System and Method for Difference Frame Threshold Encoding and Decoding
	13/767,759	14-Feb-2013		Solid Via Pins for Improved Thermal and Electrical Conductivity
	13/774,923	22-Feb-2013		Adaptive Equalizer Utilizing Eye Diagram
	13/783,074	01-Mar-2013		Integrated Motion Detection Using Changes in Acoustic Echo Path
	13/790,967	08-Mar-2013		Method for Implementing Isolation Gates During Low-Power

			Modes
	13/840,302	15-Mar-2013	Reduced-Delay Subband Signal Processing System and Method
	13/851,906	27-Mar-2013	Copper Sphere Array Package
	13/856,393	03-Apr-2013	Methods to Reduce Current Spikes in Capacitive DC-DC Converters Employing Gain-Hopping
	13/860,477	10-Apr-2013	Class D Amplifier with Pulse Density Modulation Output Feedback for Higher Performance Acoustic Echo Canceller
	13/887,230	03-May-2013	Low Power Dual Voltage Mode Receiver
	13/892,064	10-May-2013	Reduced Delay Level Shifter
	13/919,973	20-Jun-2013	Low Area All Digital Delay-Locked Loop Insensitive to Reference Clock Duty Cycle and Jitter
	13/935,413	03-Jul-2013	Video Door Monitor Using SmartTV with Voice Wakeup
	13/941,350	12-Jul-2013	Echo Cancellation System and Method with Multiple Microphones and Multiple Speakers
	13/950,242	24-Jul-2013	Hybrid Analog/Digital Headset
	14/040,429	27-Sep-2013	Quad Flat No Lead (QFN) Package with Corner Leads
	14/062,840	24-Oct-2013	Programmable Revision Cell ID System and Method
	61/711,672	09-Oct-2012	Using 3-State Line Coding to Simplify Data Recovery Over High Speed Serial Link
	61/737,590	14-Dec-2012	Estimation of Reverberation Decay Related Applications
	61/772,441	04-Mar-2013	Robust Speech Boundaries Detection System and Method
	61/772,451	04-Mar-2013	Adaptive Biasing Technique for Audio Circuitry
	61/825,452	20-May-2013	Real-Time Secure Digital Fax Implementation Using Cloud Services
	61/826,906	23-May-2013	System and Method for Reducing Crosstalk in Multichannel SDI Receiver
	61/833,388	10-Jun-2013	Interleaving Multiple Compressed Video Streams Onto a Single SDI Link
	61/898,038	31-Oct-13	An On-line and Computationally Efficient, Low Memory, Selective Source Pickup (SSP) for Real-World Multichannel Convolutional Mixtures Based on Blind Source Signal Extraction