

## PATENT ASSIGNMENT COVER SHEET

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
LYRIC SEMICONDUCTOR, INC.	04/11/2016
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<b>PROPERTY NUMBERS Total: 3</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	12521505
Application Number:	13491212
Application Number:	13187466
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<b>DATE SIGNED:</b>	06/08/2016
<b>Total Attachments: 3</b>	
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source=07-C-0231m-o#page3.tif	

## License to the United States Government

Invention Title: Belief Propagation Processor

Inventor(s): David Reynolds, Benjamin Vigoda, Alexander Alexeyev

U.S. Filing/Issue Date: January 7, 2013 / March 3, 2015

Patent or Application Serial No.: 12/521505 / 8972831

Foreign Applications filed/intended in (countries): PCT (expired) No Foreign applications

Grant, Cooperative Agreement or Contract Number(s): **DARPA FA8750-07-C-0231**

Contractor: **Lyric Semiconductor, Inc.** (Acquired by Analog Devices, Inc.)


The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause included in your grant, cooperative agreement or contract with the United States Government. The Standard Patent Rights clause will be one of the following: **37 CFR 401.14, FAR 52.227-11** or **DFARS 252.227-7038**. This document is confirmatory of:

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Signed this 11 day of April, 2016.

By   
Benjamin Brown

Title: Senior I.P Counsel

For: Lyric Semiconductor, Inc.  
(now Lyric Labs, Analog Devices, Inc.)

At: Analog Devices, Inc., One Technology Way, Norwood, MA 02062

## License to the United States Government

Invention Title: Programmable Probability Processing  
Inventor(s): David Reynolds

U.S. Filing/Issue Date: June 7, 2012

Patent or Application Serial No.: 13/491212 / Not Yet Issued

Foreign Applications filed/intended in (countries): Europe, Korea, China

Grant, Cooperative Agreement or Contract Number(s): **DARPA FA8750-07-C-0231**

Contractor: **Lyric Semiconductor, Inc.** (Acquired by Analog Devices, Inc.)

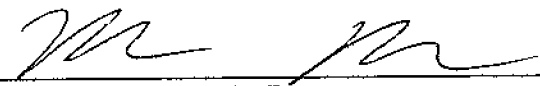
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For: Lyric Semiconductor, Inc.  
(now Lyric Labs, Analog Devices, Inc.)

At: Analog Devices, Inc., One Technology Way, Norwood, MA 02062

## License to the United States Government

Invention Title: Programmable Probability Processing

Inventor(s): Jeffrey Bernstein, Benjamin Vigoda

U.S. Filing/Issue Date: July 20, 2011 / Not Yet Issued

Patent or Application Serial No.: 13/187466

Foreign Applications filed/intended in (countries): None

Grant, Cooperative Agreement or Contract Number(s): **DARPA FA8750-07-C-0231**

Contractor: **Lyric Semiconductor, Inc.** (Acquired by Analog Devices, Inc.)


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