

## PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT3943901

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
KAZUO TANAKA	12/07/1998
HIROYUKI MIZUNO	12/07/1998
RIE NISHIYAMA	12/07/1998
MANABU MIYAMOTO	12/07/1998
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	HITACHI, LTD.
<b>Street Address:</b>	6, KANDA SURUGADAI 4-CHOME, CHIYODA-KU
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>Name:</b>	HITACHI ULSI SYSTEMS CO., LTD.
<b>Street Address:</b>	22-1, JOSUIHONCHO 5-CHOME, KODAIRA-SHI
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>PROPERTY NUMBERS Total: 9</b>	
<b>Property Type</b>	<b>Number</b>
Patent Number:	6392439
Patent Number:	6504400
Patent Number:	6677780
Patent Number:	6853217
Patent Number:	7091767
Patent Number:	7403361
Patent Number:	7944656
Patent Number:	8139332
Patent Number:	8674745
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(703)684-1157
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	7036841120

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<b>Email:</b>	escotti@mmpiaw.com
<b>Correspondent Name:</b>	MATTINGLY & MALUR, PC
<b>Address Line 1:</b>	1800 DIAGONAL ROAD
<b>Address Line 2:</b>	SUITE 210
<b>Address Line 4:</b>	ALEXANDRIA, VIRGINIA 22314

<b>ATTORNEY DOCKET NUMBER:</b>	Z-1672(USMA)/T&A-GEN (G)
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<b>NAME OF SUBMITTER:</b>	JOHN R. MATTINGLY
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<b>SIGNATURE:</b>	/John R. Mattingly/
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<b>DATE SIGNED:</b>	06/30/2016
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**Total Attachments: 1**

source=(g) Inventors to Hit Ltd and Hitachi ULSI Systems#page1.tif

## ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar(\$1.00) or the equivalent thereof, and other good and valuable consideration paid to

**Kazuo TANAKA, Hiroyuki MIZUNO, Rie NISHIYAMA, Manabu MIYAMOTO**

citizens of Japan by Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.

located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan and  
22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan

receipt of which is hereby acknowledged, we, the said

**Kazuo TANAKA, Hiroyuki MIZUNO, Rie NISHIYAMA, Manabu MIYAMOTO**

do hereby sell and assign to said Hitachi, Ltd. and  
Hitachi ULSI Systems Co., Ltd.

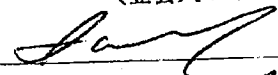
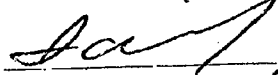
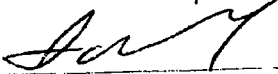

their successors and assigns, all our right, title and interest, in and for the United States of America, in and to **LEVEL CONVERSION CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE EMPLOYING THE LEVEL CONVERSION CIRCUIT** invented by us and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.

their Successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by us if this assignment and sale had not been made;

And we hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And we do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.

Signed on the dates indicated aside our signatures:

INVENTORS (発明者フルネームサイン)	Date Signed (署名日)	Witnesses (立会人サイン)
1) <u>Kazuo Tanaka</u> (Kazuo TANAKA)	December 7, 1998	
2) <u>Hiroyuki Mizuno</u> (Hiroyuki MIZUNO)	December 7, 1998	
3) <u>Rie Nishiyama</u> (Rie NISHIYAMA)	December 7, 1998	
4) <u>Manabu Miyamoto</u> (Manabu MIYAMOTO)	December 7, 1998	
5) _____ ( )		
6) _____ ( )		
7) _____ ( )		
8) _____ ( )		

RECORDED: 01/19/2001

RECORDED: 06/30/2016

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REEL: 011455 FRAME: 0602

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