

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT3958379

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ANUP BHALLA	07/26/2011
XIAOTIAN ZHANG	07/26/2011
YUEH-SE HO	07/26/2011
YONGPING DING	07/26/2011
MADHUR BOBDE	07/27/2011
RECEIVING PARTY DATA	
Name:	ALPHA AND OMEGA SEMICONDUCTOR INCORPORATED
Street Address:	475 OAKMEAD PARKWAY
City:	SUNNYVALE
State/Country:	CALIFORNIA
Postal Code:	94805
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15177191
CORRESPONDENCE DATA	
Fax Number:	(510)668-0239
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	510-668-0965
Email:	jessica@jdiipatent.com
Correspondent Name:	JOSHUA D. ISENBERG
Address Line 1:	809 CORPORATE WAY
Address Line 4:	FREMONT, CALIFORNIA 94539
ATTORNEY DOCKET NUMBER:	ANO-047/DIV
NAME OF SUBMITTER:	JOSHUA D. ISENBERG
SIGNATURE:	/Joshua D. Isenberg, Reg. No. 41088/
DATE SIGNED:	07/12/2016
Total Attachments: 5	
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ASSIGNMENT

INVENTOR Whereas, I, **Anup Bhalla** of **1406 Block Drive**
AND CITY **Santa Clara, California 95050, USA**

have invented:

TITLE: **METHODS FOR FABRICATING ANODE SHORTED FIELD
STOP INSULATED GATE BIPOLAR TRANSISTOR**

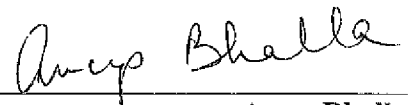
DATE INVENTOR and executed a United States patent application therefor
SIGNED THE
DECLARATION on 7/26/, 2011. Said assignor hereby authorize and request
their attorney, Joshua D. Isenberg, of 809 Corporate Way, Fremont,
California, to insert here in parentheses (Application number
15/177,191, filed June 8, 2016) the filing
date and application number of said application when known.

Whereas, **Alpha and Omega Semiconductor Incorporated**, a
California Corporation, having its registered address at 475 Oakmead
Pkwy, Sunnyvale, CA 94085, (hereinafter called **AOS**), desires to acquire
the entire right, title and interest of said application and invention, and to
any United States and foreign patents to be obtained therefore;

Now therefore for a valuable consideration, receipt whereof is
hereby acknowledged, I, the above named, hereby sell, assign, and transfer
to **AOS**, its successors and assigns, the entire right, title and interest in the
said application and invention therein disclosed for the United States and
foreign countries, and all rights of priority resulting from the filing said
United States application, and I request the Commissioner of Patents to
issue any Letters Patent granted upon the invention set forth in said
application to **AOS** its successors and assigns; and I hereby agree that
AOS may apply for foreign Letters Patent on said invention and I will
execute all papers necessary in connection with the United States and
foreign applications when called upon to do so by

CITY AND Signed and Sealed at SUNNYVALE

DATE on 7/26/, 2011



Anup Bhalla

ASSIGNMENT

**INVENTOR
AND CITY**

Whereas, I, **Xiaotian Zhang** of **4666 Cherrywood Dr.**
San Jose, CA 95129, USA

have invented:

TITLE:

**METHODS FOR FABRICATING ANODE SHORTED FIELD
STOP INSULATED GATE BIPOLAR TRANSISTOR**

**DATE INVENTOR
SIGNED THE
DECLARATION**

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CITY AND

Signed and Sealed at Sunnyvale

DATE

on July 26, 2011



Xiaotian Zhang

ASSIGNMENT

**INVENTOR
AND CITY**

Whereas, I, **Yueh-Se Ho** of **735 Iris Ave.**
Sunnyvale, CA 94086, USA

have invented:

TITLE:

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STOP INSULATED GATE BIPOLAR TRANSISTOR**

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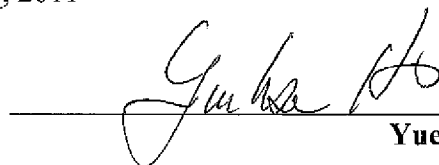
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foreign applications when called upon to do so by

CITY AND

Signed and Sealed at Sunnyvale

DATE

on July 26, 2011


Yueh-Se Ho

ASSIGNMENT

**INVENTOR
AND CITY**

Whereas, I, **Yongping Ding** of **1121 Miramar Way, Apt. 13**
Sunnyvale, CA 94086, USA

have invented:

TITLE:

**METHODS FOR FABRICATING ANODE SHORTED FIELD
STOP INSULATED GATE BIPOLAR TRANSISTOR**

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foreign applications when called upon to do so by

CITY AND

Signed and Sealed at Sunnyvale

DATE

on July 26, 2011

Yongping Ding
Yongping Ding

ASSIGNMENT

**INVENTOR
AND CITY**

Whereas, I, **Madhur Bobde** of **1602 Goldfinch Way
Sunnyvale, CA 94087, USA**

have invented;

TITLE:

**METHODS FOR FABRICATING ANODE SHORTED FIELD
STOP INSULATED GATE BIPOLAR TRANSISTOR**

**DATE INVENTOR
SIGNED THE
DECLARATION** and executed a United States patent application therefor
on July 27th, 2011. Said assignor hereby authorize and request

their attorney, Joshua D. Isenberg, of 809 Corporate Way, Fremont, California, to insert here in parentheses (Application number 15/177,191, filed June 8, 2016) the filing date and application number of said application when known.

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CITY AND

Signed and Sealed at Sunnyvale CA

DATE

on July 27th, 2011



Madhur Bobde