

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT4087354

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	JAY PAUL JOHN	10/06/2016
	VISHAL TRIVEDI	10/06/2016
	JAMES ALBERT KIRCHGESSNER	10/06/2016
RECEIVING PARTY DATA		
Name:	FREESCALE SEMICONDUCTOR, INC.	
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City:	AUSTIN	
State/Country:	TEXAS	
Postal Code:	78735	
PROPERTY NUMBERS Total: 1		
Property Type	Number	
Application Number:	15286988	
CORRESPONDENCE DATA		
Fax Number:		
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ATTORNEY DOCKET NUMBER:	82000793US01	
NAME OF SUBMITTER:	TRACY BUFFETT	
SIGNATURE:	/Tracy Buffett/	
DATE SIGNED:	10/07/2016	
Total Attachments: 1		
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ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we, as below-named Assignor/s, hereby sell, assign, and transfer the entire and exclusive right, title, and interest in the following to **FREESCALE SEMICONDUCTOR, INC.**, having a place of business at 6501 William Cannon Drive, Austin, TX 78735, USA, its successors, assigns, and legal representatives, including any nominees (collectively "the Assignee"):

(1) our invention relating to "SEMICONDUCTOR DEVICE STRUCTURE WITH NON PLANAR SLIDE WALL" for which the following patent application(s) has/have been filed (we hereby authorize and request my/our attorneys associated with U.S. Patent & Trademark Office Customer Number 23125 to insert below the filing date and application number of the U.S. application when known):

<u>Country</u>	<u>Application No.</u>	<u>Filing Date (mm/dd/yyyy)</u>	<u>Claims Priority of</u>
US			


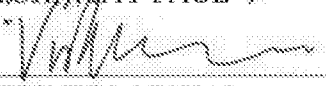

(2) the foregoing application(s) and all other United States, foreign and international patent applications associated therewith, based thereon, or claiming priority there from including, but not limited to, any and all provisionals, non-provisionals, divisions, continuations, continuations-in-part, re-examinations, reissues, and extensions thereof, and

(3) the right to claim priority thereto, and the entire and exclusive right, title, and interest in and to any and all patents granted on these applications.

We authorize and request that the Patent Office officials in the United States and in any and all foreign countries to issue any and all Letters Patent when granted, solely to **FREESCALE SEMICONDUCTOR, INC.**, for its sole use, and that of its successors, assigns, and legal representatives.

We will provide our cooperation to enable the Assignee to enjoy the foregoing right, title, and interest to the fullest extent. Upon request at the expense of the Assignee, we agree to execute all papers, take all rightful oaths, testify in all legal proceedings including patent prosecutorial actions and infringement actions, and do all other such acts which may be necessary, desirable, or convenient for securing and maintaining patents on the foregoing invention or for perfecting title thereto in the Assignee.

We certify that we have the full right to convey the above rights.

<u>October 6, 2016</u> Date	 JOHN JAY PAUL, Assignor
<u>October 6, 2016</u> Date	 TRIVEDI, VISHAL, Assignor
<u>October 6, 2016</u> Date	 KIRCHGEßNER, JAMES ALBERT, Assignor