504011413 09/19/2016

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

EPAS ID: PAT4058070

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
EMILY P. CHUNG	09/17/2016
FRANK T. HADY	07/25/2016
GEORGE VERGIS	09/14/2016

RECEIVING PARTY DATA

Name:	INTEL CORPORATION
Street Address:	2200 MISSION COLLEGE BLVD
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	15201370

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 503-730-8937

Email: vincent@compassiplaw.com, bob@compassiplaw.com

VINCENT H. ANDERSON **Correspondent Name:**

Address Line 1: C/O CPA GLOBAL

Address Line 2: 900 2ND AVENUE SOUTH, SUITE 600 MINNEAPOLIS, MINNESOTA 55402 Address Line 4:

ATTORNEY DOCKET NUMBER:	P96011
NAME OF SUBMITTER:	ROBERT CALDWELL
SIGNATURE:	/Robert Caldwell/
DATE SIGNED:	09/19/2016

Total Attachments: 6

source=P96011_Assignment_AsFiled _2016_09_19#page1.tif source=P96011 Assignment AsFiled 2016 09 19#page2.tif source=P96011 Assignment AsFiled 2016 09 19#page3.tif

PATENT REEL: 040072 FRAME: 0033 504011413

source=P96011_Assignment_AsFiled _2016_09_19#page4.tif source=P96011_Assignment_AsFiled _2016_09_19#page5.tif source=P96011_Assignment_AsFiled _2016_09_19#page6.tif

> PATENT REEL: 040072 FRAME: 0034

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

EMILY P. CHUNG; FRANK T. HADY; GEORGE VERGIS

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Ciara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

LOAD REDUCED NONVOLATILE MEMORY INTERFACE

	tey having appropriate authority from the assignee to insert on application number of said application when known.)	the
which was filed on <u>July 1, 2016</u>	as	
<u>US</u> COUNTRY or International Office	Application Number <u>15/201,370</u>	and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filled in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and

Page 1 of 2

the history thereof; and generally assist said Assignce, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignce, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

EMILY P. CHUNG	See A 2016 Date signed
FRANK T. HADY	Date signed
GEORGE VERGIS	Date signed

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

EMILY P. CHUNG: FRANK T. HADY: GEORGE VERGIS

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assigner"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

LOAD REDUCED NONVOLATILE MEMORY INTERFACE

which was filed on	July 1, 2016	85

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below.

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good fuith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and

Page Lof2

the history thereof, and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

EMILY P. CHUNG	Date signed
Thull UZ	7/25/16
FRANK T. HADY	Date signed
GEORGE VERGIS	Date signed

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

EMILY P. CHUNG; FRANK T. HADY; GEORGE VERGIS

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

LOAD REDUCED NONVOLATILE MEMORY INTERFACE

(I hereby authorize and request any attorney designated lines below, the filing date and ap				he
which was filed on July 1, 2016			as	
US COUNTRY or International Office	Application Number	15/201,370		and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and

the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

EMILY P. CHUNG	Date signed
FRANK T. HADY	Date signed
George Nergis	<u>G//4/20/6</u> Date signed