

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4108452

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	MICRON TECHNOLOGY, INC.	09/26/2008
RECEIVING PARTY DATA		
Name:	APTINA IMAGING CORPORATION	
Street Address:	C/O CITICO TRUSTEE (CAYMAN) LIMITED	
Internal Address:	REGATTA OFFICE PARK, WEST BAY ROAD	
City:	GRAND CAYMAN	
State/Country:	CAYMAN ISLANDS	
Postal Code:	Y1-1205	
PROPERTY NUMBERS Total: 1		
	Property Type	Number
	Patent Number:	7821557
CORRESPONDENCE DATA		
Fax Number:	(602)244-3169	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>		
Phone:	6022443574	
Email:	patents@onsemi.com	
Correspondent Name:	SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC	
Address Line 1:	5005 E. MCDOWELL ROAD	
Address Line 2:	MAILDROP A700	
Address Line 4:	PHOENIX, ARIZONA 85008	
ATTORNEY DOCKET NUMBER:	APTPBIT-0061D01	
NAME OF SUBMITTER:	SHARRON CASTILLO	
SIGNATURE:	/Sharron Castillo/	
DATE SIGNED:	10/21/2016	
Total Attachments: 11		
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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3.

[REDACTED]

[REDACTED]

4.

[REDACTED]

[REDACTED]

5.

[REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

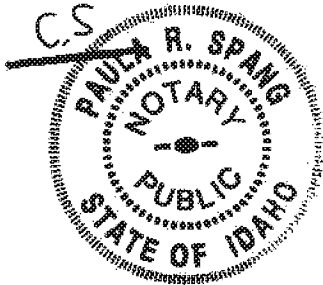
Notarization
State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton
(date) (notary name)
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas J. Lawrence, Jr.

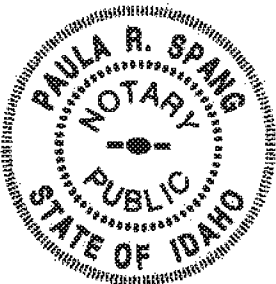
Date: 9/26/08, 2008

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State of Idaho
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On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas J. Lawrence, Jr.
(date) (notary name)
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"



ATTACHMENT "B"



ATTACHMENT "C"



ATTACHMENT "D"



ATTACHMENT "E"



Aptina Applications

PATENT	TITLE	STATUS	COUNTRY	PATENT NUMBER	DATE
PBIT-0044.00AUS	NONLINEAR FLASH ANALOG TO DIGITAL CONVERTER USED IN AN ACTIVE PIXEL SYSTEM	Abandoned	United States of America	11175041	Jul 8, 2005
PBIT-0048.00PR	ACTIVE PIXEL SENSOR WITH COLUMN PARALLEL DIGITAL TO ANALOG CONVERTER	Abandoned	United States of America	60106480	Oct 29, 1996
PBIT-0047.00PR	FLOATING PINNED PHOTOGATE ACTIVE PIXEL SENSOR	Abandoned	United States of America	60100751	Sep 18, 1998
PBIT-0051.00PR	ACTIVE PIXEL SENSOR WITH INTERSPERSED COLOR CODING	Abandoned	United States of America	60106279	Oct 29, 1996
PBIT-0061.00US	ACTIVE PIXEL SENSOR WITH INTERSPERSED COLOR CODING	Pending	United States of America	09430734	Oct 29, 1999
PBIT-0063.00PR	LOW POWER OPERATION OF MULTIPLEXING LOW LOCAL BRANDED TECHNIQUE	Pending	United States of America	09205052	Dec 7, 1998
PBIT-0064.00PR	Pixel for micropower, microdot CMOS active sensor	Abandoned	United States of America	60104285	Oct 14, 1996
PBIT-0069.01US	LOW POWER SIGNAL FLUX FOR IMAGE SENSORS	Pending	United States of America	11066828	Feb 17, 2005
PBIT-0058.00PR	Use of Clear Plastic GPP for Packaging of CMOS Active Pixel Image Sensors	Abandoned	United States of America	60111997	Nov 18, 1998
PBIT-0060.00PR	High-Speed Dual Pixel Readout for CMOS Image Sensors	Abandoned	United States of America	60109022	Oct 29, 1996
PBIT-0060.01AUS	SEMICONDUCTOR IMAGING SENSOR ARRAY DEVICES WITH DUAL-PORT DIGITAL READOUT	Pending	United States of America	11051554	Feb 7, 2005
PBIT-0061.00PR	Low Power Active Pixel Sensor Array with High-Speed Readout	Abandoned	United States of America	60102255	Mar 12, 1998
PBIT-0061.00AUS	HIGH-SPEED SAMPLING OF SIGNALS IN ACTIVE PIXEL SENSORS	Pending	United States of America	09627422	Mar 17, 2000
PBIT-0061.01AUS	HIGH-SPEED SAMPLING OF SIGNALS IN ACTIVE PIXEL SENSORS	Pending	United States of America	11475116	Oct 29, 2005