504081518 11/03/2016

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4128186

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SI-FLASH, LLC	11/02/2016

RECEIVING PARTY DATA

Name:	SI-FLASH DRIVES, LLC
Street Address:	35 HUGUS ALLEY
Internal Address:	SUITE 210
City:	PASADENA
State/Country:	CALIFORNIA
Postal Code:	91103

PROPERTY NUMBERS Total: 6

Property Type	Number
Patent Number:	7855916
Patent Number:	8194452
Patent Number:	8531880
Patent Number:	8817537
Patent Number:	9257184
Application Number:	15018599

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email:brian@prognosisip.comCorrespondent Name:SI-FLASH DRIVES, LLCAddress Line 1:35 HUGUS ALLEY

Address Line 2: SUITE 210

Address Line 4: PASADENA, CALIFORNIA 91103

NAME OF SUBMITTER:	BRIAN YATES
SIGNATURE:	/brian yates/
DATE SIGNED:	11/03/2016

Total Attachments: 2

PATENT REEL: 040218 FRAME: 0201

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source=Executed Assignment from Si-Flash to Si-Flash Drives November 2, 2016#page1.tif source=Executed Assignment from Si-Flash to Si-Flash Drives November 2, 2016#page2.tif

PATENT REEL: 040218 FRAME: 0202

ASSIGNMENT AGREEMENT

This Assignment Agreement ("Assignment") is entered into by and between Si-Flash Drives, LLC, a California limited liability company, hereinafter referred to as "Assignee," and Si-Flash, LLC, a Texas limited liability company, hereinafter referred to as "Assignor," and shall become effective upon being duly signed by the authorized representative of Assignor ("Effective Date").

RECITALS:

Assignor hereby desires to assign to Assignee all of its rights, title and interest in and to the following United States patents and pending application:

- 1. U.S. Patent Number 7,855,916, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on October 22, 2008, and issued on December 21, 2010;
- 2. U.S. Patent Number 8,194,452, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on October 29, 2010, and issued on June 5, 2012;
- 3. U.S. Patent Number 8,531,880, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on April 27, 2012, and issued on September 10, 2013;
- 4. U.S. Patent Number 8,817,537, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on August 15, 2013, and issued on August 26, 2014;
- 5. U.S. Patent Number 9,257,184, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on July 18, 2014, and issued on February 9, 2016; and
- 6. U.S. Patent Application Number 15/018599, entitled, "Nonvolatile memory systems with embedded fast read and write memories," filed on February 8, 2016, and is pending (collectively referred to hereinafter referred to as the "Patent").

For good and valuable consideration, Assignor and Assignee hereby agree as follows:

ASSIGNMENT:

1. Assignor hereby assigns to Assignee its entire worldwide right, title and interest in and to Assignor's Patent, including the entire worldwide right, title and interest in and to any

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reexamination or reissue of the Patent. This includes, without limitation, Assignor's right to prosecute, and receive, all damages, royalties, awards and compensation for past, present and/or future infringement.

- 2. Assignor agrees to do the following without further consideration, in order to carry out the intent of this Assignment:
 - i. Execute all oaths, assignments, applications, and other papers necessary or desirable to fully secure to Assignee the rights, titles and interests herein conveyed; and
 - ii. Generally do all lawful acts for vesting in Assignee the rights, titles, and interests herein conveyed.
- 3. Assignor has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed to Assignee.
- 4. Assignor hereby represents and warrants that Assignor has the authority to assign the applicable Patent pursuant to this Assignment.

The undersigned having read this Assignment and fully understanding the provisions of this Assignment, hereby executes this Assignment as of the Effective Date.

Si-Flash, LLC (Assignor) Nicolas Labbit, Manager

Signature:

Printed Name: Nicolas Labbit

Date: November 2, 2016

RECORDED: 11/03/2016