

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
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ISAO TAKAYANAGI	08/22/2002
JUNICHI NAKAMURA	08/22/2002
RECEIVING PARTY DATA	
Name:	MICRON TECHNOLOGY, INC.
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State/Country:	IDAHO
Postal Code:	83706
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	7525588
Patent Number:	7365773
Patent Number:	7880791
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DATE SIGNED:	11/14/2016
Total Attachments: 3	
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ASSIGNMENT AND AGREEMENT

For value received, we, Isao Takayanagi and Junichi Nakamura, hereby sell, assign and transfer to Micron Technology, Inc., a corporation of the State of Delaware, having an office at 8000 S. Federal Way, Boise, Idaho 83706-9632, U.S.A., and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to an invention entitled A CMOS APS WITH STACKED AVALANCHE MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS, described in an application for Letters Patent of the United States, executed by us of even date herewith, and all the rights and privileges in said application and under any and all Letters Patent that may be granted in the United States for said inventions; and we also concurrently hereby sell, assign and transfer to Micron Technology, Inc. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

We authorize Micron Technology, Inc. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from us, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

We hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Micron Technology, Inc. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

We request that any and all patents for said inventions be issued to Micron Technology, Inc. in the United States and in all countries foreign to the United States, or to such nominees as Micron Technology, Inc. may designate.

We agree that, when requested, we shall, without charge to Micron Technology, Inc. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

Docket No.: M4065.0630/P630
Micron No.: 02-0595

AND Assignor acknowledge an obligation of assignment of this invention to
Assignee at the time the invention was made.

8/22/02
Date

Isao Takayanagi
Isao Takayanagi

Witness:

8/23/02
Date

Tachirari Otsuka

Witness:

8/23/02
Date

Haruhisa Ando

Docket No.: M4065.0630/P630
Micron No.: 02-0595

8/22/02
Date

Junichi Nakamura
Junichi Nakamura

Witness:

8/23/02
Date

Tachiraki Otake

Witness:

8/23/02

Haruhiko Ando