

<b>PATENT ASSIGNMENT COVER SHEET</b>
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EPAS ID: PAT4148264

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ANIRUDDHA SATOSKAR	09/09/2016
DANIEL J. ALLEN	09/06/2016
EDMUND MARK SCHNEIDER	09/07/2016
SAURABH SINGH	09/22/2016
JOHN L. MELANSON	09/20/2016
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	CIRRUS LOGIC INTERNATIONAL SEMICONDUCTOR LTD.
<b>Street Address:</b>	WESTFIELD HOUSE
<b>Internal Address:</b>	26 WESTFIELD ROAD
<b>City:</b>	EDINBURGH
<b>State/Country:</b>	UNITED KINGDOM
<b>Postal Code:</b>	EH11 2QB
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	15269043
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(512)391-2173
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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<b>ATTORNEY DOCKET NUMBER:</b>	141841.00433-P3055US
<b>NAME OF SUBMITTER:</b>	MINDY MAYER
<b>SIGNATURE:</b>	/mindy mayer/
<b>DATE SIGNED:</b>	11/17/2016

**Total Attachments: 6**

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***INVENTOR ASSIGNMENT***

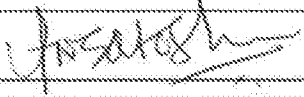
WHEREAS, **ANIRUDDHA SATOSKAR**, a citizen of India, residing at 14300 Tandem Blvd., Apartment 136, Austin, Texas 78728, **DANIEL J. ALLEN**, a citizen of the United States of America, residing at 2107 Wright Street, Austin, Texas 78704, **EDMUND MARK SCHNEIDER**, a citizen of the United States of America, residing at 11420 Carnelian Drive, Austin, Texas 78739, **SAURABH SINGH**, a citizen of India, residing at 1005 Williams Way, Cedar Park, Texas 78613, and **JOHN L. MELANSON**, a citizen of the United States of America, residing at 901 West 9<sup>th</sup> Street, #201, Austin, Texas 78703, collectively called the “Assignors,” have made the invention described in the United States patent application entitled ***Matching Paths in a Multiple Path Analog-to-Digital Converter***, Attorney Docket No. 141841.00433 (P3055US MP ADC Path Match), filed on **September 19, 2016** and assigned Application U.S. Serial No. **15/269043**; and

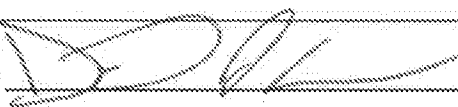
For good and valuable consideration, the receipt and sufficiency of which is acknowledged, I hereby sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd., a wholly-owned subsidiary of Cirrus Logic, Inc. organized under and pursuant to the laws of Scotland and having its principal place of business at Westfield House, 26 Westfield Road, Edinburgh EH11 2QB, United Kingdom, the entire right, title, and interest in and to the invention and all related intellectual property rights (the “Invention”) and in and to all applications related to the Invention for Letters Patent in the United States of America, including but not limited to any and all direct and indirect divisions, continuations, continuations-in-part, and reexaminations of said applications and reissues of any resulting patents, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent in the United States of America directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention. Furthermore, for good and valuable consideration, the receipt of which is acknowledged, I hereby also sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd. the entire right, title, and interest in and to the Invention and in and to all applications related to the Invention for all countries foreign to the United States of America, including but not limited to all rights under any and all international conventions and treaties in respect of the Invention and the applications for Letters Patent, utility models, utility patents, design patents, supplementary protection certificates, patent certificates, and all similar protections in all countries foreign to the United States of America, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent, utility models, utility patents, and

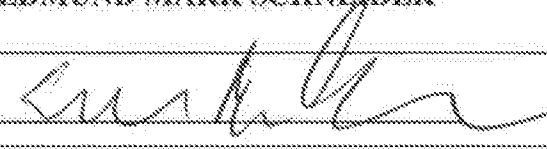
patent certificates in all countries foreign to the United States directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention under the provisions of any and all laws, international conventions, treaties, or otherwise.

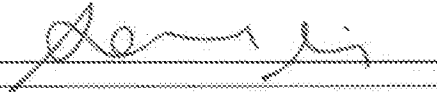
I hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent in the United States of America upon the aforesaid application, and any application related to the aforesaid application in the United States of America, to Cirrus Logic International Semiconductor Ltd., for the sole use and benefit of Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives, to the full end of the term for which the Letters Patent in the United States of America may be granted, the same as they would have been held and enjoyed by me had this assignment not been made. I hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to Cirrus Logic International Semiconductor Ltd.

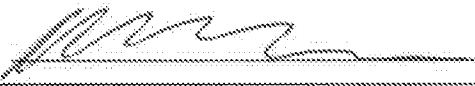
I agree that, when requested, I will, without charge to Cirrus Logic, Inc. or its subsidiaries, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable, or convenient for securing and maintaining any and all patent protection (as described above) for the Invention in any and all countries and for respectively vesting title thereto in Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives or nominees. To the maximum extent allowed by law, I hereby irrevocably designate and appoint Cirrus Logic International Semiconductor Ltd. and its duly authorized officers and agents, or Cirrus Logic International Semiconductor Ltd.'s applicable designee, as my agents and attorneys-in-fact to act for and on my behalf and instead of me to sign all papers, take all rightful oaths, and do all acts described in the foregoing sentence should I refuse or become otherwise unavailable to do so.

NAME OF FIRST INVENTOR	ANIRUDDHA SATOSKAR
SIGNATURE OF FIRST INVENTOR	
DATE	SEP 09 2016

NAME OF SECOND INVENTOR	DANIEL J. ALLEN
SIGNATURE OF SECOND INVENTOR	
DATE	SEP 06 2016

NAME OF THIRD INVENTOR	EDMUND MARK SCHNEIDER
SIGNATURE OF THIRD INVENTOR	
DATE	SEP 07 2016

NAME OF FOURTH INVENTOR	SAURABH SINGH
SIGNATURE OF FOURTH INVENTOR	
DATE	9/22/2016

NAME OF FIFTH INVENTOR	JOHN L. MELANSON
SIGNATURE OF FIFTH INVENTOR	
DATE	SEP 20 2016.