

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4162582

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	MICRON TECHNOLOGY, INC.	09/26/2008
RECEIVING PARTY DATA		
Name:	APTING IMAGING CORPORATION	
Street Address:	C/O CITICO TRUSTEES (CAYMAN) LIMITED	
Internal Address:	REGATTA OFFICE PARK, WEST BAY ROAD	
City:	GRAND CAYMAN	
State/Country:	CAYMAN ISLANDS	
Postal Code:	Y1-1205	
PROPERTY NUMBERS Total: 2		
	Property Type	Number
	Patent Number:	7473378
	Patent Number:	7564631
CORRESPONDENCE DATA		
Fax Number:	(602)244-3169	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>		
Phone:	6022443574	
Email:	patents@onsemi.com	
Correspondent Name:	SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC	
Address Line 1:	5005 E. MCDOWELL ROAD	
Address Line 2:	MAILDROP A700	
Address Line 4:	PHOENIX, ARIZONA 85008	
ATTORNEY DOCKET NUMBER:	APT2004-1228D01_D02	
NAME OF SUBMITTER:	SHARRON CASTILLO	
SIGNATURE:	/Sharron Castillo/	
DATE SIGNED:	11/30/2016	
Total Attachments: 7		
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PATENT ASSIGNMENT AGREEMENT

1. Definitions

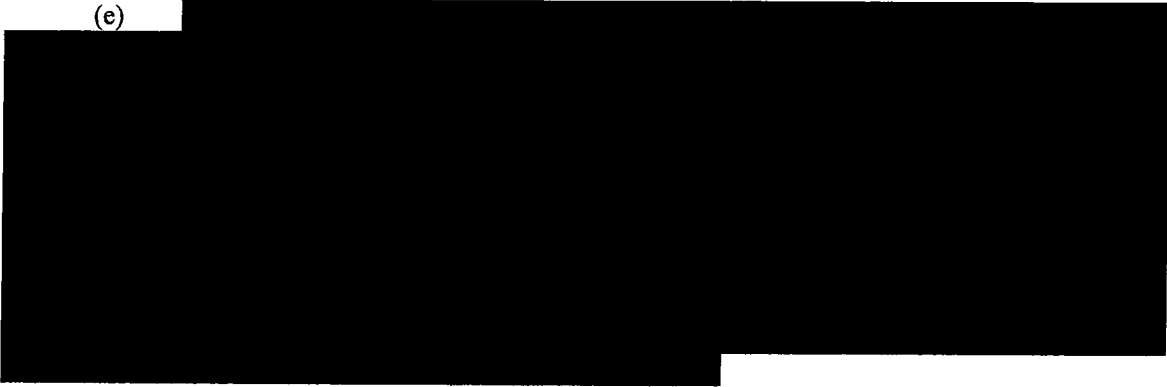
(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)

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(f)

A large rectangular area of the document is completely redacted with black ink, covering approximately six lines of text.

(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: _____

Date: 9/26/08, 2008

Notarization

State of Idaho

County of Ada

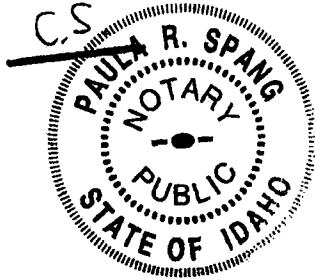
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Stan R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: _____

Date: 9/26/08, 2008

Notarization

State of Idaho

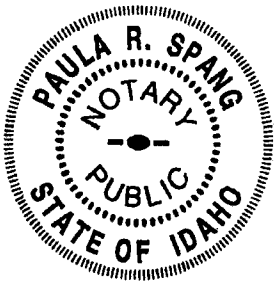
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lowe, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

IMAGING PATENTS

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
7303938	2002-0013.01/US	GATED ISOLATION STUCTURE FOR IMAGERS	United States of America	Dec 4, 2007
7307327	AVGO-0136.00/US	Reduce Crosstalk CMOS Image Sensors	United States of America	Dec 11, 2007
7307788	2004-0812.00/US	GAPLESS MICROLENS ARRAY AND METHOD OF FABRICATION	United States of America	Dec 11, 2007
7310262	2003-1218.01/US	CLOSED-LOOP HIGH-VOLTAGE BOOSTER	United States of America	Dec 18, 2007
7312431	2003-0110.01/US	CMOS IMAGING FOR ALC AND CDS	United States of America	Dec 25, 2007
7315014	2004-1200.00/US	IMAGE SENSORS WITH OPTICAL TRENCH	United States of America	Jan 1, 2008
7317480	PBIT-0142.00/US	BLACK LEVEL COMPENSATION WITH OF A SUCCESSIVE APPROXIMATION A/D CONVERTER	United States of America	Jan 8, 2008
7317579	2004-1228.00/US	METHOD AND APPARATUS PROVIDING GRADED INDEX MICRO LENSES	United States of America	Jan 8, 2008
7321455	2003-0512.01/US	MICROELECTRONIC DEVICES AND METHODS FOR PACKAGING MICROELECTRONIC DEVICES	United States of America	Jan 22, 2008
7321504	2004-0060.00/US	STATIC RANDOM ACCESS MEMORY CELL	United States of America	Jan 22, 2008
7324290	AVGO-0176.00/US	Variable Focus Optic Module And Optic System	United States of America	Jan 29, 2008
7328503	2005-0212.00/US	PROCESS FOR COLOR FILTER ARRAY RESIDUAL PIGMENT REMOVAL	United States of America	Feb 5, 2008
7328904	2002-0272.01/US	IN-PIXEL KTC NOISE SUPPRESSION USING CIRCUIT TECHNIQUES	United States of America	Feb 5, 2008
7327393	AVGO-0047.00/US	CMOS image sensor with variable conversion gain	United States of America	Feb 5, 2008
7329856	AVGO-0029.00/US	Image Sensor Having Integrated Electrical Optical Device And Related Method	United States of America	Feb 12, 2008
7325861	AVGO-0073.00/US	Integrally packaged imaging module	United States of America	Feb 12, 2008
7330146	2003-0820.02/US	MINIMIZED SAR-TYPE COLUMN-WIDE ADC FOR IMAGE SENSORS	United States of America	Feb 12, 2008
7330211	AVGO-0066.00/US	Camera module with focus adjustment	United States of America	Feb 12, 2008