

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4131438

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TESSERA TECHNOLOGIES HUNGARY KFT.	11/19/2010
RECEIVING PARTY DATA	
Name:	TESSERA TECHNOLOGIES IRELAND LIMITED
Street Address:	CLIONA BUILDING ONE
Internal Address:	PARKMORE EAST BUSINESS PARK
City:	GALWAY, BALLYBRIT
State/Country:	IRELAND
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	14089539
CORRESPONDENCE DATA	
Fax Number:	(908)654-7866
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	(908) 518-6394
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Correspondent Name:	LDLK&M
Address Line 1:	600 SOUTH AVENUE WEST
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ATTORNEY DOCKET NUMBER:	TIPI 3.3-1006 CON CON CON
NAME OF SUBMITTER:	MELINDA C. CORMIER
SIGNATURE:	/Melinda C. Cormier/
DATE SIGNED:	11/07/2016
Total Attachments: 24	
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CONFIRMATORY ASSIGNMENT

In consideration of and in exchange for the sum of one dollar (\$1.00) and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, this Confirmatory Assignment ("CONFIRMATORY ASSIGNMENT") is made and entered into by and between:

Tessera Technologies Hungary Holding Limited Liability Company, a Limited Liability Company of Hungary, the same company also being known as "Tessera Technologies Hungary Holding KFT.", and "Tessera Technologies Hungary KFT.", ("SELLER")
c/o TMF Hungary Ltd.
Wesselenyi u. 16
1077 Budapest, Hungary

and

Tessera Technologies Ireland Limited, a Corporation of Ireland
("BUYER")
Cliona
Building One, Parkmore East Business Park
Ballybrit, Galway
Ireland

1. SELLER and BUYER have entered into a Asset Purchase Agreement between the parties (the "ASSET PURCHASE AGREEMENT"), with an effective date of June 1, 2010 (the "EFFECTIVE DATE").

2. In the ASSET PURCHASE AGREEMENT, SELLER has assigned to BUYER for valuable consideration all of SELLER's rights, title and interests held, or that may come to be held, in and to patents and patent applications ("the ASSIGNED PATENTS") as now existing or which may hereafter be acquired, including all the patents and patent applications listed in Exhibit B of the ASSET PURCHASE AGREEMENT, a copy of which is appended hereto and incorporated herein. BUYER accepts the assignment of the ASSIGNED PATENTS including the assignment of specific rights confirmed in paragraph 3 (below).

3. **Confirmation of Assignment.** SELLER hereby confirms that it has assigned and conveyed to BUYER SELLER's entire right, title and interest in and to the ASSIGNED PATENTS. The effective date of the ASSET PURCHASE AGREEMENT hereby confirmed is the EFFECTIVE DATE.

4. **Consent.** SELLER hereby consents to BUYER performing, solely in its name, all necessary procedures for recording this CONFIRMATORY ASSIGNMENT of ASSIGNED PATENTS.

[Signatures follow on Page 2]

For SELLER (Tessera Technologies Hungary Holding Limited Liability Company):

Michael Bereziuk
SELLER Authorized Signature

Date: 19th Nov 2010

Michael Bereziuk; Managing Director
Printed Name and Title

For BUYER (Tessera Technologies Ireland Limited)

Michael Bereziuk
BUYER Authorized Signature

Date: 19th Nov 2010

Michael Bereziuk; Director
Printed Name and Title

[Signature Notarizations follow on Page 3]

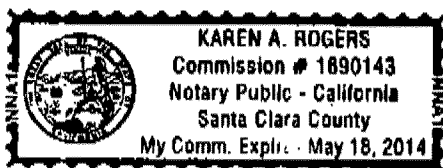
Notarization of SELLER Authorized Signature:

STATE OF CaliforniaCOUNTY OF Santa ClaraOn 11-19-2010, before me, Karen A. Rogers, personally appeared,
(Printed Name of Notary)

Michael Bereziuk, who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the governing state that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Karen A. Rogers
Notary Public

SEAL

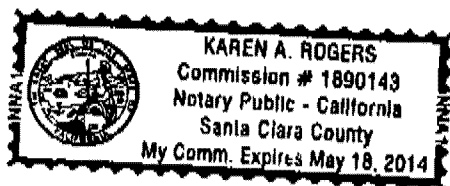
Notarization of BUYER Authorized Signature:

STATE OF CaliforniaCOUNTY OF Santa ClaraOn 11-19-2010, before me, Karen A. Rogers, personally appeared,
(Printed Name of Notary)

Michael Bereziuk, who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the governing state that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Karen A. Rogers
Notary Public

SEAL

EXHIBIT B

List of Patents Owned by Seller

Patent Number	Country	Patent Owner	Status	Title	Application Number	Publication Number	Class
280/139	US	Tessera Technologies Hungary, LLC	Issued	SYNTHESIS OF LIGHT BEAMS	09/277,322		6343307
280/140	US	Tessera Technologies Hungary, LLC	Issued	PHASE-ONLY FILTER FOR GENERATING AN ARBITRARY ILLUMINATION PATTERN	08/942,121		5909312
280/140 IL	IL	Tessera Technologies Hungary, LLC	Issued	PHASE-ONLY FILTER FOR GENERATING AN ARBITRARY ILLUMINATION PATTERN	119341		119341
280/142	US	Tessera Technologies Hungary, LLC	Issued	ELECTRONIC ZOOM IMAGE INPUT METHOD	09/331,008		6873358
280/142 JP DIV	JP	Tessera Technologies Hungary, LLC	Issued	ELECTRONIC ZOOM IMAGE INPUT METHOD	2008-150921	2008-228352	4235683
280/511	US	Tessera Technologies Hungary, LLC	Issued	METHOD AND SYSTEM FOR SUPER RESOLUTION	09/937,580		7003177
TESHEL 3.0-001 TAIWAN DIV	TW	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PRODUCING	83110998	328644	93642

WEST121974979.6

Patent Number	Country Code	Applicant Name	Inventor Name	Title	Publication No.	Patent No.	Class
TESHEL 3.0-003 SINGAPORE	SG	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICES METHODS AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	9501998-0		50376
TESHEL 3.0-003 TAIWAN	TW	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	84100321	360957	104431
TESHEL 3.0-006 TAIWAN	TW	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICE METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	88103574	526568	176465
TESHEL 3.0-007	US	Tessera Technologies Hungary, LLC	Issued		09/725,166	20010018236	677767
TESHEL 3.0-007 DIV	US	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING PACKAGED	09/922,770	20020027296	7144745

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PATENT

REEL: 040574 FRAME: 0868

Patent Number	Source/Origin	Applicant/Inventor	Attorney/Agent	Title	Application #	Publication #	Class #
TESHEL 3.0-007 TAIWAN	TW	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	89125196	466722	147977
TESHEL 3.0-008	US	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES AND PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	10/277,498	2004/0076797	7033664

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PATENT

REEL: 040574 FRAME: 0869

Patent Number	Country	Applicant	Inventor	Title	Application Number	Publication Number	Priority Number
TESHEL 3.0-009	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	10/884,058	20050104179	7192796
TESHEL 3.0-009 DIV	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	11/699,852	20070138498	7495341
TESHEL 3.0-009 DIV CONT	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	11/894,473	20080017879	7479398
TESHEL 3.0-010	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	10/462,576	20040251525	6972480
TESHEL 3.0-010 CON	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	11/125,624	20050205977	7265440
TESHEL 3.0-010 CON CON	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR	11/891,867	20080012115	7642629

WEST01974979.6

Patent Number	Country Code	Applicant Name	Inventor Name	Title	Publication No.	IPC Class.	Priority No.
TESHEL 3.0-011 TAIWAN	TW	Tessera Technologies Hungary, LLC	Issued	PACKAGING INTEGRATED CIRCUIT DEVICES PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	90133234	546807	NI-183350
TESHEL 3.3-001	US	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES AND PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	071962,222		5455455
TESHEL 3.3-001 DIV	US	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	08774,251		5547906
TESHEL 3.3-001 DIV CON	US	Tessera Technologies Hungary, LLC	Issued	METHODS OF PRODUCING INTEGRATED CIRCUIT DEVICES	08681,598		6117707

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PATENT

REEL: 040574 FRAME: 0871

Patent Number	Country (ID)	Applicant (ID)	Inventor (ID)	Title	Appl. No.	Pub. No.	Pub. Date
TESHEL 3.3-002	US	Tessera Technologies Hungary, LLC	Issued	METHOD AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	08/602,853		5716759
TESHEL 3.3-003	US	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	08/682,556		6040235
TESHEL 3.3-004	US	Tessera Technologies Hungary, LLC	Issued	PROCESS FOR MANUFACTURING SOLDER LEADS ON A SEMICONDUCTOR DEVICE PACKAGE	08/765,473		6022758
TESHEL 3.3-005	US	Tessera Technologies Hungary, LLC	Issued	BONDING MACHINE	08/952,019		5980663
TESHEL 3.3-006	US	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICE	09/601,895		6646289
TESHEL 3.3-006 CIP	US	Tessera Technologies Hungary, LLC	Issued	PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	09/758,906	20030080398	6624505
TESHEL 3.3-006 CONT	US	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICE	10/385,555	20030151124	7157742

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Patent Number	Country	Applicant	Inventor	Title	Publication No.	Publication Date	Patent No.
TESHEL 3.3-011	US	Tessera Technologies Hungary, LLC	Issued	PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	10/451,564	2004/0183185	7408249
TESHEL 3.4-001 JAPAN	JP	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	1994-507721	3621093	3621093
TESHEL 3.4-001 S KOREA	KR	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PRODUCING INTEGRATED CIRCUIT DEVICES	10-1995-700986	10-1995-0703798	310220
TESHEL 3.4-003 FRANCE (EP)	FR	Tessera Technologies Hungary, LLC	Issued	METHOD OF PRODUCING INTEGRATED CIRCUIT DEVICES	95906334.8	0740852	0740852
TESHEL 3.4-003 GERMANY (EP)	DE	Tessera Technologies Hungary, LLC	Issued	METHOD OF PRODUCING INTEGRATED CIRCUIT DEVICES	95906334.8	0740852	69511241-4
TESHEL 3.4-003 GR BRIT (EP)	GB	Tessera Technologies Hungary, LLC	Issued	METHOD OF PRODUCING INTEGRATED CIRCUIT DEVICES	95906334.8	0740852	0740852

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Patent No.	Country	Applicant	Inventor	Title	Application No.	Publication No.	Class
TESHEL 3.4-003 IRELAND (EP)	IB	Tessera Technologies Hungary, LLC	Issued	METHOD OF PRODUCING INTEGRATED CIRCUIT DEVICES	95906334.8	0740852	0740852
TESHEL 3.4-006 S KOREA	KR	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICE	7008624/2000		657117
TESHEL 3.4-006 SINGAPORE	SG	Tessera Technologies Hungary, LLC	Issued	INTEGRATED CIRCUIT DEVICE	200003844-8	10-2001-0040740	74511
TESHEL 3.4-007 CHINA	CN	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	00816990.X	CN1409869A	ZL00816990.X
TESHEL 3.4-007 S KOREA	KR	Tessera Technologies Hungary, LLC	Issued	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES	2002-7007284	10-2002-0074158	725107

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PATENT

REEL: 040574 FRAME: 0874

Patent No.	Applicant	Inventor	Issue Date	Class	IPC Class	Pub No.	Pub Date
TESHEL 3.4-007 SINGAPORE	SG	Tessera Technologies Hungary, LLC	Issued	PRODUCED THEREBY	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	200203440-3	89619
TESHEL 3.4-009 CHINA	CN	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	200480025199.8	CN1977384A ZL0480025199.8
TESHEL 3.4-009 INDIA	IN	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	01447/MUMNP/2005	222850
TESHEL 3.4-010 CHINA	CN	Tessera Technologies Hungary, LLC	Issued	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	200480023300.6	CN1836325A ZL0480023300.6

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PATENT

REEL: 040574 FRAME: 0875

Patent Number	Country	Applicant	Status	Title	Pub. No.	Pub. Date	Pub. No.
TESHEL 3.4-011 S KOREA	KR	Tessera Technologies Hungary, LLC	Issued	PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	2003-7008481	10-2004-0040404	855015
TESHEL 3.4-011 SINGAPORE	SG	Tessera Technologies Hungary, LLC	Issued	PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	200303644.9		98561
TESSOP 3.0-011 EUROPE	EP	Tessera Technologies Hungary, LLC	Issued	A METHOD AND SYSTEM FOR SUPER RESOLUTION IMAGE CAPTURE USING A MASK	0912890.1	1169847	EP1169847
280/135 PCT	WO	Tessera Technologies Hungary, LLC	Pending / PCT	Folded optic, camera system including the same, and associated methods	TBA		
280/122 EP	EP	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS	7872051.3	2104877	
280/123	US	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM	12213.473	20090122180	

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Patent Number	Country	Applicant	Inventor(s)	Title	Pub. No.	Pub. Date	Pub. No.	Pub. Date
		LLC		WITH RELAXED ASSEMBLY TOLERANCES AND ASSOCIATED METHODS				
280/123 BP	BP	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM WITH RELAXED ASSEMBLY TOLERANCES AND ASSOCIATED METHODS	7870432.7	2069651		
280/125	US	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS	12/213,472	20090115885		
280/125 BP	BP	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS	7874537.9	2108231		
280/125 JP	JP	Tessera Technologies Hungary, LLC	Pending / Published	IMAGING SYSTEM WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS	2009-527927	2010-504014		
280/125 KR	KR	Tessera Technologies Hungary,	Pending / Published	IMAGING SYSTEM	2009-7007745	2009-0055635		

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PATENT

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Patent Number	Country	Applicant	Publication Number	Publication Date	IPC Class	Priority Number	Priority Date	IPC Class
		LLC			WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS			
280/129	US	Tessera Technologies Hungary, LLC	Pending / Published		IMAGING SYSTEM WITH MULTI-STATE ZOOM AND ASSOCIATED METHODS	12/289,339	20090122162	
280/141	US	Tessera Technologies Hungary, LLC	Pending / Published		IMAGE CAPTURING DEVICE WITH IMPROVED IMAGE QUALITY	12/225,591	20090225171	
280/141 CN	CN	Tessera Technologies Hungary, LLC	Pending / Published		IMAGE CAPTURING DEVICE WITH IMPROVED IMAGE QUALITY	200680054027.2	CN10143857A	
280/141 EP	EP	Tessera Technologies Hungary, LLC	Pending / Published		IMAGE CAPTURING DEVICE WITH IMPROVED IMAGE QUALITY	6723830.3	1999947	
280/141 IN	IN	Tessera Technologies Hungary, LLC	Pending / Published		IMAGE CAPTURING DEVICE WITH IMPROVED IMAGE QUALITY	3892/K.OLNP/2008	3892/K.OLNP/2008	
280/141 JP	JP	Tessera Technologies Hungary, LLC	Pending / Published		IMAGE CAPTURING DEVICE WITH	2009-501859	2009-531723	

PATENT

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Patent Number	Country/ID Number	Applicant/Contrast/ID Number	Inventor/Contrast/ID Number	Title	Application #	Publication #	Article #
280/141 KR	KR	Tessera Technologies Hungary, LLC	Pending / Published	IMPROVED IMAGE QUALITY IMAGE CAPTURING DEVICE WITH IMPROVED IMAGE QUALITY	10-2008-7026489	2009-0005103	
280/142 JP	JP	Tessera Technologies Hungary, LLC	Pending / Published	ELECTRONIC ZOOM IMAGE INPUT METHOD	1997-351958	10-233950	
60166-0014	US	Tessera Technologies Hungary, LLC	Pending / Published	TECHNIQUES FOR ADJUSTING THE EFFECT OF APPLYING KERNELS TO SIGNALS TO ACHIEVE DESIRED EFFECT ON SIGNAL	11/970,427	20080266413	
60166-0053	WO	Tessera Technologies Hungary, LLC	Pending / Published	TECHNIQUES FOR REDUCING NOISE WHILE PRESERVING CONTRAST IN AN IMAGE	PCT/IB09/06257	WO2009/147535	
TESHEL 3.0-007 DIV CONT	US	Tessera Technologies Hungary, LLC	Pending / Published	CHIP PACKAGES WITH COVERS	11/588,489	20070040257	
TESHEL 3.0-009 TAIWAN	TW	Tessera Technologies Hungary, LLC	Pending / Published	METHODS AND APPARATUS FOR PACKAGING INTEGRATED	93120132	200514212	

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PATENT

REEL: 040574 FRAME: 0879

Patent Number	Country ID	Applicant	Inventor(s)	IPC Class	Application No.	Publication No.	Priority
TESHEL 3.0-010 TAIWAN	TW	Tessera Technologies Hungary, LLC	Pending / Published	CIRCUIT DEVICES METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	93117306	200511453	
TESHEL 3.0-014 IV A	US	Tessera Technologies Hungary, LLC	Pending / Published	WAFER LEVEL PACKAGING TO LIDDED CHIPS	11/655,739	20070190747	
TESHEL 3.0-014 IV A TAIWAN	TW	Tessera Technologies Hungary, LLC	Pending / Published	WAFER LEVEL PACKAGING TO LIDDED CHIPS	96102489	200737431	
TESHEL 3.0-014 IV B	US	Tessera Technologies Hungary, LLC	Pending / Published	WAFER LEVEL CHIP PACKAGING	11/655,777	20070190691	
TESHEL 3.0-014 IV B TAIWAN	TW	Tessera Technologies Hungary, LLC	Pending / Published	WAFER LEVEL CHIP PACKAGING	96102490	200739838	
TESHEL 3.0-017	US	Tessera Technologies Hungary, LLC	Pending / Published	WAFER-LEVEL FABRICATION OF LIDDED CHIPS WITH ELECTRODEPOSITED DIELECTRIC COATING	11/590,616	20080099900	
TESHEL 3.0-017 CIP	US	Tessera Technologies Hungary, LLC	Pending / Published	WAFER-LEVEL FABRICATION OF LIDDED CHIPS WITH ELECTRODEPOSITED DIELECTRIC	11/789,694	20080099907	

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PATENT

REEL: 040574 FRAME: 0880

Law Firm/Ref Number	Country/Ref Number	Assigned By/Contract ID/Company	Invention Status/ID Lookup	Title	Applicant #	Publication #	Patent #
TESHEL 3.3-006 CONT CONT	US	Tessera Technologies Hungary, LLC	Pending / Published	COATING INTEGRATED CIRCUIT DEVICE	11/588,490	US 2007-0040180 A1	
TESHEL 3.3-006 CONT DIV	US	Tessera Technologies Hungary, LLC	Pending / Published	INTEGRATED CIRCUIT DEVICE	11/588,439	20070042562	
TESHEL 3.4-006 EP	EP	Tessera Technologies Hungary, LLC	Pending / Published	INTEGRATED CIRCUIT DEVICE	99902809.5	1051746	
TESHEL 3.4-006 JAPAN	JP	Tessera Technologies Hungary, LLC	Pending / Published	INTEGRATED CIRCUIT DEVICE	2000-530942	2002-512436	
TESHEL 3.4-007 EP	EP	Tessera Technologies Hungary, LLC	Pending / Published	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES PRODUCED THEREBY	0979896.8	1247293	
TESHEL 3.4-007 JAPAN	JP	Tessera Technologies Hungary, LLC	Pending / Published	METHODS FOR PRODUCING PACKAGED INTEGRATED CIRCUIT DEVICES & PACKAGED INTEGRATED CIRCUIT DEVICES	2001-543771	2003-516634	

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PATENT

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Patent Number	Country	Applicant	Status	Abstract	IPC Class	Pub. No.	Pub. Date	Pub. No.	Pub. Date
TESHEL 3.4-009 JAPAN	JP	Tessera Technologies Hungary, LLC	Pending / Published	PRODUCED THEREBY	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	2006-518485	2007-528120		
TESHEL 3.4-009 KOREA	KR	Tessera Technologies Hungary, LLC	Pending / Published	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	2006-7008184		10-2006-0026953		
TESHEL 3.4-010 KOREA	KR	Tessera Technologies Hungary, LLC	Pending / Published	METHODS AND APPARATUS FOR PACKAGING INTEGRATED CIRCUIT DEVICES	2005-7024161		2006-0023991		
TESHEL 3.4-011 EP	EP	Tessera Technologies Hungary, LLC	Pending / Published	PACKAGED INTEGRATED CIRCUITS AND METHODS OF PRODUCING THEREOF	1271792.2		1356718		
TESHEL 3.4-011 JAPAN	JP	Tessera Technologies Hungary, LLC	Pending / Published	PACKAGED INTEGRATED CIRCUITS AND METHODS OF	2002-552378		2004-534375		

WESTN21974979.6

PATENT

REEL: 040574 FRAME: 0882

Law Firm/Inventor Number	Country ID/Location	Assignee/Corporate ID/Bookup	Inventions Status ID/Bookup	Title	Application #	Publication #	Patent #
TESHEL 3.4-017 CIP CHINA	CN	Tessera Technologies Hungary, LLC	Pending / Published	PRODUCING THERMOF WAFER-LEVEL FABRICATION OF LIDDED CHIPS WITH ELECTRODEPOSITED DIELECTRIC COATING	200780048865.3	CN101578703A	
TESHEL 3.4-017 CIP EP	EP	Tessera Technologies Hungary, LLC	Pending / Published	WAFER-LEVEL FABRICATION OF LIDDED CHIPS WITH ELECTRODEPOSITED DIELECTRIC COATING	7852914.6	2087516	
TESSP106WO	WO	Tessera Technologies Hungary, LLC	Pending / Published	CUSTOMIZED DEPTH OF FIELD OPTICAL SYSTEM AND COMPACT FAST LENS ARCHITECTURE	PCT/US08/12670	WO2009/061519	
280/125 CN	CN	Tessera Technologies Hungary, LLC	Pending / Unpublished	IMAGING SYSTEM WITH IMPROVED IMAGE QUALITY AND ASSOCIATED METHODS	200780041349.8		
280/125 IN	IN	Tessera Technologies Hungary, LLC	Pending / Unpublished	IMAGING SYSTEM WITH IMPROVED	1339/KOLNP/2009		

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Patent Number	Country	Applicant	Inventor	Title	Pub. No.	Pub. Date	Pub. Ref.
280/142 RE	US	Tessera Technologies Hungary, LLC		IMAGE QUALITY AND ASSOCIATED METHODS	11/728,982		
60166-0043	US	Tessera Technologies Hungary, LLC		REISSUE OF U.S. PATENT NUMBER 6,873,358	12/651,190		
60166-0044	US	Tessera Technologies Hungary, LLC		Auto white balance algorithm using RGB product measure	12/774,486		
60166-0046	CN	Tessera Technologies Hungary, LLC		Techniques For Adjusting The Effect Of Applying Kernels To Signals To Achieve Desired Effect On Signal			
60166-0047	EP	Tessera Technologies Hungary, LLC		Techniques For Adjusting The Effect Of Applying Kernels To Signals To Achieve Desired Effect On Signal			
60166-0048	JP	Tessera Technologies Hungary, LLC		Techniques For Adjusting The Effect Of Applying Kernels To Signals To Achieve	TBA		

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Law Firm/Attorney/Inventor	Country (ID) (app)	Assigned By (Source/ID) (app)	Inventions (Status) (ID) (app)	Title	Application #	Publication #	Patent #
60166-0049	KR	Tessera Technologies Hungary, LLC	Pending / Unpublished	Desired Effect On Signal Techniques For Adjusting The Effect Of Applying Kernels To Signals To Achieve Desired Effect On Signal	tba		
60899/387216	TW	Tessera Technologies Hungary, LLC	Pending / Unpublished	METHODS AND SYSTEMS FOR READING AN IMAGE SENSOR BASED ON A TRAJECTORY	TBA		
60899-387215	WO	Tessera Technologies Hungary, LLC	Pending / Unpublished	METHODS AND SYSTEMS FOR READING AN IMAGE SENSOR BASED ON A TRAJECTORY	PCT/EP10/54734		
TESHEL 3.0-030	US	Tessera Technologies Hungary, LLC	Pending / Unpublished	Packaged Microelectronic Assembly with Stacked Die	12/723,039		
TESHEL 3.0-030 TAIWAN	TW	Tessera Technologies Hungary, LLC	Pending / Unpublished	STACKED MICROELECTRONIC ASSEMBLIES HAVING VIAS EXTENDING	99107382		

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REEL: 040574 FRAME: 0885

Patent Number	Country	Inventor(s)	Applicant	Publication Number	Title	Publication Date	Publication Number	Publication Date
TESHEL 3.4-017 CIP KOREA	KR		Tessera Technologies Hungary, LLC	Pending / Unpublished	THROUGH BOND PADS WAFER-LEVEL FABRICATION OF LADDED CHIPS WITH ELECTRODEPOSITED DIELECTRIC COATING	10-2809-7011216		
TESHEL 3.4-030	WO		Tessera Technologies Hungary, LLC	Pending / Unpublished	STACKED MICROELECTRONIC ASSEMBLIES HAVING VIAS EXTENDING THROUGH BOND PADS	PCT/US10/00777		
TESSP101US	US		Tessera Technologies Hungary, LLC	Pending / Unpublished	FIXED FOCAL LENGTH OPTICAL LENS ARCHITECTURE PROVIDING A CUSTOMIZED DEPTH OF FOCUS OPTICAL SYSTEM	12/620,936		
TESHEL 3.3-027	US		Tessera Technologies Hungary, LLC	Pending / Unpublished	STACK PACKAGES USING RECONSTITUTED WAFERS	12/671,993		

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Patent Number	IPC Class	Applicant	Priority Date	IPC Class	IPC Class	IPC Class	IPC Class	IPC Class	IPC Class
TESHEL 3.4-027 (U)	WO	Tessera Technologies Hungary, LLC	Expired / PCT	STACKED PACKAGES USING RECONSTITUTED WAFERS	PCT/US08/09353	WO/02009/020572			
TESHEL 3.4-027 (U) CN	CN	Tessera Technologies Hungary, LLC	Pending / Unpublished	STACKED PACKAGES USING RECONSTITUTED WAFERS	TBA				
TESHEL 3.4-027 (U) EP	EP	Tessera Technologies Hungary, LLC	Pending / Unpublished	STACKED PACKAGES USING RECONSTITUTED WAFERS	8795002.8				
TESHEL 3.4-027 (U) JAPAN	JP	Tessera Technologies Hungary, LLC	Pending / Unpublished	STACKED PACKAGES USING RECONSTITUTED WAFERS	TBA				

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