

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4135727

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the STREET ADDRESS OF THE ASSIGNEE TO C/O CITICO TRUSTEES (CAYMAN) LIMITED previously recorded on Reel 040085 Frame 0441. Assignor(s) hereby confirms the ASSIGNMENT.

CONVEYING PARTY DATA

Name	Execution Date
MICRON TECHNOLOGY, INC.	09/26/2008

RECEIVING PARTY DATA

Name:	APTINA IMAGING CORPORATION
Street Address:	C/O CITICO TRUSTEES (CAYMAN) LIMITED
Internal Address:	REGATTA OFFICE PARK, WEST BAY ROAD
City:	GRAND CAYMAN
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205

PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	8218051

CORRESPONDENCE DATA

Fax Number: (602)244-3169

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 6022443574

Email: patents@onsemi.com

Correspondent Name: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC

Address Line 1: 5005 E. MCDOWELL ROAD

Address Line 2: MAILDROP A700

Address Line 4: PHOENIX, ARIZONA 85008

ATTORNEY DOCKET NUMBER:	APTPBIT-0056C02
NAME OF SUBMITTER:	SHARRON CASTILLO
SIGNATURE:	/Sharron Castillo/
DATE SIGNED:	11/09/2016

Total Attachments: 13

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PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MICRON TECHNOLOGY, INC.	09/26/2008
RECEIVING PARTY DATA	
Name:	APTINA IMAGING CORPORATION
Street Address:	C/O CITIGO TRUSTEES (CAYMAN) LIMITED
Internal Address:	REGATTA OFFICE PARK, WEST BAY ROAD
City:	GRAND CAYMAN
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	8218051
CORRESPONDENCE DATA	
Fax Number:	(602)244-3169
Phone:	6022443574
Email:	patents@onsemi.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC
Address Line 1:	5005 E. MCDOWELL ROAD
Address Line 2:	MAILDROP A700
Address Line 4:	PHOENIX, ARIZONA 85008
ATTORNEY DOCKET NUMBER:	APTPBIT-0056C02
NAME OF SUBMITTER:	SHARRON CASTILLO
Signature:	/Sharron Castillo/
Date:	10/21/2016

PATENT

REEL 040581 FRAME 0690

Total Attachments: 11

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RECEIPT INFORMATION

EPAS ID: PAT4107975
Receipt Date: 10/21/2016

PATENT

PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

①

[REDACTED]

[REDACTED]

[REDACTED]

3

[REDACTED]

[REDACTED]

4.

[REDACTED]

[REDACTED]

5.

[REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: *[Signature]*

Date: 9/26/08, 2008

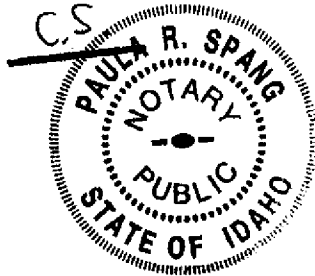
Notarization
State of Idaho
County of Ada

On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Steven R. Appleton*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*
My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: *Thomas L. Law, Jr.*

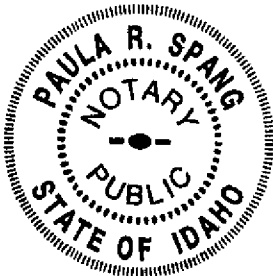
Date: 9/26/08, 2008

Notarization
State of Idaho
County of Ada

On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Thomas L. Law, Jr.*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

IMAGING PATENTS

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
6853045	1998-0272.02/US	PIXEL CELL WITH HIGH STORAGE CAPACITANCE FOR A CMOS IMAGER	United States of America	Feb 8, 2005
6858460	1998-0449.04/US	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	United States of America	Feb 22, 2005
6858829	AVGO-0091.00/US	AVALANCHE PHOTODIODE ARRAY BIASING DEVICE AND AVALANCHE PHOTODIODE STRUCTURE	United States of America	Feb 22, 2005
6859227	PBIT-0084.00/US	ACTIVE PIXEL SENSOR WITH REDUCED FIXED PATTERN NOISE	United States of America	Feb 22, 2005
6861634	2002-0307.00/US	CMOS ACTIVE PIXEL SENSOR WITH A SAMPLE AND HOLD CIRCUIT HAVING MULTIPLE INJECTION CAPACITORS AND A FULLY DIFFERENTIAL CHARGE MODE LINEAR SYNTHESIZER WITH SKEW CONTROL	United States of America	Mar 1, 2005
6861637	2002-0432.01/US	PROVIDING CURRENT TO COMPENSATE FOR SPURIOUS CURRENT WHILE RECEIVING SIGNALS THROUGH A LINE	United States of America	Mar 1, 2005
6870149	PBIT-0072.01/US	Superpixel Multi-Junction Color APS	United States of America	Mar 22, 2005
6870496	PBIT-0088.02/US	LOW VOLTAGE ANALOG-TO-DIGITAL CONVERTERS WITH INTERNAL REFERENCE VOLTAGE AND OFFSET	United States of America	Mar 22, 2005
6870565	PBIT-0060.00/US	SEMICONDUCTOR IMAGING SENSOR ARRAY DEVICES WITH DUAL-PHOTODIGITAL READOUT	United States of America	Mar 22, 2005
6872640	2003-0188.00/US	SOI CMOS DEVICE WITH REDUCED DIBL	United States of America	Mar 29, 2005
6873663	PBIT-0068.00/US	TECHNIQUE FOR REDUCING OVERSATURATED PIXELS	United States of America	Mar 29, 2005
6873364	PBIT-0056.00/US	LOW-POWER SIGNAL CHAIN FOR IMAGE SENSORS	United States of America	Mar 29, 2005
6878568	2002-0057.01/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Apr 12, 2005
6881942	PBIT-0159.00/US	READOUT OF ARRAY-BASED ANALOG DATA IN SEMICONDUCTOR-BASED DEVICES	United States of America	Apr 19, 2005

ATTACHMENT "B"



ATTACHMENT "C"



ATTACHMENT "D"



ATTACHMENT "E"

