

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT4202114

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	PANASONIC CORPORATION	12/12/2016
RECEIVING PARTY DATA		
Name:	SK HYNIX INC.	
Street Address:	2091, GYEONGCHUNG-DAERO, BUBAL-EUB, ICHEON-SI	
City:	GYEONGGI-DO	
State/Country:	KOREA, REPUBLIC OF	
PROPERTY NUMBERS Total: 8		
Property Type	Number	
Patent Number:	6829302	
Patent Number:	6809777	
Patent Number:	6785831	
Patent Number:	7206949	
Patent Number:	7398403	
Patent Number:	6680870	
Patent Number:	8055924	
Patent Number:	8190924	
CORRESPONDENCE DATA		
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ATTORNEY DOCKET NUMBER:	1026-RT-8-1	
NAME OF SUBMITTER:	MYUNG SOO KIM	
SIGNATURE:	/Myung Soo Kim/	
DATE SIGNED:	12/27/2016	

PATENT

Total Attachments: 3

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Appendix B-1 – Transfer Documents

Panasonic Corporation, a Japanese corporation having its principal place of business at 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501, Japan ("**Assignor**"), hereby irrevocably assigns to SK hynix Inc, a Korean corporation with a business address at 2091 Gyeongchung-daero, Bubal-eup, Icheon-si, Gyeonggi-do, Korea ("**Assignee**"), as of the date set forth below, the entire Assignor's right, title, and interest in and to all patents as listed in Appendix A, and any patents or patent applications subject to any terminal disclaimer with regard to such patents and/or patent applications (hereinafter, collectively, "**Patent Assets**"), and all causes of action, rights, and remedies arising under any such Patent Assets prior to, on or after the Effective Date of this Agreement and all claims for damages by reason of past, present or future infringement or other unauthorized use of such Patent Assets with the right to sue for and collect such damages.

Assignor also hereby authorizes the respective patent office of governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the Patent Assets in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this assignment shall inure to the benefit of Assignee, its successors, assigns, and other legal representatives, and shall be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF, Assignor has caused its duly authorized representatives to execute this Assignment.

ASSIGNOR

Panasonic Corporation

By: Koichi Nomura
Name: Koichi Nomura
Title: Authorized signing officer
Date: 2016/12/12

Appendix A

Country	Patent No. / Publication No.	Title of Invention
JP	JP4686048	Apparatus For Image Proceeding
US	US6829302	Apparatus For Image Proceeding
US	US6809777	Apparatus For Pixel For Processing
US	US6785831	Clock Management Mean For Power Reduction
JP	JP4360938	System and method for controlling a clock oscillator and power supply voltage based on the energy consumed for processing a predetermined amount of data
US	US7206949	System and method for controlling a clock oscillator and power supply voltage based on the energy consumed for processing a predetermined amount of data
DE	DE1460519	System and method for controlling a clock oscillator and power supply voltage based on the energy consumed for processing a predetermined amount of data
CN	ZL200410028783.0	System and method for controlling a clock oscillator and power supply voltage based on the energy consumed for processing a predetermined amount of data
DE	DE2431839	System and method for controlling a clock oscillator and power supply voltage based on the energy consumed for processing a predetermined amount of data
JP	JP4402622	A Multi-Processor System Using Synchronize Prediction
CN	ZL200510098045.8	A Multi-Processor System Using Synchronize Prediction
US	US7398403	A Multi-Processor System Using Synchronize Prediction

US	US6680870	Memory Device
US	US8055924	Semiconductor integrated circuit and electric power supply controlling method thereof
JP	JP4940033	Computer system, processor device, and method for controlling computer system
US	US8190924	Computer system, processor device, and method for controlling computer system