

PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	ASSIGNMENT
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Property Type	Number
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DATE SIGNED:	01/04/2017
Total Attachments: 6	
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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Michael C. Stephens, Jr., an individual with an address at 22779 Montrose Court, Los Gatos, CA 95033 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto III Holdings 2, LLC, a Delaware limited liability company, with an address at 2711 Centerville Rd, Suite 400, Wilmington, DE 19808 (“*Assignee*”), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the “*Patent Rights*”):

(a) the provisional patent applications, patent applications and patents listed in the table below (the “*Patents*”);

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
11/809812	US	06/01/2007	Portable device emergency beacon Michael Claude Stephens, Jr.
8599595	US	01/13/2012	Memory devices with serially connected signals for stacked arrangements Michael C. Stephens, Jr.
8559258	US	01/24/2012	Self-refresh adjustment in memory devices configured for stacked arrangements Michael C. Stephens, Jr.
13/365413	US	02/03/2012	Stack position determination in memory devices configured for stacked arrangements Michael C. Stephens
8564999	US	02/23/2012	Pad selection in memory devices configured for stacked arrangements Michael C. Stephens, Jr.
8565029	US	03/21/2012	Supply adjustment in memory devices configured for stacked arrangements Michael C. Stephens, Jr.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
13/455893	US	04/25/2012	Serial searching in memory devices configured for stacked arrangements Michael C. Stephens
8614909	US	05/08/2012	Internal supply testing in memory devices configured for stacked arrangements Michael C. Stephens, Jr.
8659928	US	06/07/2012	Valid command detection based on stack position identifiers in memory devices configured for stacked arrangements Michael C. Stephens, Jr.
13/534279	US	06/27/2012	Latency adjustment based on stack position identifier in memory devices configured for stacked arrangements Michael C. Stephens
13/534402	US	06/27/2012	Signal timing alignment based on a common data strobe in memory devices configured for stacked arrangements Michael C. Stephens
13/964764	US	08/12/2013	Self-refresh adjustment in memory devices configured for stacked arrangements Michael C. Stephens
14/010864	US	08/27/2013	Pad selection in memory devices configured for stacked arrangements Michael C. Stephens

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
8681524	US	09/09/2013	Supply adjustment in memory devices configured for stacked arrangements Michael C. Stephens, Jr.
14/060276	US	10/22/2013	Memory devices with serially connected signals for stacked arrangements Michael C. Stephens
14/071443	US	11/04/2013	Internal supply redundancy across memory devices configured for stacked arrangements Michael C. Stephens
14/105457	US	12/13/2013	Valid command detection based on stack position identifiers in memory devices configured for stacked arrangements Michael C. Stephens

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and/or (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents above and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Assignor represents, warrants and covenants that:

(1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required to enter into this Agreement and to carry out its obligations hereunder, including the assignment of the Patent Rights to Assignee; and

(2) Assignor owns, and by this document assigns to Assignee, all right, title, and interest to the Patent Rights, including, without limitation, all right, title, and interest to sue for infringement of the Patent Rights. Assignor has obtained and properly recorded previously executed assignments for the Patent Rights as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction. The Patent Rights are free and clear of all liens, claims, mortgages, security interests or other encumbrances, and restrictions. There are no actions, suits, investigations, claims or proceedings threatened, pending or in progress relating in any way to the Patent

Rights. There are no existing contracts, agreements, options, commitments, proposals, bids, offers, or rights with, to, or in any person to acquire any of the Patent Rights.

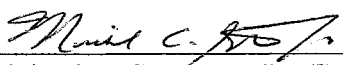
Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Los
64703, CA. on 05/12/2014.

ASSIGNOR:



Michael C. Stephens, Jr. (Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Michael C. Stephens, Jr. to the above Assignment of Patent Rights and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Michael C. Stephens, Jr. is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on May 12, 2014, 2014 to execute the above Assignment of Patent Rights.
3. Michael C. Stephens, Jr. subscribed to the above Assignment of Patent Rights.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on May 12, 2014 (date)



Print Name: Christine Stephens