504185310 01/19/2017

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4231985

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
INTEL TECHNOLOGY INDIA PVT LTD.	11/29/2016

RECEIVING PARTY DATA

Name:	INTEL CORPORATION
Street Address:	2200 MISSION COLLEGE BLVD
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	15101027

CORRESPONDENCE DATA

Fax Number: (603)668-2970

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 520-339-6522

Email:drobertson@gtpp.comCorrespondent Name:EDMUND P. PFLEGERAddress Line 1:C/O CPAGLOBAL

Address Line 2: 900 SECOND AVENUE SOUTH, SUITE 600

Address Line 4: MINNEAPOLIS, MINNESOTA 55402

ATTORNEY DOCKET NUMBER:	P47252US	
NAME OF SUBMITTER:	EDMUND P. PFLEGER	
SIGNATURE:	/Edmund P. Pfleger, Reg. No. 41252/	
DATE SIGNED:	01/19/2017	

Total Attachments: 2

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PATENT 504185310 REEL: 041013 FRAME: 0220

Attorney Docket No.: P47252US

ASSIGNMENT

This agreement is between:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA, and

Intel Technology India Pvt Ltd.

#23-56P, Devarabcesanahalli, Outer Ring Road, Varthur Hobli, Bengaluru-560 103, Karnataka, India.

For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor does hereby sell, assign, transfer and convey to Assignee and its successors and assigns all right, title and interest, in and to any and all inventions and improvements that are disclosed in:

United States Patent Application entitled:

CAPABILITY DETERMINATION FOR COMPUTING RESOURCE ALLOCATION, Serial No. 15/101,027 filed on 06-02-2016 (Attorney Docket No. P47252US),

and in and to said application and all design, utility, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all foreign countries on said inventions and improvements; and in and to all rights of priority resulting from the filing of said United States applications.

11/

AGREED to by Assignor as of the Date of	the Signature Below:
Assignor: Intel Technology India Pvt Ltd.	Signature: Printed/Typed Name: Jitendra Chaddah
	Title: Senior Director, Strategy and Operations
	Date: November 29, 2016
ACCEPTED by Assignee as of the Date of the Signature Below: Assignee: Intel Corporation Signature: Printed/Typed Name: Kerry Tweet	
	Title: Assistant Director of tatals
	Date: January 13, 2017

Page 1 of 1



August 8, 2016

Re: Delegation of Signature Authority

To Whom It May Concern:

I, Jeff Draeger, Vice President for Intel Corporation, hereby delegate authority to sign Powers of Attorney in patent matters before any patent office, Revocations of Power of Attorney in patent matters before any patent office, Patent Assignments, and other patent-related documents to: Libby Hope, Sharon Wong and Kerry Tweet as Assistant Directors, Intel Patent Group, Intel Corporation.

I hereby further delegate authority to sign Powers of Attorney in patent matters before any patent office and Revocations of Power of Attorney in patent matters before any patent office to Lisa Hopkinson and Gwen Olds as Administrators of Patents, Intel Patent Group, Intel Corporation.

Thank you for your cooperation in this matter.

Sincerely,

Jeff Draeger Vice President Intel Patent Group

Intel Corporation

Intel Corporation 2200 Mission College Blvd. Santa Clara, CA 95054

PATENT

REEL: 041013 FRAME: 0222