

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
RAMIN ZANBAGHI	01/31/2017
TEJASVI DAS	01/27/2017
JOHN L. MELANSON	01/25/2017
AXEL THOMSEN	01/16/2017
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15365451
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DATE SIGNED:	02/16/2017
Total Attachments: 3	
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INVENTOR ASSIGNMENT

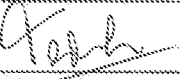
WHEREAS, Ramin Zambaghi, Tejasvi Das, John L. Melanson, and Axel Thomsen, residing at 6612 Estana Lane., Austin, Texas 78739; 14521 Ballimamore Drive, Austin, Texas 78717; 901 W. 9th Street, #201, Austin, Texas 78703, and 2715 W. 49th Street, Austin, Texas 78731, respectively, called the "Assignors," have made the invention described in the United States patent application entitled NOISE REDUCTION IN VOLTAGE REFERENCE SIGNAL, Attorney Docket No. CIRR.P0086US/11608144-(3078) filed on November 30, 2016 and assigned Application U.S. Serial No. 15/365,451, and

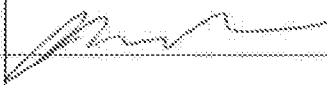
For good and valuable consideration, the receipt and sufficiency of which is acknowledged, I hereby sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd., a wholly-owned subsidiary of Cirrus Logic, Inc. organized under and pursuant to the laws of Scotland and having its principal place of business at Westfield House, 26 Westfield Road, Edinburgh EH11 2QB, United Kingdom, the entire right, title, and interest in and to the invention and all related intellectual property rights (the "Invention") and in and to all applications related to the Invention for Letters Patent in the United States of America, including but not limited to any and all direct and indirect divisions, continuations, continuations-in-part, and reexaminations of said applications and reissues of any resulting patents, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent in the United States of America directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention. Furthermore, for good and valuable consideration, the receipt of which is acknowledged, I hereby also sell, assign, and transfer unto Cirrus Logic International Semiconductor Ltd. the entire right, title, and interest in and to the Invention and in and to all applications related to the Invention for all countries foreign to the United States of America, including but not limited to all rights under any and all international conventions and treaties in respect of the Invention and the applications for Letters Patent, utility models, utility patents, design patents, supplementary protection certificates, patent certificates, and all similar protections in all countries foreign to the United States of America, and I further authorize and assign all rights to Cirrus Logic International Semiconductor Ltd. to apply for Letters Patent, utility models, utility patents, and patent certificates in all countries foreign to the United States directly in its own name, and to claim priority of the filing date of an application for the earliest-filed patent application(s) for the Invention under the provisions of any and all laws, international conventions, treaties, or otherwise.

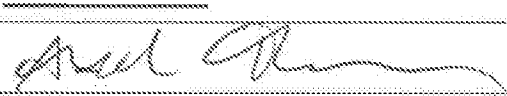
I hereby authorize and request the Commissioner of Patents of the United States of America to issue Letters Patent in the United States of America upon the aforesaid application, and any application related to the aforesaid application in the United States of America, to Cirrus Logic International Semiconductor Ltd., for the sole use and benefit of Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives, to the full end of the term for which the Letters Patent in the United States of America may be granted, the same as they would have been held and enjoyed by me had this assignment not been made. I hereby authorize and request the equivalent authorities in foreign countries to issue the patents of their respective countries to Cirrus Logic International Semiconductor Ltd.

I agree that, when requested, I will, without charge to Cirrus Logic, Inc. or its subsidiaries, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable, or convenient for securing and maintaining any and all patent protection (as described above) for the Invention in any and all countries and for respectively vesting title thereto in Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives or nominees. To the maximum extent allowed by law, I hereby irrevocably designate and appoint Cirrus Logic International Semiconductor Ltd. and its duly authorized officers and agents, or Cirrus Logic International Semiconductor Ltd.'s applicable designee, as my agents and attorneys-in-fact to act for and on my behalf and instead of me to sign all papers, take all rightful oaths, and do all acts described in the foregoing sentence should I refuse or become otherwise unavailable to do so.

Name of First Inventor	<u>Ramin Zambaghi</u>
Signature of First Inventor	
Date	01/31/2017

Name of Second Inventor	<u>Tejasvi Das</u>
Signature of Second Inventor	
Date	1/27/2017

Name of Third Inventor	<u>John L. Melanson</u>
Signature of Third Inventor	
Date	JAN 25 2017

Name of Fourth Inventor	<u>Axel Thomsen</u>
Signature of Fourth Inventor	
Date	JAN 16 2017