PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	ASSIGNMENT
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CONVEYING PARTY DATA

Name	Execution Date
CREDO SEMICONDUCTOR (HONG KONG) LIMITED	02/21/2017

RECEIVING PARTY DATA

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PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	8427353

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DATE SIGNED:	02/23/2017

Total Attachments: 3

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PATENT 504241346 REEL: 041357 FRAME: 0033

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, **Credo Semiconductor (Hong Kong) Limited**, a corporation organized and existing under the laws of Hong Kong and having an address at

Unit 103B, 1/F Enterprise Place (Bldg 9) No. 5 Science Park West Avenue Hong Kong Science Park, Shatin N.T., Hong Kong

(hereinafter referred to as "Assignor") do hereby assign, sell, and convey to Credo Technology Group Limited, a corporation organized and existing under the laws of the Cayman Islands and having an address at

P.O. Box 309 Ugland House, Grand Cayman KY1-1104 Cayman Islands

(hereinafter referred to as "Assignee"), its successors and assigns, the entire right, title and interest throughout the world in and to the patent rights enumerated here, namely:

1. all patent applications and patents currently held by the Assignor, including the patent applications and patents identified in this table:

Filing Date	Dkt/App/Pat#	Title
2009-09-24	US Pat. 8,638,886	Parallel Viterbi decoder with end-state information passing
2009-11-15	US Pat. 8,301,036	High-speed adaptive decision feedback equalizer
2011-02-15	US Pat. 8,427,353	High-speed flash analog to digital converter
2012-08-24	US Pat. 9,071,479	High-speed parallel decision feedback equalizer
2015-12-17	US App 14/972,830	Transition replacement for current leveling in a high-speed transmitter
2015-12-01	US App 62/261,648	Communications link performance analyzer that accommodates forward error correction (US Prv)
2016-11-30	US App 15/365,579	Communications link performance analyzer that accommodates forward error correction (US)
2016-12-01	Dkt# CRDO-008B	Communications link performance analyzer that accommodates forward error correction (CN)

Page 1 of 3

- all claimable inventions disclosed in each of the patent applications and patents in the above table, together with all other patent applications and patents thereon that have now been or may in the future be made by us or our legal representatives;
- 3. all patent applications and patents that directly or indirectly claim priority to any of the patent applications and patents in items 1 and 2, as well as all patent applications and patents that serve as the basis for a direct or indirect priority claim by any of the patent applications and patents in items 1 and 2;
- 4. all reissues, reexaminations, extensions, substitutions, revivals, renewals, continuations, continuations-in-part, continuing prosecution applications, requests for continued examination, divisionals, national phase entries, registrations, foreign counterparts, certificates of invention, utility models, and industrial design protection for the patents and patent applications in items 1 through 3;
- 5. all rights to apply in any and all countries of the world for patents, certificates of invention, utility models, industrial design protection, or other governmental grants or issuances of intellectual property protection based on the inventions, patent applications, patents, and the like described in items 1 through 4, including all international rights of priority associated therewith;
- 6. all causes of action (whether known or unknown, whether currently filed, pending, or otherwise) and other enforcement rights under, or on account of, any patent applications, patents, or the like described in items 1 through 5, including all causes of action or other enforcement rights for damages, injunctive relief, and any other remedies for past, current, or future infringement; and
- 7. all rights to collect royalties and other payments under or on account of the patent applications, patents, and the like described in items 1 through 6.

Assignor hereby authorizes the patent office or other governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, or other governmental grants or issuances of industrial property rights deriving from the foregoing patent rights, in the name of the Assignee as the owner of the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of the Assignee, its successors, assigns, and other legal representatives,

and will be binding upon the Assignor, its successors, assigns, and other legal representatives, who will execute all papers and performs such other acts as may be reasonably necessary to provide the full benefit of this Assignment to the Assignee.

In witness whereof, this Assignment of Patent Rights is executed:		
in <u>Shanghai</u> China	(city, state; or province, country)	
on Feb 2111, 2016 2017	(date)	
by	(signature of Assignor 's representative)	
Joh Lam, COO of Gredo	,(print name & title of representative)	
before the undersigned authority or witnesses who witnessed the execution of the above signature and know it to be that of the above-identified representative who has acknowledged to us that they executed this instrument for the purposes and consideration expressed therein:		
signature(s)		
print name(s) <u>Haoli Qian</u>	- Sheng-Huang	

This Assignment of Patent Rigi effective as of the execution date p	hts is hereby acknowledged and accepted, rovided above:
by Bum	(signature of Assignee 's representative)
William Brennan, CEO	(print name & title of representative)

Page 3 of 3