

PATENT ASSIGNMENT COVER SHEET

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UNIVERSITY OF WASHINGTON	01/31/2017
RECEIVING PARTY DATA	
Name:	National Science Foundation
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Internal Address:	Room 1265
City:	Arlington
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Property Type	Number
Application Number:	15186170
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Invention Title: Fully-Integrated, High-Voltage Compliant, Programmable Electrical Stimulation Systems using Standard CMOS Technology

Inventor(s): Eric Pepin, Jacques Christophe Rudell

U.S. Filing/Issue Date: 06/17/2016

Patent or Application Serial No.: 15/186,170

Grant/Contract Number(s): EEC1028725

Foreign Applications filed/intended in (countries): _____

The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause at **37 CFR 401.14, FAR 52.227-11** or **FAR 52.227-12** (if applicable) which are included among the terms of the above identified grant or contract award from the United States Government. This document is confirmatory of:

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Signed this 31st day of January, 20 17.

By Jesse S. Kindra
(Institutional Business Official)

Jesse S. Kindra
(Signature)

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