

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT4360393

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
LAKSHMINARAYANA PAPPU	04/05/2017
BARUCH SCHNARCH	04/03/2017
CHRISTOPHER J. NELSON	04/05/2017
DANKA GOLDIN SCHWABOVA	04/09/2017
RECEIVING PARTY DATA	
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City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15475879
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:	42P107186
NAME OF SUBMITTER:	SPENCER K. HUNTER
SIGNATURE:	/Spencer K. Hunter/
DATE SIGNED:	04/10/2017
Total Attachments: 5	
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ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

Lakshminarayana Pappu; Baruch Schnarch; Christopher J. Nelson; Danka Goldin Schwabova

hereby sell, assign, and transfer to

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

SYSTEMS, METHODS, AND APPARATUSES FOR IMPLEMENTING FAULT IDENTIFICATION OF A THROUGH SILICON VIA (TSV) IN TWO-LEVEL MEMORY (2LM) STACKED DIE SUBSYSTEMS

(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)

which was filed on March 31, 2017 as


US Application Number 15/475,879 and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.


Inventor 1: **Lakshminarayana Pappu**

09/05/2017
Date Signed

Inventor 2: **Baruch Schnarch**

Date Signed

Inventor 3: **Christopher J. Nelson**

Date Signed

Inventor 4: **Danka Goldin Schwabova**

Date Signed

Inventor 5:

Date Signed

Inventor 6:

Date Signed

Inventor 7:

Date Signed

Inventor 8:

Date Signed

Inventor 9:

Date Signed

Inventor 10:

Date Signed

Inventor 11:

Date Signed

Inventor 12:

Date Signed

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Inventor 1: **Lakshminarayana Pappu**

Date Signed

B. Schnarch
Inventor 2: **Baruch Schnarch**

4/5/17
Date Signed

Inventor 3: **Christopher J. Nelson**

Date Signed

Inventor 4: **Danka Goldin Schwabova**

Date Signed

Inventor 5:

Date Signed

Inventor 6:

Date Signed

Inventor 7:

Date Signed

Inventor 8:

Date Signed

Inventor 9:

Date Signed

Inventor 10:

Date Signed

Inventor 11:

Date Signed

Inventor 12:

Date Signed

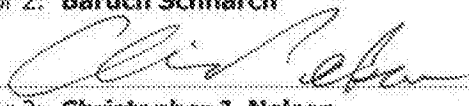
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Inventor 1: **Lakshminarayana Pappu**

Date Signed

Inventor 2: **Baruch Schnarch**

Date Signed


Inventor 3: **Christopher J. Nelson**

April 5, 2017
Date Signed

Inventor 4: **Danka Goldin Schwabova**

Date Signed

Inventor 5:

Date Signed

Inventor 6:

Date Signed

Inventor 7:

Date Signed

Inventor 8:

Date Signed

Inventor 9:

Date Signed

Inventor 10:

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Inventor 1: **Lakshminarayana Pappu**

Date Signed

Inventor 2: **Baruch Schnarch**

Date Signed

Inventor 3: **Christopher J. Nelson**

Date Signed

DANKA GOLDIN SCHWABOVA

Inventor 4: **Danka Goldin Schwabova**

Date Signed

9 APRIL 2012

Inventor 5:

Date Signed

Inventor 6:

Date Signed

Inventor 7:

Date Signed

Inventor 8:

Date Signed

Inventor 9:

Date Signed

Inventor 10:

Date Signed

Inventor 11:

Date Signed

Inventor 12:

Date Signed