

## PATENT ASSIGNMENT COVER SHEET

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<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ATMEL NANTS S.A.S.	04/09/2013
<b>RECEIVING PARTY DATA</b>	
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<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95110
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	15223227
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<b>NAME OF SUBMITTER:</b>	JUDITH A. WITZEL
<b>SIGNATURE:</b>	/Judith A. Witzel/
<b>DATE SIGNED:</b>	04/11/2017
<b>Total Attachments: 3</b>	
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## ASSIGNMENT

WHEREAS, ATMEL NANTES S.A.S, a corporation organized under the laws of France, with a registered address at La Chanterie, BP 70602, 44306 Nantes Cedex 3, FRANCE, (hereinafter "Assignor"), is the owner of the entire right, title, and interest throughout the world in the invention(s) and improvement(s) described in United States patent applications listed in the Appendix appended hereto ("ASSIGNED INTELLECTUAL PROPERTY");

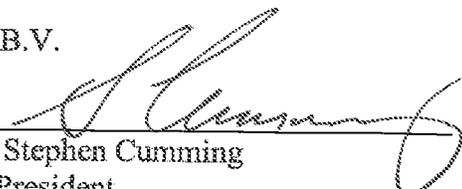
AND WHEREAS, ATMEL CORPORATION, a corporation organized and existing under the laws of the State of Delaware in the United States of America, and having an office and place of business at 1600 TECHNOLOGY DRIVE, SAN JOSE, CALIFORNIA 95110, UNITED STATES OF AMERICA (hereinafter "Assignee"), is desirous of acquiring the entire right, title, and interest throughout the world in the ASSIGNED INTELLECTUAL PROPERTY;

AND WHEREAS, ATMEL NANTES S.A.S, hereby grants the Assignee, its successors, legal representatives and assigns, the power to insert on this instrument any further identification that may be necessary or desirable to comply with the recordation rules of any appropriate and competent authority, including, without limitation, the United States Patent and Trademark Office.

NOW, THEREFORE, to all whom it may concern, be it known that for good and valuable consideration, the receipt and sufficiency whereof is hereby acknowledged, and in accordance with a January 1, 2011 Research and Development Service Agreement between the parties, Assignor does hereby assign, sell, and transfer to Assignee, its successors, and assigns, Assignor's entire right, title, and interest throughout the world in the ASSIGNED INTELLECTUAL PROPERTY, including all right, title, and interest throughout the world that presently exists or that may arise in the future, including, but not limited to, the right to claim priority; all divisionals, continuations, continuations-in-part, or renewals thereof; all patents, utility models, or design registrations that may be granted therefrom, including all reissues, reexamination certificates, or extensions of such patents; all related applications which have been or shall be filed in any country; and all rights, powers, privileges, and immunities arising from the ASSIGNED INTELLECTUAL PROPERTY, together with Assignor's right, title, and interest throughout the world in all causes of action, either in law or equity, for infringement thereof, including all rights of action and damages for past infringement.

For Atmel Nantes S.A.S.

Atmel B.V.

By:   
Name: Stephen Cumming  
Title: President

Date: 4/9/2013

**APPENDIX**

SERIAL NO	FILED	TITLE	MATTER NO
12/768,120	4/27/2010	GENERAL PURPOSE INPUT/OUTPUT PIN MAPPING	20275-0328001 / 09052NTO
13/331,411	12/20/2011	Pulse Width Modulation Controller Architectures	20275-0344001 /11105MCU
13/462,746	5/2/2012	Serial Bit Processor	20275-1198001 / 12002MCU
13/603,155	9/4/2012	REFERENCE VOLTAGE CIRCUITS IN MICROCONTROLLER SYSTEMS	20275-1217001 / 12059MCU
61/676,595	7/27/2012	REFERENCE VOLTAGE CIRCUITS IN MICROCONTROLLER SYSTEMS	20275-1217P01 / 12059MCU/PRV
13/597,939	8/29/2012	PREVENTING INTERFERENCE BETWEEN MICROCONTROLLER COMPONENTS	20275-1218001 / 12064MCU
61/676,605	7/27/2012	PREVENTING INTERFERENCE BETWEEN MICROCONTROLLER COMPONENTS	20275-1218P01 / 12064MCU/PRV
13/753,380	1/29/2013	FETCHING INSTRUCTIONS OF A LOOP ROUTINE	20275-1222001/ 11162MCU
13/687,044	11/28/2012	CONNECTING MULTIPLE SLAVE DEVICES TO A SINGLE MASTER	20275-1223001 / 12090MCU
13/624,651	9/21/2012	BIFURCATED PROCESSOR CHIP RESET ARCHITECTURES	20275-1224001 / 12072MCU
13/624,706	9/21/2012	HOT-PLUGGING DEBUGGER ARCHITECTURES	20275-1225001 / 12081MCU
13/606,515	9/7/2012	MICROCONTROLLER INPUT/OUTPUT CONNECTOR STATE RETENTION IN LOW-POWER MODES	20275-1226001 / 12061MCU
13/617,125	9/14/2012	PACKAGE DEPENDENT SEGMENT TERMINAL REMAPPING FOR DRIVING LIQUID CRYSTAL DISPLAYS	20275-1227001 / 12075MCU
13/608,203	9/10/2012	LOW-POWER MODES OF MICROCONTROLLER OPERATION WITH ACCESS TO CONFIGURABLE INPUT/OUTPUT CONNECTORS	20275-1228001 / 12067MCU
13/788,366	3/7/2013	CHANGING POWER MODES OF A MICROCONTROLLER SYSTEM	20275-1229001 / 12060MCU
61/703,979	9/21/2012	CHANGING POWER MODES OF A MICROCONTROLLER SYSTEM	20275-1229P01 / 12060MCU/PRV

SERIAL NO	FILED	TITLE	MATTER NO
13/789,902	3/8/2013	DELAYING INTERRUPTS IN A MICROCONTROLLER SYSTEM	20275-1230001 / 12065MCU
61/703,998	9/21/2012	DELAYING INTERRUPTS IN A MICROCONTROLLER SYSTEM	20275-1230P01 / 12065MCU/PRV
13/785,999	3/5/2013	DELAYING RESET SIGNALS IN A MICROCONTROLLER SYSTEM	20275-1231001
61/704,037	9/21/2012	DELAYING RESET SIGNALS IN A MICROCONTROLLER SYSTEM	20275-1231P01 / 12066MCU/PRV
13/786,042	3/5/2013	CONFIGURING POWER DOMAINS OF A MICROCONTROLLER SYSTEM	20275-1232001 / 12083MCU
61/704,051	9/21/2012	CONFIGURING POWER DOMAINS OF A MICROCONTROLLER SYSTEM	20275-1232P01 / 12083MCU/PRV
13/614,468	9/13/2012	VOLTAGE SCALING SYSTEM WITH SLEEP MODE	20275-1233001 / 12062MCU
13/614,467	9/13/2012	VOLTAGE SCALING SYSTEM SUPPORTING SYNCHRONOUS APPLICATIONS	20275-1234001 / 12063MCU
13/592,173	8/22/2012	VOLTAGE SCALING SYSTEM	20275-1235001 / 12088MCU
13/624,644	9/21/2012	ADC SEQUENCING	20275-1236001 / 12071MCU
13/692,531	12/3/2012	AUTOMATING DIGITAL DISPLAY	20275-1237001 / 12073MCU
61/704,296	9/21/2012	AUTOMATED DIGITAL DISPLAY	20275-1237P01 / 12073MCU/PRV
13/800,422	3/13/2013	GENERATING KEYS USING SECURE HARDWARE	20275-1239001 / 12105APG
13/798,991	3/13/2013	CURRENT MONITORING CIRCUIT FOR MEMORY WAKEUP TIME	20275-1241001 / 12098MCU
61/704,065	9/21/2012	CURRENT MONITORING CIRCUIT FOR MEMORY WAKEUP TIME	20275-1241P01 / 12098MCU/PRV
13/672,940	11/9/2012	COUNTER CIRCUIT	20275-1249001 / 12130MCU
61/790,421	3/15/2013	MANAGING WAIT STATES FOR MEMORY ACCESS	20275-1257P01 / 13001MCU/PRV
13/797,538	3/12/2013	GENERATING CLOCK ON DEMAND	20275-1258001 / 12159MCU