PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT4353739

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	SECURITY AGREEMENT

CONVEYING PARTY DATA

Name	Execution Date
INTEGRATED DEVICE TECHNOLOGY, INC.	04/04/2017
GIGPEAK, INC.	04/04/2017
MAGNUM SEMICONDUCTOR, INC.	04/04/2017
ENDWAVE CORPORATION	04/04/2017
CHIPX, INCORPORATED	04/04/2017

RECEIVING PARTY DATA

Name:	JPMORGAN CHASE BANK, N.A., AS COLLATERAL AGENT
Street Address:	MAIL CODE NY1-C413, 4 CMC
City:	BROOKLYN
State/Country:	NEW YORK
Postal Code:	11245-0001

PROPERTY NUMBERS Total: 774

Property Type	Number
Patent Number:	5825068
Patent Number:	5831486
Patent Number:	5831899
Patent Number:	5870517
Patent Number:	5887005
Patent Number:	5887006
Patent Number:	5888861
Patent Number:	5889793
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Patent Number:	5956234
Patent Number:	5978307
Patent Number:	5981356
Patent Number:	5982700

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Application Number:	14133261
Patent Number:	RE35869
PCT Number:	US2016059177
Patent Number:	8860481

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Address Line 2:	SUITE 125
Address Line 4:	COLUMBUS, OHIO 43219

NAME OF SUBMITTER:	ELAINE CARRERA
SIGNATURE:	/Elaine Carrera/
DATE SIGNED:	04/05/2017

Total Attachments: 49
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Patent Security Agreement

This **Patent Security Agreement** dated as of April 4, 2017 made by Integrated Device Technology, Inc., ChipX, Incorporated, ENDWAVE CORPORATION, GigPeak, Inc. and Magnum Semiconductor, Inc. as pledgors and debtors (in such capacities and together with any successors in such capacities, individually, a "Pledgor", and, collectively, the "Pledgors"), in favor of JPMORGAN CHASE BANK, N.A., in its capacity as Collateral Agent pursuant to the Credit Agreement (as defined in the Security Agreement), as pledgee, assignee and secured party (in such capacities and together with any successors in such capacities, the "Collateral Agent").

W I T N E S S E T H:

WHEREAS, the Pledgors are party to a Security Agreement of even date herewith (as amended, amended and restated, supplemented or otherwise modified from time to time in accordance with the provisions thereof, the "Security Agreement") made in favor of the Collateral Agent pursuant to which the Pledgors are required to execute and deliver this Patent Security Agreement;

NOW, THEREFORE, in consideration of the foregoing premises and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, and to induce the Collateral Agent, for the benefit of the Secured Parties, to enter into the Credit Agreement, the Pledgors and the Collateral Agent hereby agree as follows:

SECTION 1.   Defined Terms. Unless otherwise defined herein, terms defined in the Security Agreement and used herein have the meaning given to them in the Security Agreement.

SECTION 2.   Grant of Security Interest in Patent Collateral. As collateral security for the payment and performance in full of all Secured Obligations, each Pledgor hereby pledges and grants to the Collateral Agent for the benefit of the Secured Parties, a lien on and security interest in all of the right, title and interest of such Pledgor in, to and under the following property, wherever located, and whether now existing or hereafter arising or acquired from time to time (collectively, the "Patent Collateral"):

(a) all patents and all patent applications (whether issued, allowed or filed in the United States or any other country or any trans-national patent registry) of such Pledgor now or hereafter, owned, filed or acquired by, or assigned to, such Pledgor, including the patents and patent applications listed on Schedule I attached hereto, together with any and all (i) rights and privileges arising under applicable law with respect to the foregoing and all rights corresponding thereto throughout the world, (ii) inventions, discoveries, designs and improvements described or claimed therein, (iii) reissues, divisions, continuations, reexaminations, extensions and continuations-in-part thereof and amendments thereto and (iv) rights to sue for past, present and future infringements thereof; and

(b) all Proceeds of any and all of the foregoing.

Notwithstanding anything to the contrary contained in clauses (a) and (b) above or otherwise set forth in this Patent Security Agreement, the security interest created by this Patent Security Agreement shall not extend to, and the term "Patent Collateral" shall not include, any Excluded Property.

SECTION 3.   Security Agreement. The lien and security interest granted pursuant to this Patent Security Agreement is granted in conjunction with the lien and security interest granted to the

Collateral Agent pursuant to the Security Agreement and the Pledgors hereby acknowledge and affirm that the rights and remedies of the Collateral Agent with respect to the security interest in the Patent Collateral made and granted hereby are more fully set forth in the Security Agreement, the terms and provisions of which are incorporated by reference herein as if fully set forth herein. In the event that any provision of this Patent Security Agreement is deemed to conflict with the Security Agreement, the provisions of the Security Agreement shall control unless the Collateral Agent shall otherwise determine.

SECTION 4. Termination. Upon the payment in full of the Secured Obligations (other than contingent obligations for which no claim has been asserted) and termination of the Security Agreement and at the other times required by Section 9.15 of the Credit Agreement, the Collateral Agent shall promptly, upon reasonable request of the Pledgors, execute, acknowledge, and deliver to the Pledgors an instrument in writing in recordable form releasing the collateral pledge, grant, assignment, lien and security interest in the Patent Collateral under this Patent Security Agreement, all at the Pledgors' sole cost and expense.

SECTION 5. Counterparts. This Patent Security Agreement and any amendments, waivers, consents or supplements hereto may be executed in any number of counterparts and by different parties hereto in separate counterparts, each of which when so executed and delivered shall be deemed to be an original, but all such counterparts together shall constitute one and the same instrument. Delivery of any executed counterpart of a signature page of this Patent Security Agreement by facsimile or other electronic transmission shall be effective as delivery of a manually executed counterpart of this Patent Security Agreement.

SECTION 6. Governing Law; Consent to Jurisdiction and Service of Process; Waiver of Jury Trial. Sections 9.09 and 9.10 of the Credit Agreement are incorporated herein, *mutatis mutandis*, as if a part hereof.

[signature page follows]

IN WITNESS WHEREOF, each Pledgor and the Collateral Agent have caused this Patent Security Agreement to be duly executed and delivered by their duly authorized officers as of the date first above written.

INTEGRATED DEVICE TECHNOLOGY, INC.,
as Pledgor

By:

Name: Brian C. White
Title: Vice President and Chief Financial Officer
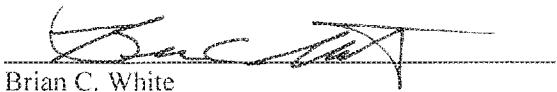
GIGPEAK, INC.,
as Pledgor

By:
Name: Brian C. White
Title: Treasurer and Chief Financial Officer
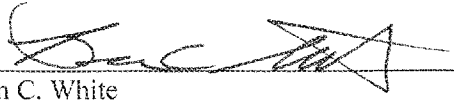
MAGNUM SEMICONDUCTOR, INC.,
as Pledgor

By: _____
Name: Brian C. White
Title: Treasurer and Chief Financial Officer

ENDWAVE CORPORATION,
as Pledgor

By: _____
Name: Brian C. White
Title: Treasurer and Chief Financial Officer

CHIPX, INCORPORATED,
as Pledgor

By: _____
Name: Brian C. White
Title: Treasurer and Chief Financial Officer

AGREED TO AND ACCEPTED:

JPMORGAN CHASE BANK, N.A.,
as Collateral Agent

By: _____
     Name: Caitlin Stewart
     Title: Vice President

Registrations:

Owner*	Patent No.	Title
Integrated Device Technology, Inc.	5,825,068	Integrated Circuits that Include a Barrier Layer Reducing Hydrogen Diffusioni into a Polysilicon Resistor, and Methods for Fabricating such Circuits
Integrated Device Technology, Inc.	5,831,486	An Extended Range Current Controlled Oscillator
Integrated Device Technology, Inc.	5,831,899	Local Interconnect Structure and Process for Six-Transistor SRAM Cell
Integrated Device Technology, Inc.	5870517	Package Including Self-Aligned Laser Diode And Method Of Aligning A Laser Diode
Integrated Device Technology, Inc.	5,887,005	Method and Apparatus for Error Correction
Integrated Device Technology, Inc.	5,887,006	Methods and Apparatus for Error Correction Timed
Integrated Device Technology, Inc.	5,888,861	Method of Manufacturing a BiCMOS Integrated Circuit Fully Integrated within a CMOS Process Flow
Integrated Device Technology, Inc.	5,889,793	Methods and Apparatus for Error Correction
Integrated Device Technology, Inc.	5910922	Method for Testing Data Retention in a Static Random Access Memory Using Isolated Vcc Supply
Integrated Device Technology, Inc.	5,913,103	Method Of Detecting Metal Contaminants In A Wet Chemical Using Enhanced Semiconductor Growth Phenomena
Integrated Device Technology, Inc.	5939762	SRAM Cell Using Thin Gate Oxide Pulldown Transistors
Integrated Device Technology, Inc.	5,949,127	Electrically Programmable Interlevel Fusible Link for Integrated Circuits
Integrated Device Technology, Inc.	5,956,234	Method And Structure For A Surface Mountable Rigid-Flex Printed Circuit Board
Integrated Device Technology, Inc.	5978307	Integrated Circuit Memory Devices Having Partitioned Multi-Port Memory Arrays Therein For Increasing Data Bandwidth And Methods Of Operating Same
Integrated Device Technology, Inc.	5,981,356	IsolationTrenches With Protected Corners
Integrated Device Technology, Inc.	5982700	Buffer Memory Arrays Having Nonlinear Columns for Providing Parallel Data Access Capability and Methods of Operatiang Same

Integrated Device Technology, Inc.	5,986,885	Semiconductor Package With Internal Heatsink And Assembly Method
Integrated Device Technology, Inc.	5,987,031	Method for Fair Dynamic Scheduling of ABR Service Under ATM
Integrated Device Technology, Inc.	5,994,945	Circuit For Compensating For Variations In Both Temperature And Supply Voltage
Integrated Device Technology, Inc.	5,999,478	Highly Integrated Tri-Port Memory Buffers Having Fast Fall-Through Capability and Methods of Operating Same
Integrated Device Technology, Inc.	6,025,260	Method For Fabricating Air Gap With Borderless Contact
Integrated Device Technology, Inc.	6037807	Synchronous Sense Amplifier With Temperature And Voltage Compensated Translator
Integrated Device Technology, Inc.	6,043,129	High Density MOSFET with Raised Source and Drain Regions
Integrated Device Technology, Inc.	6,063,676	MOSFET With Raised Source And Drain Regions
Integrated Device Technology, Inc.	6,069,054	A Method for Forming Isolation Regions Subsequent to Gate Formation and Structure Thereof
Integrated Device Technology, Inc.	6,069,782	ESD Damage Protection Using A Clamp Circuit
Integrated Device Technology, Inc.	6,081,478	Separate Byte Control On Fully Synchronous Pipelined SRAM
Integrated Device Technology, Inc.	6,091,260	Integrated Circuit Output Buffers Having Low Propagation Delay And Improved Noise Characteristics
Integrated Device Technology, Inc.	6,100,128	Process For Making Six Transistor SRAM Cell Local Interconnect Structure (As Amended)
Integrated Device Technology, Inc.	6,111,199	Method For Using A Gas To Insulate Electrical Conductors In An Integrated Circuit Package And The Resulting Package
Integrated Device Technology, Inc.	6,114,840	Signal Transfer Devices Having Self-Timed Booster Circuits Therein
Integrated Device Technology, Inc.	6,115,320	Separate Byte Control On Fully Synchronous Pipelined SRAM
Integrated Device Technology, Inc.	6,128,703	Method and Apparatus for Memory Prefetch Operation of Volatile Non-Coherent Data
Integrated Device Technology, Inc.	6,130,563	Output Driver Circuit For High Speed Digital Signal Transmission
Integrated Device Technology, Inc.	6,136,687	Method of Forming Air Gaps for Reducing Interconnect Capacitance
Integrated Device Technology, Inc.	6,137,779	Transmission Rate Calculation Scheme Using Table Lookup
Integrated Device Technology, Inc.	6,154,064	Differential Sense Amplifier Circuit
Integrated Device Technology, Inc.	6,157,939	Methods And Apparatus For Generating Multiplicative Inverse Product
Integrated Device Technology, Inc.	6,165,918	A Method for Forming Gate Oxides of Different Thicknesses

Integrated Device Technology, Inc.	6,167,047	Packet Classification State Machine
Integrated Device Technology, Inc.	6,173,425	Methods Of Testing Integrated Circuits To Include Data Traversal Path Identification Information And Related Status Information In Test Data Streams
Integrated Device Technology, Inc.	6,191,460	Identical Gate Conductivity Type Static Random Access Memory Cell
Integrated Device Technology, Inc.	6,204,557	Reduction Of Topside Movement During Temperature Cycles
Integrated Device Technology, Inc.	6,216,205	Methods Of Controlling Memory Buffers Having Tri-Port Cache Arrays Therein
Integrated Device Technology	6,226,756	Apparatus And Method For Providing A Common System Interface For Processors
Integrated Device Technology, Inc.	6,232,647	Air Gap With Borderless Contact
Integrated Device Technology, Inc.	6,236,697	Clock Recovery for Multiple Frequency Input Data
Integrated Device Technology, Inc.	6,242,942	Integrated Circuit Output Buffers Having Feedback Switches Therein For Reducing Simultaneous Switching Noise And Improving Impedance Matching Characteristics
Integrated Device Technology, Inc.	6,287,894	ACOUSTIC DEVICE PACKAGED AT WAFER LEVEL (ICS case)
Integrated Device Technology, Inc.	6,304,196	Disparity And Transition Density Control System And Method
Integrated Device Technology, Inc.	6,306,771	Process For Preventing The Formation Of Ring Defects
Integrated Device Technology, Inc.	6,307,399	High Speed Buffer Circuit With Improved Noise Immunity
Integrated Device Technology, Inc.	6,307,757	High Frequency Switch-Mode DC Powered Computer System
Integrated Device Technology, Inc.	6,462,964	High Frequency Power Generator and Related Methods
Integrated Device Technology, Inc.	6,486,058	Method Of Forming A Photoresist Pattern
Integrated Device Technology, Inc.	6,528,842	Electronically Erasable Memory Cell Using CMOS Technology
Integrated Device Technology, Inc.	6,545,450	Multiple Power Converter System Using Combining Transformers
Integrated Device Technology, Inc.	6,570,405	INTEGRATED OUTPUT DRIVER CIRCUITS HAVING CURRENT SOURCING AND CURRENT SINKING CHARACTERISTICS THAT INHIBIT POWER BOUNCE AND GROUND BOUNCE
Integrated Device Technology, Inc.	6,583,992	High Frequency Powe System and Related Methods
Integrated Device Technology, Inc.	6,590,786	System for Controlling the Delivery of Power to DC Computer Components Utilizing Phase Shift Regulation
Integrated Device Technology, Inc.	6,650,156	IC CHARGE PUMPS HAVING CONTROL CIRCUITS THEREIN THAT INHIBIT PARASITIC CHARGE INJECTION FROM CONTROL SIGNAL

Integrated Device Technology, Inc.	6,686,221	METHOD FOR FORMING PACKAGED SEMICONDUCTOR DEVICE HAVING STACKED DIE
Integrated Device Technology, Inc.	6,686,727	Method for Power Conversion Using Combining Transformer
Integrated Device Technology, Inc.	6,694,438	System for controlling the Delivery of Power to DC Computer Components
Integrated Device Technology, Inc.	6,696,823	Low Voltage, High Current DC Computer Power System
Integrated Device Technology, Inc.	6,700,425	MULTI-PHASE CLOCK GENERATORS THAT UTILIZE DIFFERENTIAL SIGNALS TO ACHIEVE REDUCED SETUP AND HOLD TIMES
Integrated Device Technology, Inc.	6,720,786	Lead Formation, Assembly Strip Test, And Singulation System
Integrated Device Technology, Inc.	6,724,601	ESD Protection Circuit
Integrated Device Technology, Inc.	6,731,231	High Speed High Resolution ADC Architecture
Integrated Device Technology, Inc.	6,733,936	METHOD FOR GENERATING A SWING CURVE AND PHOTORESIST FEATURE FORMED USING SWING CURVE
Integrated Device Technology, Inc.	6,736,946	PHYSICAL VAPOR DEPOSITION APPARATUS WITH MODIFIED SHUTTER DISK AND COVER RING
Integrated Device Technology, Inc.	6,740,549	Gate Structures Having Sidewall Spacers Using Selective Deposition And Method Of Forming The Same
Integrated Device Technology, Inc.	6,754,777	FIFO MEMORY DEVICES AND METHODS OF OPERATING FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY DEVICES THEREIN
Integrated Device Technology, Inc.	6,765,269	Conformal Surface Silicide Strap On Spacer And Method Of Making Same
Integrated Device Technology, Inc.	6,778,454	FIFO Memory Devices Having Single Data Rate (SDR) And Dual Data Rate (DDR) Capability
Integrated Device Technology, Inc.	6,791,155	STRESS RELIEVED STI STRUCTURE AND FORMATION OF SUCH
Integrated Device Technology, Inc.	6,791,197	METHOD FOR REDUCING LAYER SEPARATION OR CRACKING ON INTEGRATED CIRCUITS
Integrated Device Technology, Inc.	6,791,848	Autobias Driving a High Frequency Power System
Integrated Device Technology, Inc.	6,795,360	FIFO Memory Devices That Support all Four Combinations of DDR or SDR Write Modes with DDR or SDR Read Modes
Integrated Device Technology, Inc.	6,797,456	Dual-layer deep ultraviolet photoresist process and structure
Integrated Device Technology, Inc.	6,798,039	INTEGRATED CIRCUIT INDUCTORS HAVING HIGH QUALITY FACTORS
Integrated Device Technology, Inc.	6,798,629	Overvoltage Protection Circuits
Integrated Device Technology, Inc.	6,806,154	Salicided MOSFET Structure With Tunable Oxynitride Spacer
Integrated Device Technology, Inc.	6,846,751	Nitrogen Implantation To Minimize Device Variation

Integrated Device Technology, Inc.	6,861,366	PACKAGED SEMICONDUCTOR DEVICE HAVING STACKED DIE - DIVISIONAL
Integrated Device Technology, Inc.	6,861,751	Etchstop Layer for Use in a Self-Aligned Contact Etch
Integrated Device Technology, Inc.	6,867,627	DELAY-LOCKED LOOP (DLL) INTEGRATED CIRCUITS HAVING HIGH BANDWIDTH AND RELIABLE LOCKING CHARACTERISTICS
Integrated Device Technology, Inc.	6,872,668	Multi-Step Tungsten Etchback Process To Preserve Barrier Integrity In An Integrated Circuit Structure
Integrated Device Technology, Inc.	6,874,064	FIFO MEMORY DEVICES AND METHODS OF OPERATING FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY DEVICES THEREIN
Integrated Device Technology, Inc.	6,882,195	SIGNAL TIMING ADJUSTMENT CIRCUIT WITH AN EXTERNAL RESISTOR (ICS case)
Integrated Device Technology, Inc.	6,894,529	Impedance Matching Output Driver (LINEAR)
Integrated Device Technology, Inc.	6,907,479	INTEGRATED CIRCUIT FIFO MEMORY DEVICES THAT ARE DIVISIBLE INTO INDEPENDENT FIFO QUEUES, AND SYSTEMS AND METHODS FOR CONTROLLING SAME
Integrated Device Technology, Inc.	6,913,872	DUAL-WAVELENGTH EXPOSURE FOR REDUCTION OF IMPLANT SHADOWING
Integrated Device Technology, Inc.	6,924,683	Edge accelerated sense-amp flip-flop with high fanout
Integrated Device Technology, Inc.	6,930,400	GRID ARRAY MICROELECTRONIC PACKAGES WITH INCREASED PERIPHERY
Integrated Device Technology, Inc.	6,934,816	Integrated circuit memory devices having asynchronous flow-through capability (AKA ASYNC Multi-Port Memory Device With Loopback Mode (APPARATUS)
Integrated Device Technology, Inc.	6,961,251	Waveform Independent High Frequency Power System
Integrated Device Technology, Inc.	6,965,272	World Wide Marketing Logistics Network Including Strategically Located Centers for Frequency Programming Crystal Oscillators to Customer Specification
Integrated Device Technology, Inc.	6,967,501	Impedance-matched output driver circuits having enhanced predriver control (FKA An Output Impedance Matching Circuit Using Gate Voltage Control)
Integrated Device Technology, Inc.	6,975,527	Memory Device Layout
Integrated Device Technology, Inc.	7,015,116	Stress-relieved shallow trench isolation (STI) structure and method for forming the same
Integrated Device Technology, Inc.	7,016,987	Transaction Aligner Microarchitecture
Integrated Device Technology, Inc.	7,037,774	SELF-ALIGNED CONTACT STRUCTURE AND PROCESS FOR FORMING SELF-ALIGNED CONTACT STRUCTURE

Integrated Device Technology, Inc.	7,042,792	MULTI-PORT MEMORY CELLS FOR USE IN FIFO APPLICATIONS THAT SUPOPRT DATA TRANSFERS BETWEEN CACHE AND SUPPLEMENTAL MEMORY ARRAYS
Integrated Device Technology, Inc.	7,046,058	Delayed-Locked Loop with Fine and Coarse Control Using Cascaded Phase Interpolator and Variable Delay Circuit
Integrated Device Technology, Inc.	7,053,661	Impedance-matched output driver circuits having enhanced predriver control
Integrated Device Technology, Inc.	7,067,364	Gate Structures Having Sidewall Spacers Using Selective Deposition And Method Of Forming The Same
Integrated Device Technology, Inc.	7,069,406	Double Data Rate Synchronous SRAM With 100% Bus Utilization
Integrated Device Technology, Inc.	7,071,767	Precise Voltage/Current Reference Circuit Using Current-Mode Technique in CMOS Technology
Integrated Device Technology, Inc.	7,079,446	DRAM INTERFACE CIRCUITS HAVING ENHANCED SKEW, SLEW RATE AND IMPEDANCE CONTROL
Integrated Device Technology, Inc.	7,082,071	INTEGRATED DDR/SDR FLOW CONTROL MANAGERS THAT SUPPORT MULTIPLE QUEUES AND MUX, DEMUX AND BROADCAST OPERATING MODES
Integrated Device Technology, Inc.	7,092,479	Ripple Counter Circuits in IC Devices Having Fast Terminal Count Capability and Methods of Operating the Same (FKA - Ripple Counter Programmable Dividers)
Integrated Device Technology, Inc.	7,093,047	INTEGRATED CIRCUIT MEMORY DEVICED HAVING CLOCK SIGNAL ARBITRATION CIRCUITS THEREIN AND METHODS OF PERFORMING CLOCK SIGNAL ARBITRATION
Integrated Device Technology, Inc.	7,093,083	Integrated circuit memory devices having asynchronous flow-through capability
Integrated Device Technology, Inc.	7,098,114	Method for forming cmos device with self-aligned contacts and region formed using salicide process
Integrated Device Technology, Inc.	7,099,231	INTERLEAVING MEMORY BLOCKS TO RELIEVE TIMING BOTTLENECK IN A MULTI-QUEUE FIRST-IN FIRST-OUT MEMORY SYSTEM
Integrated Device Technology, Inc.	7,102,862	Electrostatic Discharge Protection Circuit
Integrated Device Technology, Inc.	7,109,760	Delay-locked loop (DLL) integrated circuits that support efficient phase locking of clock signals having non-unity duty cycles (fka DIGITAL PHASE COMPARATOR UNAFFECTED BY VARIABLE CLOCK DUTY CYCLE)
Integrated Device Technology, Inc.	7,110,266	Multiple Element Rectification Circuit

Integrated Device Technology, Inc.	7,110,321	Multi-Bank Integrated Circuit Memory Devices Having High-Speed Memory Access Timing (fka: NOVEL MEMORY ARCHITECTURE (Partition of Memory to reduce clock cycle (increase frequency) (fka: Partition of Memory to reduce Impedance, QDR architecture
Integrated Device Technology, Inc.	7,110,400	Random Access Memory Architecture and Serial Interface with Continuous Packet Handling Capability
Integrated Device Technology, Inc.	7,119,591	Delay-locked loop (DLL) integrated circuits having binary-weighted delay chain units with built-in phase comparators that support efficient phase locking (fka DIGITAL DELAY LOCKED LOOP INTEGRATED CIRCUITS AND PHASE COMPARATORS THAT SUPPORT CLOCK SIGNALS
Integrated Device Technology, Inc.	7,120,075	Multi-FIFO integrated circuit devices that support multi-queue operating modes with enhanced write path and read path queue switching
Integrated Device Technology, Inc.	7,120,655	DIGITAL FILTER (ICS case)
Integrated Device Technology, Inc.	7,125,775	METHOD FOR FORMING HYBRID DEVICE GATES
Integrated Device Technology, Inc.	7,125,783	Dielectric Anti-Reflective Coating Surface Treatment To Prevent Defect Generation In Associated Wet Clean
Integrated Device Technology, Inc.	7,126,389	METHOD AND APPARATUS FOR AN OUTPUT BUFFER WITH DYNAMIC IMPEDANCE CONTROL
Integrated Device Technology, Inc.	7,126,911	Timer Rollover Handling Mechanism For Traffic Policing
Integrated Device Technology, Inc.	7,133,951	Alternate Set Of Registers To Service Critical Interrupts And Operating System Traps
Integrated Device Technology, Inc.	7,154,327	SELF-TIMED MULTIPLE BLANKING FOR NOISE SUPPRESSION DURING FLAG GENERATION IN A MULTI-QUEUE FIRST-IN FIRST-OUT MEMORY SYSTEM
Integrated Device Technology, Inc.	7,158,440	FIFO MEMORY DEVICES HAVING WRITE AND READ CONTROL CIRCUITS THAT SUPPORT X4N, X2N AND XN DATA WIDTHS DURING DDR AND SDR MODES OF OPERATION
Integrated Device Technology, Inc.	7,162,673	SCAN CHAIN REGISTERS THAT UTILIZE FEEDBACK PATHS WITHIN LATCH UNITS TO SUPPORT TOGGLING OF LATCH UNIT OUTPUTS DURING ENHANCED DELAY FAULT TESTING
Integrated Device Technology, Inc.	7,163,881	CMOS STRUCTURE WITH VOID-FREE DIELECTRIC FILM
Integrated Device Technology, Inc.	7,166,905	Stacked Paddle Micro Lead Frame Package
Integrated Device Technology, Inc.	7,166,966	Penlight and Touchscreen Data Input System and Method for Flat Panel Displays
Integrated Device Technology, Inc.	7,167,997	APPARATUS AND METHOD FOR LIMITING DATA TRANSMISSION RATES

Integrated Device Technology, Inc.	7,176,104	Method for Forming Shallow Trench Isolation Structure With Deep Oxide Region
Integrated Device Technology, Inc.	7,197,138	PROGRAMMABLE DEVICE AND METHODS FOR DISABLING A RING SIGNAL IN RESPONSE TO A DETECTED OFF-HOOK CONDITION
Integrated Device Technology, Inc.	7,209,983	SEQUENTIAL FLOW CONTROL AND FIFO MEMORY DEVICES THAT ARE DEPTH EXPANDABLE IN STANDARD MODE OPERATION
Integrated Device Technology, Inc.	7,214,990	MEMORY CELL WITH REDUCED SOFT ERROR RATE
Integrated Device Technology, Inc.	7,224,195	OUTPUT DRIVE CIRCUIT THAT ACCOMMODATES VARIABLE SUPPLY VOLTAGES
Integrated Device Technology, Inc.	7,227,400	HIGH SPEED MOSFET OUTPUT DRIVER
Integrated Device Technology, Inc.	7,227,423	Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,227,424	Tranceconductance & Current Modulation for Resonant Frequency Control and Selection
Integrated Device Technology, Inc.	7,235,336	AN ACCURATE MINIMUM RESIST THICKNESS DECIDED BY A SINGLE WAFER PROCESS
Integrated Device Technology, Inc.	7,239,188	Locked-Loop Integrated Circuits Having Speed Tracking Circuits Therein
Integrated Device Technology, Inc.	7,246,277	Test Bus Architecture For Embedded RAM And Method Of Operating Same
Integrated Device Technology, Inc.	7,246,300	Sequential Flow-Control and FIFO Memory Devices Having Error Detection and Correction Capability With Diagnostic Bit Generation
Integrated Device Technology, Inc.	7,248,124	Frequency Calibration For a Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,257,115	DATA MEMORY ADDRESS GENERATION FOR TIME-SLOT INTERCHANGE SWITCHES
Integrated Device Technology, Inc.	7,257,687	Synchronization of Active Flag and Status Bus Flags in a Multi-Queue First-In First-Out Memory Status
Integrated Device Technology, Inc.	7,259,614	VOLTAGE SENSING CIRCUIT
Integrated Device Technology, Inc.	7,260,093	TIME-SLOT INTERCHANGE SWITCHES HAVING EFFICIENT BLOCK PROGRAMMING AND ON-CHIP BYPASS CAPABILITIES AND METHODS OF OPERATING SAME
Integrated Device Technology, Inc.	7,266,128	TIME-SLOT INTERCHANGE SWITCHES HAVING EFFICIENT BLOCK PROGRAMMING AND ON-CHIP BYPASS CAPABILITIES AND METHODS OF OPERATING SAME
Integrated Device Technology, Inc.	7,269,700	Status Bus Accessing Only Available Quadrans During Loop Mode Operation In a Multi-Queue First-In First-Out Memory System

Integrated Device Technology, Inc.	7,275,188	METHOD AND APPARATUS FOR BURN-IN OF SEMICONDUCTOR DEVICES
Integrated Device Technology, Inc.	7,279,938	Delay Chain Integrated Circuits Having Binary-Weighted Delay Chain Units with Built-In Phase Comparators Therein
Integrated Device Technology, Inc.	7,286,438	Dual Port Memory Cell With Reduced Coupling Capacitance and Small Cell Size
Integrated Device Technology, Inc.	7,296,109	Buffer Bypass Circuit for Reducing Latency In Information Transfers to a Bus
Integrated Device Technology, Inc.	7,298,636	Packet Processors Having Multi-Functional Range Match Cells Therein
Integrated Device Technology, Inc.	7,307,486	Low Latency Start-Up for a Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,321,238	OVER-VOLTAGE TOLERANT MULTIFUNCTION INPUT STAGE
Integrated Device Technology, Inc.	7,327,044	Integrated Circuit Package Encapsulating a Hermetically Sealed Device
Integrated Device Technology, Inc.	7,327,367	METHOD AND APPARATUS FOR FONT PROCESSING
Integrated Device Technology, Inc.	7,327,814	AMPLITUDE AND BANDWIDTH PRE-EMPHASIS OF A DATA SIGNAL
Integrated Device Technology, Inc.	7,329,958	Method and Apparatus with Power and Ground Strips for Connecting to Decoupling Capacitors
Integrated Device Technology, Inc.	7,330,145	Dual Residue Pipe-Line ADC
Integrated Device Technology, Inc.	7,336,780	Differential Signaling Transmission Circuit
Integrated Device Technology, Inc.	7,342,420	LOW POWER OUTPUT DRIVER
Integrated Device Technology, Inc.	7,349,752	DYNAMICALLY COUPLED METROLOGY AND LITHOGRAPHY
Integrated Device Technology, Inc.	7,350,202	PACKET CLASSIFICATION STATE MACHINE
Integrated Device Technology, Inc.	7,353,345	EXTERNAL OBSERVATION AND CONTROL OF DATA IN A COMPUTING PROCESSOR
Integrated Device Technology, Inc.	7,358,826	Discrete Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,365,614	Integrated Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,368,938	INPUT TERMINATION CIRCUITRY WITH HIGH IMPEDANCE AT POWER OFF
Integrated Device Technology, Inc.	7,375,392	Gate Structures Having Sidewall Spacers Formed Using Selective Deposition
Integrated Device Technology, Inc.	7,378,289	Method for forming photomask having test patterns in blading areas
Integrated Device Technology, Inc.	7,378,876	Complementary Output Inverter
Integrated Device	7,382,159	HIGH VOLTAGE INPUT BUFFER

Technology, Inc.		
Integrated Device Technology, Inc.	7,388,262	NITROGEN IMPLEMENTATION TO MINIMIZE DEVICE VARIATION
Integrated Device Technology, Inc.	7,390,122	AUTOMATED BALL MOUNTING PROCESS WITH SOLDER BALL TESTING
Integrated Device Technology, Inc.	7,392,354	Multi-Queue FIFO Memory Devices that Support a Backed-Off Standard Mode of Operation and Methods of Operating Same
Integrated Device Technology, Inc.	7,392,924	AUTOMATED BALL MOUNTING SYSTEM WITH SOLDER BALL TESTING
Integrated Device Technology, Inc.	7,394,414	Cancellation of Random Errors of Unit Elements in a DAC
Integrated Device Technology, Inc.	7,400,026	THIN FILM RESISTOR STRUCTURE
Integrated Device Technology, Inc.	7,405,594	Current mode driver with constant voltage swing
Integrated Device Technology, Inc.	7,408,751	SELF-BIASED Electrostatic Discharge Protection Method and Circuit
Integrated Device Technology, Inc.	7,414,460	System and Method for Integrated Circuit Charge Recycling
Integrated Device Technology, Inc.	7,419,748	PHOTOMASK WITH REDUCED ELECROSTATIC DISCHARGE DEFECTS
Integrated Device Technology, Inc.	7,436,224	Low Variation Voltage Output Differential for Differential Drivers
Integrated Device Technology, Inc.	7,436,865	Tunable Optical Oscillator
Integrated Device Technology, Inc.	7,443,747	MEMORY ARRAY BIT LINE COUPLING CAPACITOR CANCELLATION
Integrated Device Technology, Inc.	7,447,812	Multi-Queue FIFO Memory Devices that Support Flow-Through of Write and Read Counter Updates Using Multi-Port Flag Counter Register Files
Integrated Device Technology, Inc.	7,456,699	Frequency Controller for a Monolithic Clock Generator and Frequency/Timing Reference
Integrated Device Technology, Inc.	7,472,322	ON-CHIP INTERFACE TRAP CHARACTERIZATION AND MONITORING
Integrated Device Technology, Inc.	7,474,011	Method for Improved Single Event Latch Up Resistance in an Integrated Circuit
Integrated Device Technology, Inc.	7,478,186	INTERRUPT COALESCER FOR DMA CHANNEL
Integrated Device Technology, Inc.	7,504,899	Inductor and Capacitor-Based Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,505,468	Method and Apparatus for Logical Identification Priority
Integrated Device Technology, Inc.	7,518,844	OVER-VOLTAGE TOLERANT ESD PROTECTION CIRCUIT
Integrated Device Technology, Inc.	7,523,232	Mark/Re-Read and Mark/Re-Write Operations in a Multi-Queue First-In First-out Memory System

Integrated Device Technology, Inc.	7,536,614	BUILT-IN-REDUNDANCY ANALYSIS USING RAM
Integrated Device Technology, Inc.	7,544,556	PROCESS FOR FORMING CMOS DEVICES USING REMOVABLE SPACERS
Integrated Device Technology, Inc.	7,548,125	Frequency Calibration for a Monolithic Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,548,132	Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,554,379	HIGH-SPEED, LOW-POWER LEVEL SHIFTER FOR MIXED SIGNAL-LEVEL ENVIRONMENTS
Integrated Device Technology, Inc.	7,555,668	DRAM Interface Circuits that Support Fast Deskew Calibration and Methods of Operating Same
Integrated Device Technology, Inc.	7,560,800	Die Seal with Reduced Noise Coupling
Integrated Device Technology, Inc.	7,560,936	METHOD AND APPARATUS FOR GROUND BOUNCE AND POWER SUPPLY BOUNCE DETECTION
Integrated Device Technology, Inc.	7,565,597	Fast parity scan of memory arrays
Integrated Device Technology, Inc.	7,570,115	Common Mode Voltage Extraction Circuit
Integrated Device Technology, Inc.	7,571,267	Core Clock Alignment Circuits that Utilize Clock Phase Learning Operations to Achieve Accurate Clocking of Data ...(FKA Method to optimize core clock alignment using frame clocks from all high speed lanes)
Integrated Device Technology, Inc.	7,571,300	MODULAR DISTRIBUTIVE ARITHMETIC LOGIC UNIT
Integrated Device Technology, Inc.	7,573,896	METHOD AND APPARATUS FOR GENERIC INTERFACE, PACKET CUT-THROUGH, OVERBOOKING, QUEUE CONCATENATION, AND LOGICAL IDENTIFICATION PRIORITY FOR A SYSTEM PACKET INTERFACE DEVICE
Integrated Device Technology, Inc.	7,580,355	METHOD OF PERFORMING WEIGHTED ROUND-ROBIN QUEUE SCHEDULING USING A DYNAMIC LINK LIST AND STRUCTURE FOR IMPLEMENTING SAME
Integrated Device Technology, Inc.	7,582,567	Method for Forming CMOS Device with Self-Aligned Contacts and Region Formed Using Salicide Process
Integrated Device Technology, Inc.	7,583,087	In-Situ Monitor of Process and Device Parameters in Integrated Circuits
Integrated Device Technology, Inc.	7,586,343	OUTPUT DRIVE CIRCUIT THAT ACCOMMODATES VARIABLE SUPPLY VOLTAGES
Integrated Device Technology, Inc.	7,594,149	IN-SITU MONITOR OF PROCESS AND DEVICE PARAMETERS IN INTEGRATED CIRCUITS
Integrated Device Technology, Inc.	7,596,142	Packet Processing in a Packet Switch With Improved Output Data Distribution
Integrated Device Technology, Inc.	7,602,318	METHOD AND APPARATUS FOR IMPROVED EFFICIENCY IN PROTOCOLS USING CHARACTER CODING

Integrated Device Technology, Inc.	7,617,346	RAPID INPUT/OUTPUT DOORBELL COALESCING TO MINIMIZE CPU UTILIZATION AND REDUCE SYSTEM INTERRUPT LATENCY
Integrated Device Technology, Inc.	7,634,774	SYSTEM AND METHOD OF SCHEDULING COMPUTING THREADS
Integrated Device Technology, Inc.	7,636,367	Method and Apparatus for Overbooking on FIFO memory
Integrated Device Technology, Inc.	7,642,867	SIMPLE TECHNIQUE FOR REDUCTION OF GAIN IN A VOLTAGE CONTROLLED OSCILLATOR
Integrated Device Technology, Inc.	7,642,892	NEGATIVE VOLTAGE COEFFICIENT RESISTOR AND METHOD OF MANUFACTURE
Integrated Device Technology, Inc.	7,644,311	Logic Soft Error Rate Prediction and Improvement (fka IC LOGIC with low soft error rate (SER))
Integrated Device Technology, Inc.	7,647,535	USING A DELAY CLOCK TO OPTIMIZE THE TIMING MARGIN OF SEQUENTIAL LOGIC
Integrated Device Technology, Inc.	7,656,007	Discrete Capacitors Embedded in Package Substrate
Integrated Device Technology, Inc.	7,656,243	Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,656,244	Discrete Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,656,245	Integrated Clock Generator & Timing/Frequency Reference
Integrated Device Technology, Inc.	7,671,602	METHOD AND APPARATUS FOR CROSS-POINT DETECTION
Integrated Device Technology, Inc.	7,675,790	OVER DRIVING PIN FUNCTION SELECTION METHOD AND CIRCUIT
Integrated Device Technology, Inc.	7,679,395	LOW-LOSS IMPEDANCE-MATCHED SOURCE-FOLLOWER FOR REPEATING OR SWITCHING SIGNALS ON A HIGH SPEED LINK
Integrated Device Technology, Inc.	7,679,463	Multi-terminal Harmonic Oscillator Integrated Circuit With Frequency Calibration and Frequency Configuration
Integrated Device Technology, Inc.	7,683,720	Novel Pre-Amplifier Uses High-Pass RC Filter in Receiver
Integrated Device Technology, Inc.	7,684,431	SYSTEM AND METHOD FOR ARBITRATION IN A PACKET SWITCH
Integrated Device Technology, Inc.	7,688,105	Impedance Matching Logic
Integrated Device Technology, Inc.	7,692,499	DIGITALLY COMPENSATED HIGHLY STABLE HOLDOVER CLOCK GENERATION TECHNIQUES USING ADAPTIVE FILTERING
Integrated Device Technology, Inc.	7,693,040	Processing Switch for Orthogonal Frequency Division Multiplexing
Integrated Device Technology, Inc.	7,701,957	METHOD AND APPARATUS FOR SWITCHING, MERGING, AND DEMERGING DATA BETWEEN DATA COMMUNICATION LOCATIONS
Integrated Device Technology, Inc.	7,705,619	Small Pitch Ball Grid Array of a Package Assembly for Use with Conventional Burn-in Sockets

Integrated Device Technology, Inc.	7,706,113	Electrical Overstress (EOS) and Electrostatic Discharge (ESD) Protection Circuit and Method of Use
Integrated Device Technology, Inc.	7,706,387	System and Method for Round Robin Arbitration
Integrated Device Technology, Inc.	7,714,620	Comparator With Amplitude and Time Hysteresis
Integrated Device Technology, Inc.	7,715,377	Apparatus and Method for Matrix Memory Switching Element
Integrated Device Technology, Inc.	7,719,371	Spread Spectrum Clock and Reference Signal Generator
Integrated Device Technology, Inc.	7,728,632	INTEGRATED CIRCUIT COMPARATORS HAVING IMPROVED INPUT RESOLUTION AND METHODS OF OPERATING SAME
Integrated Device Technology, Inc.	7,739,424	Packet Processing Switch and Methods of Operation Thereof
Integrated Device Technology, Inc.	7,739,460	Integrated Circuit Memory Systems Having Write-Back Buffers Therein that Support Read-Write-Modify (RWM) Operations Within High Capacity Memory Devices
Integrated Device Technology, Inc.	7,741,897	METHOD AND APPARATUS FOR SELF GATE PUMPED NMOS HIGH SPEED SWITCH
Integrated Device Technology, Inc.	7,747,828	SYSTEMS AND METHODS FOR MONITORING AND CONTROLLING BINARY STATE DEVICES USING A MEMORY DEVICE (FKA: Input Read Register / Output Drive Register (3.3V process for devices, uses 1.8V power supply))
Integrated Device Technology, Inc.	7,747,904	ERROR MANAGEMENT SYSTEM AND METHOD FOR A PACKET SWITCH
Integrated Device Technology, Inc.	7,750,618	System and Method for Testing a Clock Circuit
Integrated Device Technology, Inc.	7,750,666	Reduced Power Differential Type Termination Circuit
Integrated Device Technology, Inc.	7,770,078	Integrated Circuit Communication Self-Testing
Integrated Device Technology, Inc.	7,774,526	METHOD FOR DETERMINISTIC TIMED TRANSFER OF DATA WITH MEMORY USING A SERIAL INTERFACE
Integrated Device Technology, Inc.	7,782,780	SYSTEM AND METHOD FOR ARBITRATION OF MULTICAST DATA PACKETS USING HOLDS
Integrated Device Technology, Inc.	7,786,763	Clock Circuit With Harmonic Frequency Detector
Integrated Device Technology, Inc.	7,786,812	OSCILLATORS HAVING REACTANCE-ADJUSTABLE FREQUENCY CONTROL
Integrated Device Technology, Inc.	7,786,917	Digital-To-Analog Converter
Integrated Device Technology, Inc.	7,796,629	Packet Switch with Configurable Bandwidth Allocation Precision
Integrated Device Technology, Inc.	7,800,236	Semiconductor Die and Method for Forming a Semiconductor Die Having Power and Ground Strips that are Oriented Diagonally

Integrated Device Technology, Inc.	7,800,404	FIELD PROGRAMMABLE APPLICATION SPECIFIC INTEGRATED CIRCUIT WITH PROGRAMMABLE LOGIC ARRAY AND METHOD OF DESIGNING AND PROGRAMMING THE PROGRAMMABLE LOGIC ARRAY
Integrated Device Technology, Inc.	7,805,551	Multifunction queue to support data offload, protocol translation and pass-through FIFO
Integrated Device Technology, Inc.	7,805,552	PARTIAL PACKET WRITE AND WRITE DATA FILTERING IN A MULTI-QUEUE FIRST-IN FIRST-OUT MEMORY SYSTEM
Integrated Device Technology, Inc.	7,809,871	COMMON ACCESS RING SYSTEM
Integrated Device Technology, Inc.	7,814,248	Common Access Ring/Sub-ring System
Integrated Device Technology, Inc.	7,817,652	System and Method of Constructing Data Packets in a Packet Switch
Integrated Device Technology, Inc.	7,818,470	ADAPTIVE INTERRUPT ON SERIAL RAPID INPUT/OUTPUT (SRIO) ENDPOINT
Integrated Device Technology, Inc.	7,821,297	LOW POWER OUTPUT DRIVER
Integrated Device Technology, Inc.	7,825,777	Packet Processors Having Comparators Therein That Determine Non-Strict Inequalities Between Applied Operands
Integrated Device Technology, Inc.	7,827,324	METHOD OF HANDLING FLOW CONTROL IN DAISY-CHAIN PROTOCOLS
Integrated Device Technology, Inc.	7,827,555	SCHEDULER FOR A MULTIPROCESSING COMPUTING SYSTEM
Integrated Device Technology, Inc.	7,830,165	System and Method for Detecting Single Event Latchup in Integrated Circuits
Integrated Device Technology, Inc.	7,830,177	LOW POWER OUTPUT DRIVER
Integrated Device Technology, Inc.	7,834,524	Micro-electromechanical devices having variable capacitors therein that compensate for temperature-induced frequency drift in acoustic resonators
Integrated Device Technology, Inc.	7,834,708	Method and Apparatus for Analog Smooth Switch in VCO loading control
Integrated Device Technology, Inc.	7,842,613	METHODS OF FORMING MICROELECTRONIC PACKAGING SUBSTRATES HAVING THROUGH-SUBSTRATE VIAS THEREIN
Integrated Device Technology, Inc.	7,843,235	OUTPUT SLEW RATE CONTROL IN LOW VOLTAGE DIFFERENTIAL SIGNAL (LVDS) DRIVER
Integrated Device Technology, Inc.	7,847,404	Circuit Board Assembly and Packaged Integrated Circuit Device with Power and Ground Channels
Integrated Device Technology, Inc.	7,864,092	THERMO-DECODER CIRCUIT
Integrated Device Technology, Inc.	7,870,310	MULTIPLE COUNTERS TO RELIEVE FLAG RESTRICTION IN A MULTI-QUEUE FIRST-IN FIRST-OUT MEMORY SYSTEM
Integrated Device Technology, Inc.	7,870,313	Method and Structure to Support System RESOURCE Access of a Serial Device Implementing a Lite-Weight Protocol

Integrated Device Technology, Inc.	7,871,857	METHODS OF FORMING MULTI-CHIP SEMICONDUCTOR SUBSTRATES
Integrated Device Technology, Inc.	7,872,541	INDUCTOR AND CAPACITOR-BASED CLOCK GENERATOR AND TIMING/FREQUENCY REFERENCE
Integrated Device Technology, Inc.	7,877,657	LOOK-AHEAD BUILT-IN SELF TESTS
Integrated Device Technology, Inc.	7,882,280	Packet Processing Switch and Methods of Operation Thereof
Integrated Device Technology, Inc.	7,898,288	INPUT TERMINATION FOR DELAY LOCKED LOOP FEEDBACK WITH IMPEDANCE MATCHING
Integrated Device Technology, Inc.	7,903,380	ESD Protection Circuit For Inside A Power Pad or Input/Output Pad
Integrated Device Technology, Inc.	7,907,625	Power Reduction Technique for Buffered Crossbar Switch
Integrated Device Technology, Inc.	7,916,779	Adaptive Decision Feedback Equalizer for High Data Rate Serial Link Receiver
Integrated Device Technology, Inc.	7,918,977	Solid State Electrochemical Gas Sensor and Method for Fabricating Same
Integrated Device Technology, Inc.	7,921,400	Method for Forming Integrated Circuit Device Using Cell Library with Soft Error Resistant Logic Cells
Integrated Device Technology, Inc.	7,924,110	Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	7,939,990	Thin-Film Bulk Acoustic Resonators Having Perforated Bodies that Provide Reduced Susceptibility to Process-Induced Lateral Dimension Variations
Integrated Device Technology, Inc.	7,940,662	Method and Apparatus for Forwarding Bursty Data
Integrated Device Technology, Inc.	7,940,762	Content Driven Packet Switch
Integrated Device Technology, Inc.	7,941,723	Clock Generator and Method for Providing Reliable Clock Signal Using Array of MEMs Resonators
Integrated Device Technology, Inc.	7,944,876	Time Slot Interchange Switch with Bit Error Rate Testing
Integrated Device Technology, Inc.	7,945,716	Serial Buffer Supporting Virtual Queue to Physical Memory Mapping
Integrated Device Technology, Inc.	7,955,885	METHODS OF FORMING PACKAGED MICRO-ELECTROMECHANICAL DEVICES
Integrated Device Technology, Inc.	7,968,989	Multi-Package Slot Array
Integrated Device Technology, Inc.	7,969,247	Charge Pump Linearization Technique for Delta-Sigma Fractional-N Synthesizers
Integrated Device Technology, Inc.	7,974,278	Method to Enable Adoption of sRIO Virtual Channels
Integrated Device Technology, Inc.	7,978,017	CONTROL VOLTAGE GENERATOR FOR A CLOCK, FREQUENCY REFERENCE, AND OTHER REFERENCE SIGNAL GENERATOR

Integrated Device Technology, Inc.	7,983,374	METHODS AND SYSTEMS FOR PROVIDING VARIABLE CLOCK RATES AND DATA RATES FOR A SERDES
Integrated Device Technology, Inc.	7,990,226	Non-Linear Crystal Oscillator Capacitive Load Circuits
Integrated Device Technology, Inc.	7,994,760	NOVEL REGULATION SCHEME FOR A CHARGE PUMP
Integrated Device Technology, Inc.	7,995,698	Method for Binary Clock and Data Recovery for Fast Acquisition and Small Tracking Error
Integrated Device Technology, Inc.	7,996,701	AUTOMATED CLOCK RELATIONSHIP DETECTION
Integrated Device Technology, Inc.	8,004,339	Apparatuses and Methods for a Level Shifter With Reduced Shoot-Through Current
Integrated Device Technology, Inc.	8,005,152	Compression of Baseband Signals in Base Transceiver Systems
Integrated Device Technology, Inc.	8,008,927	Method and Apparatus for Ground Bounce and Power Supply Bounce Detection
Integrated Device Technology, Inc.	8,008,951	High Voltage Switch Utilizing Low Voltage MOS Transistors with High Voltage Breakdown Isolation Junctions
Integrated Device Technology, Inc.	8,014,288	Packet latency based arbitration technique for a packet switch
Integrated Device Technology, Inc.	8,023,594	ASYNCHRONOUS BIPHASE MARK SIGNAL RECEIVERS AND METHODS OF OPERATING SAME
Integrated Device Technology, Inc.	8,028,211	LOOK-AHEAD BUILT-IN SELF TESTS WITH TEMPERATURE ELEVATION OF FUNCTIONAL ELEMENTS
Integrated Device Technology, Inc.	8,031,099	Analog/Digital or Digital/Analog Conversion System Having Improved LInearity
Integrated Device Technology, Inc.	8,036,216	Method and Apparatus for Packet Cut-Through
Integrated Device Technology, Inc.	8,040,888	Packet switch with port route tables
Integrated Device Technology, Inc.	8,041,552	BEHAVIORAL MODELING OF HIGH SPEED DIFFERENTIAL SIGNALS BASED ON PHYSICAL CHARACTERISTICS
Integrated Device Technology, Inc.	8,063,683	LOW POWER CLOCK AND DATA RECOVERY PHASE INTERPOLATOR
Integrated Device Technology, Inc.	8,064,472	Method and Apparatus for Queue Concatenation
Integrated Device Technology, Inc.	8,069,392	ERROR CORRECTION CODE SYSTEM AND METHOD
Integrated Device Technology, Inc.	8,072,259	VOLTAGE REFERENCE AND SUPPLY VOLTAGE LEVEL DETECTOR CIRCUITS USING PROPORTIONAL TO ABSOLUTE TEMPERATURE CELLS
Integrated Device Technology, Inc.	8,073,043	Method for Reliable Injection of Deterministic Jitter for High Speed Transceiver Simulation
Integrated Device Technology, Inc.	8,081,031	Equalization System with Stabilized Peaking Gain for a Communication System

Integrated Device Technology, Inc.	8,081,646	OLD VIRTUAL QUEUES TECHNIQUE FOR ROUTING DATA PACKETS IN A PACKET SWITCH
Integrated Device Technology, Inc.	8,085,180	Apparatuses and Methods for Multiple-Output Comparators and Analog-To-Digital Converters
Integrated Device Technology, Inc.	8,085,603	Method and Apparatus for Compression of Configuration Bitstream of Field Programmable Logic
Integrated Device Technology, Inc.	8,093,957	Method and Apparatus for Frequency Compensation for Multi-Band VCO
Integrated Device Technology, Inc.	8,093,958	CLOCK, FREQUENCY REFERENCE, AND OTHER REFERENCE SIGNAL GENERATOR WITH A CONTROLLED QUALITY FACTOR
Integrated Device Technology, Inc.	8,094,504	Buffered DRAM
Integrated Device Technology, Inc.	8,094,677	MULTI-BUS STRUCTURE FOR OPTIMIZING SYSTEM PERFORMANCE OF A SERIAL BUFFER
Integrated Device Technology, Inc.	8,095,707	METHOD FOR SYNCHRONIZATION OF PERIPHERALS WITH A CENTRAL PROCESSING UNIT IN AN EMBEDDED SYSTEM
Integrated Device Technology, Inc.	8,095,813	INTEGRATED CIRCUIT SYSTEMS HAVING PROCESSOR-CONTROLLED CLOCK SIGNAL GENERATORS THEREIN THAT SUPPORT EFFICIENT POWER MANAGEMENT
Integrated Device Technology, Inc.	8,106,724	THIN-FILM BULK ACOUSTIC RESONATORS HAVING PERFORATED RESONATOR BODY SUPPORTS THAT ENHANCE QUALITY FACTOR
Integrated Device Technology, Inc.	8,115,532	LINEAR MONOTONIC DELAY CHAIN CIRCUIT
Integrated Device Technology, Inc.	8,120,162	Package with improved connection of a decoupling capacitor
Integrated Device Technology, Inc.	8,120,405	Method and Apparatus for an Output Buffer with Dynamic Impedance Control
Integrated Device Technology, Inc.	8,127,187	Method and Apparatus of ATE IC scan test using FPGA-based system
Integrated Device Technology, Inc.	8,129,964	Interleaved/All-Phase Mode Switched PWM System
Integrated Device Technology, Inc.	8,134,414	Clock, Frequency Reference, and Other Reference Signal Generator with Frequency Stability Over Temperature Variation
Integrated Device Technology, Inc.	8,138,845	Method and Apparatus for Auto-Frequency Calibration for Multi-Band VCO
Integrated Device Technology, Inc.	8,149,224	COMPUTING SYSTEM WITH DETACHABLE TOUCH SCREEN DEVICE
Integrated Device Technology, Inc.	8,149,553	ELECTROSTATIC DISCHARGE EVENT PROTECTION FOR AN INTEGRATED CIRCUIT
Integrated Device Technology, Inc.	8,151,132	Memory Register Having an Integrated Delay-Locked Loop
Integrated Device Technology, Inc.	8,154,880	Method and Apparatus for Active Line Interface Isolation

Integrated Device Technology, Inc.	8,164,159	Semiconductor Resonators With Electromagnetic and Environmental Shielding and Methods of Forming Same
Integrated Device Technology, Inc.	8,174,428	COMPRESSION OF SIGNALS IN BASE TRANSCEIVER SYSTEMS
Integrated Device Technology, Inc.	8,179,372	ELECTRONIC DISPLAY WITH ARRAY CONTEXT-SENSITIVE SEARCH (ACS) TECHNOLOGY
Integrated Device Technology, Inc.	8,179,952	PROGRAMMABLE DUTY CYCLE DISTORTION GENERATION CIRCUIT
Integrated Device Technology, Inc.	8,194,721	Signal Amplitude Distortion Within an Integrated Circuit
Integrated Device Technology, Inc.	8,199,042	Apparatuses and Methods for Physical Layouts of Analog-to-Digital Converters
Integrated Device Technology, Inc.	8,210,360	Composite Membranes and Methods for Making Same
Integrated Device Technology, Inc.	8,212,594	METHODS AND APPARATUSES FOR CLOCK DOMAIN CROSSING
Integrated Device Technology, Inc.	8,213,448	METHOD TO SUPPORT LOSSLESS REAL TIME DATA SAMPLING AND PROCESSING ON RAPID I/O END-POINT
Integrated Device Technology, Inc.	8,230,174	MULTI-QUEUE ADDRESS GENERATOR FOR START AND END ADDRESSES IN A MULTI-QUEUE FIRST-IN FIRST-OUT MEMORY SYSTEM
Integrated Device Technology, Inc.	8,238,339	Method and Apparatus for Selective Packet Discard
Integrated Device Technology, Inc.	8,248,135	Circuit Including Current-Mode Logic Driver with Multi-Rate Programmable Pre-Emphasis Delay Element
Integrated Device Technology, Inc.	8,248,383	MULTI-TOUCH TOUCH SCREEN WITH SINGLE-LAYER ITO BARS ARRANGED IN PARALLEL
Integrated Device Technology, Inc.	8,248,741	APPARATUSES AND METHODS FOR A SCR-BASED CLAMPED ELECTROSTATIC DISCHARGE PROTECTION DEVICE
Integrated Device Technology, Inc.	8,254,399	METHOD AND APPARATUS FOR ADAPTIVE BUFFER MANAGEMENT FOR TRAFFIC OPTIMIZATION ON SWITCHES
Integrated Device Technology, Inc.	8,254,515	Method For Measuring Phase Locked Loop Bandwidth Parameters for High-Speed Serial Links
Integrated Device Technology, Inc.	8,265,219	METHOD AND APPARATUS FOR FAST PLL INITIALIZATION
Integrated Device Technology, Inc.	8,283,256	Methods of Forming Microdevice Substrates Using Double-Sided Alignment Techniques
Integrated Device Technology, Inc.	8,284,091	Flash ADC Based on Random Offset Spread
Integrated Device Technology, Inc.	8,289,061	Technique to Reduce Clock Recovery Amplitude Modulation in High-speed Serial Transceiver
Integrated Device Technology, Inc.	8,289,989	System and Method for Arbitration Using Availability Signals
Integrated Device Technology, Inc.	8,294,249	Lead Frame Package
Integrated Device Technology, Inc.	8,312,190	Protocol Translation in A Serial Buffer

Integrated Device Technology, Inc.	8,316,245	METHOD AND APPARATUS FOR FAIL-SAFE START-UP CIRCUIT FOR SUBTHRESHOLD CURRENT SOURCES
Integrated Device Technology, Inc.	8,319,540	Apparatuses and Methods for a Voltage Level Shifting
Integrated Device Technology, Inc.	8,320,392	METHOD AND APPARATUS FOR PROGRAMMABLE BUFFER WITH DYNAMIC ALLOCATION TO OPTIMIZE SYSTEM THROUGHPUT WITH DEADLOCK AVOIDANCE ON SWITCHES
Integrated Device Technology, Inc.	8,320,433	COMPRESSION OF BASEBAND SIGNALS IN BASE TRANSCEIVER SYSTEM INTERFACES
Integrated Device Technology, Inc.	8,324,948	Method and Apparatus for Duty-Cycle Correction with Reduced Current Consumption
Integrated Device Technology, Inc.	8,325,723	Method and Apparatus for Dynamic Traffic Management with Packet Classification
Integrated Device Technology, Inc.	8,331,461	Compression of Baseband Signals in Base Transceiver System Radio Units
Integrated Device Technology, Inc.	8,350,743	AD CONVERTER
Integrated Device Technology, Inc.	8,362,939	Switched-Capacitor Pipeline ADC Stage
Integrated Device Technology, Inc.	8,368,487	Thin-Film Bulk Acoustic Resonators Having Multi-Axis Acoustic Wave Propagation Therein
Integrated Device Technology, Inc.	8,378,714	5V Tolerant Circuit for CML Transceiver in AC-Couple
Integrated Device Technology, Inc.	8,378,746	Voltage-Mode Line Driving Circuit Having Adaptive Impedance Matching
Integrated Device Technology, Inc.	8,378,864	Apparatuses and Methods for Reducing Errors in Analog to Digital Converters
Integrated Device Technology, Inc.	8,379,771	System and Method for Formatting Symbols in a Data Stream
Integrated Device Technology, Inc.	8,384,431	Voltage Level Shsifting Apparatuses and Methods
Integrated Device Technology, Inc.	8,390,591	PROPORTIONAL AREA WEIGHTED SENSOR FOR TWO-DIMENSIONAL LOCATIONS ON A TOUCH SCREEN
Integrated Device Technology, Inc.	8,392,001	Method and Appartus for Externally Aided Self Adjusting Real Time Clock
Integrated Device Technology, Inc.	8,395,247	Method and Apparatus for Placing Quartz Saw Devices Together with Clock/Oscillator
Integrated Device Technology, Inc.	8,400,431	Method to Improve Performance of a Proportional Area Weighted Sensor for Two-Dimensional Locations on a Touch Screen
Integrated Device Technology, Inc.	8,410,813	Method and Apparatus for a Low Power AC On-Die-Termination (ODT) Circuit
Integrated Device Technology, Inc.	8,422,518	Managing Transmit Jitter for Multi-Format Digital Audio Transmission

Integrated Device Technology, Inc.	8,427,130	Methods and Apparatuses for Combined Frequency Compensation and Soft Start Processes
Integrated Device Technology, Inc.	8,451,154	Pipelined ADC Calibration
Integrated Device Technology, Inc.	8,451,161	SWITCHED-CAPACITOR PIPELINE STAGE
Integrated Device Technology, Inc.	8,456,237	Low Noise Variable Gain Amplifier Utilizing Variable Feedback Techniques with Constant Input/Output Impedance
Integrated Device Technology, Inc.	8,487,792	METHOD OF GAIN CALIBRATION OF AN ADC STAGE AND AN ADC STAGE
Integrated Device Technology, Inc.	8,493,357	Mechanical Means for Providing HAPTIC Feedback in Connection with Capacitive Sensing Mechanisms
Integrated Device Technology, Inc.	8,493,833	Delay-Variation Based Driver Equalization for Far-End Crosstalk Cancellation
Integrated Device Technology, Inc.	8,501,515	Methods of Forming Micro-Electromechanical Resonators Using Passive Compensation Techniques
Integrated Device Technology, Inc.	8,503,514	HIGH SPEED SWITCH WITH DATA CONVERTER PHYSICAL PORTS
Integrated Device Technology, Inc.	8,503,515	High Speed Switch with Data Converter Physical Ports and Processing Unit
Integrated Device Technology, Inc.	8,510,492	SYSTEM AND METHOD FOR COMMUNICATION HANDSHAKING BETWEEN A MASTER PROCESSORS AND A SLAVE PROCESSOR
Integrated Device Technology, Inc.	8,513,935	Combinations of Current Feedback for Frequency Compensation, Overload Detection, and Super Overload Detection in Switching Power Converters
Integrated Device Technology, Inc.	8,513,992	METHOD AND APPARATUS FOR IMPLEMENTATION OF PLL MINIMUM FREQUENCY VIA VOLTAGE COMPARISON
Integrated Device Technology, Inc.	8,519,800	Third Overtone Crystal-Based Oscillators Having Tunable All-Pass RC Filters Therein
Integrated Device Technology, Inc.	8,521,119	Method and Apparatus for an Equalized On-Die Termination (ODT) Circuit
Integrated Device Technology, Inc.	8,531,418	Touch Sensor Having Improved Edge Response
Integrated Device Technology, Inc.	8,542,040	Reconfigurable Divider Circuits with Hybrid Structure
Integrated Device Technology, Inc.	8,548,773	High Speed Chip Screening Method Using Delay Locked Loop
Integrated Device Technology, Inc.	8,552,804	Frequency Synthesizers with Dynamic Calibration Intervals
Integrated Device Technology, Inc.	8,559,587	FRACTIONAL-N DIVIDERS HAVING DIVIDER MODULATION CIRCUITS THEREIN WITH SEGMENTED ACCUMULATORS
Integrated Device Technology, Inc.	8,571,050	Method and Apparatus to Optimize Class of Service Under Multiple VCS with Mixed Reliable Transfer and Continuous Transfer Modes
Integrated Device	8,572,151	Methods and Apparatuses for Cordic Processing

Technology, Inc.		
Integrated Device Technology, Inc.	8,575,819	Microelectromechanical Resonators With Passive Frequency Tuning Using Built-In Piezoelectric-Based Varactors
Integrated Device Technology, Inc.	8,575,981	MEMS-BASED FREQUENCY SYNTHESIZERS WITH ENHANCED TEMPERATURE COMPENSATION
Integrated Device Technology, Inc.	8,604,848	MEMS-Based Frequency Synthesizers Having Variable Bandwidth Temperature Compensation
Integrated Device Technology, Inc.	8,604,879	Matched Feedback Amplifier With Improved Linearity
Integrated Device Technology, Inc.	8,610,336	Microelectromechanical Resonators Having Resistive Heating Elements Therein Configured to Provide Frequency Tuning Through Convective Heating of Resonator Bodies
Integrated Device Technology, Inc.	8,612,503	Methods and Apparatuses for Flexible and High Performance Digital Signal Processing
Integrated Device Technology, Inc.	8,624,105	Energy Conversion Device with Support Member Having Pore Channels
Integrated Device Technology, Inc.	8,624,818	Apparatuses and Methods for Reducing Power in Driving Display Panels
Integrated Device Technology, Inc.	8,625,621	Method to Support Flexible Data Transport on Serial Protocols
Integrated Device Technology, Inc.	8,633,635	Microelectromechanical Resonators with Thermally-Actuated Frequency Tuning Beams
Integrated Device Technology, Inc.	8,638,010	Systems, Devices, and Methods for Providing Backup Power to a Load
Integrated Device Technology, Inc.	8,638,174	Digital Signal Generator
Integrated Device Technology, Inc.	8,638,178	METHODS OF TESTING PACKAGED THIN-FILM PIEZOELECTRIC-ON-SEMICONDUCTOR MICROELECTROMECHANICAL RESONATORS HAVING HERMETIC SEALS
Integrated Device Technology, Inc.	8,649,388	Transmission of Multiprotocol Data In A Distributed Antenna System
Integrated Device Technology, Inc.	8,680,931	PERIODIC SIGNAL GENERATORS HAVING MICROELECTROMECHANICAL RESONATORS THEREIN THAT SUPPORT SURFACE WAVE AND BULK ACOUSTIC WAVE MODES OF OPERATION WITH DIFFERENT TEMPERATURE COEFFICIENTS OF FREQUENCY
Integrated Device Technology, Inc.	8,681,514	Controller for Secondary Side Control of a Switch, Power Converter, and Related Synchronous Rectification Control Method
Integrated Device Technology, Inc.	8,693,557	AC Coupled Clock Receiver with Common-Mode Noise Rejection
Integrated Device Technology, Inc.	8,704,684	HYBRID-CODING FOR IMPROVING TRANSIENT SWITCH RESPONSE IN A MULTI-CELL PROGRAMMABLE APPARATUS

Integrated Device Technology, Inc.	8,705,634	COMPRESSION OF BASEBAND SIGNALS IN BASE TRANSCEIVER SYSTEMS
Integrated Device Technology, Inc.	8,723,492	Autonomous Controlled Headroom Low Dropout Regulator For Single Inductor Multiple Output Power Supply
Integrated Device Technology, Inc.	8,723,573	METHOD AND APPARATUS FOR REDUCING CROWBAR CURRENT AND CONTROLLING SLEW RATE
Integrated Device Technology, Inc.	8,723,807	COMBINED TOUCH SENSOR AND LED DRIVER WITH N-TYPE MOSFET PROTECTING TOUCH SENSOR
Integrated Device Technology, Inc.	8,730,983	Method and Apparatus for a Configurable Packet Routing, Buffering and Scheduling Scheme to Optimize Throughput with Deadlock pPevention in SRIO-to-PCIe Bridges
Integrated Device Technology, Inc.	8,732,224	Method and Apparatus for Implementation of High Order MASH by Reuse of Single Stage MASH
Integrated Device Technology, Inc.	8,732,495	Systems, Apparatuses and Methods for Dynamic Voltage and Frequency Control of Components Used in a Computer System
Integrated Device Technology, Inc.	8,742,854	Periodic Signal Generators Having Microelectromechanical Resonators Therein That Support Generation of High Frequency Low-TCF Difference Signals
Integrated Device Technology, Inc.	8,754,480	Low On-Resistance Power Transistor Having Transistor Stripes
Integrated Device Technology, Inc.	8,754,673	Adaptive Reference Voltage Generators That Support High Speed Signal Detection
Integrated Device Technology, Inc.	8,754,719	Integrated Circuit Devices Using Analog Dividers
Integrated Device Technology, Inc.	8,769,329	Power Management System and Method for Perpheral Devices
Integrated Device Technology, Inc.	8,773,096	APPARATUSES AND METHODS RESPONSIVE TO OUTPUT VARIATIONS IN VOLTAGE REGULATORS
Integrated Device Technology, Inc.	8,779,851	LINEARIZING A POWER AMPLIFIER
Integrated Device Technology, Inc.	8,785,229	Methods of Forming Micro-Electromechanic Resonators Having Passive Temperature Compensation Regions Therein
Integrated Device Technology, Inc.	8,803,622	MICROELECTROMECHANICAL-BASED OSCILLATORS HAVING ADJUSTABLE GAIN AMPLIFIERS THEREIN THAT SUPPORT Q-FACTOR CONTROL
Integrated Device Technology, Inc.	8,823,407	TEST ASSEMBLY FOR VERIFYING HEAT SPREADER GROUNDING IN A PRODUCTION TEST
Integrated Device Technology, Inc.	8,826,057	Method for synchronizing a signal from one clock domain to another
Integrated Device Technology, Inc.	8,842,721	Method and Apparatus for Channel Equalization in High Speed S-RIO Based Communication Systems
Integrated Device Technology, Inc.	8,847,628	Current Mode Logoic Circuits with Automatic Sink Current Adjustment

Integrated Device Technology, Inc.	8,850,089	METHOD AND APPARATUS FOR UNIFIED FINAL BUFFER WITH POINTER-BASED AND PAGE-BASED SCHEME FOR TRAFFIC OPTIMIZATION
Integrated Device Technology, Inc.	8,854,086	Clock Distribution Systems for Low Power Applications
Integrated Device Technology, Inc.	8,892,930	Systems and Methods for Power Management in Electronic Devices
Integrated Device Technology, Inc.	8,902,765	Method and Apparatus for Congestion and Fault Management with Time-To-Live
Integrated Device Technology, Inc.	8,915,646	High Accuracy Temperature Sensor
Integrated Device Technology, Inc.	8,939,293	Composite Membrane with Integral Rim
Integrated Device Technology, Inc.	8,941,595	ALTERNATING, COMPLEMENTARY CONDUCTIVE ELEMENT PATTERN FOR MULTI-TOUCH SENSOR
Integrated Device Technology, Inc.	8,942,624	Apparatus System and Method for Back-Channel Communication in an Inductive Wireless Power Transfer System
Integrated Device Technology, Inc.	8,943,242	Timing Controllers Having Partitioned Pipelined delay Chains Therein
Integrated Device Technology, Inc.	8,949,448	System and Method for Improving the Timestamp Precision in a Precision Time Protocol (PTP) Device
Integrated Device Technology, Inc.	8,949,501	Method and Apparatus for a Configurable Packet Routing, Buffering and Scheduling Scheme to Optimize Throughput with Deadlock Prevention in SRIO-to-PCIe Bridges
Integrated Device Technology, Inc.	8,963,527	EMI Mitigation of Power Converters by Modulation of Switch Control Signals
Integrated Device Technology, Inc.	8,981,856	High Frequency Precision Oscillators Having Stable Temperature Characteristics
Integrated Device Technology, Inc.	8,989,088	OFDM Signal Procesing in a Base Transceiver System
Integrated Device Technology, Inc.	8,989,257	Method and Apparatus for Providing Near-Zero Jitter Real-Time Compression in a Communication System
Integrated Device Technology, Inc.	9,000,853	Packaged MEMS-Based Oscillator Circuits That Support Frequency Margining and Methods of Operating Same
Integrated Device Technology, Inc.	9,055,472	Transmission of Multiprotocol Data In A Distributed Antenna System
Integrated Device Technology, Inc.	9,059,778	Frequency Domain Compression in a Base Tranceiver System
Integrated Device Technology, Inc.	9,065,459	Clock Generation Circuits Using Jitter Attenuation Control Circuits With Dynamic Range Shifting
Integrated Device Technology, Inc.	9,090,451	MICROELECTROMECHANICAL RESONATORS HAVING OFFSET [100] AND [110] CRYSTAL ORIENTATIONS

Integrated Device Technology, Inc.	9,092,217	Power Management Integrated Circuit Using a Flexible Script-Based Configurator and Method for Power Management
Integrated Device Technology, Inc.	9,112,410	Apparatuses and System Having Separate Power Cotrol and Timing Control of a Power Control System and Related Method
Integrated Device Technology, Inc.	9,118,295	SYSTEMS AND METHODS FOR ADAPTIVE EQUALIZATION CONTROL FOR HIGH-SPEED WIRELINE COMMUNICATIONS
Integrated Device Technology, Inc.	9,118,333	SELF-ADAPTIVE MULTI-MODULUS DIVIDERS CONTAINING DIV2/3 CELLS THEREIN
Integrated Device Technology, Inc.	9,124,286	Protection for Analog to Digital Converters
Integrated Device Technology, Inc.	9,136,764	Apparatus and System and Method for Auto-Configuration of a Power Control Systems
Integrated Device Technology, Inc.	9,143,346	Method and Apparatus for a Configurable Packet Routing, Buffering and Scheduling Scheme to Optimize Throughput with Deadlock Prevention in SRIO-to-PCIe Bridges
Integrated Device Technology, Inc.	9,148,024	Apparatuses, Systems and Methods for a Monotonic Transfer Function in Wireless Power Transfer Systems
Integrated Device Technology, Inc.	9,154,147	Systems and Methods for Estimation of Offset and Gain Errors in a Time-Interleaved Analog-to-Digital Converter
Integrated Device Technology, Inc.	9,166,835	SYSTEMS AND METHODS FOR PEAK DETECTION IN AUTOMATIC GAIN CONTROL CIRCUITS IN HIGH-SPEED WIRELINE COMMUNICATIONS
Integrated Device Technology, Inc.	9,184,659	Self-Adaptive Current-Mode-Control Circuit for a Switching Regulator
Integrated Device Technology, Inc.	9,203,769	Method and Apparatus for Congestion and Fault Management with Time-to-Live
Integrated Device Technology, Inc.	9,203,933	METHOD AND APPARATUS FOR EFFICIENT DATA COMPRESSION IN A COMMUNICATION SYSTEM
Integrated Device Technology, Inc.	9,215,296	Method and Apparatus for Efficient Radio Unit Processing in a Communication System
Integrated Device Technology, Inc.	9,236,871	Digital Filter for Phase-Locked Loop Integrated Circuits
Integrated Device Technology, Inc.	9,236,873	Fractional Divider Based Phase Locked Loops With Digital Noise Cancellation
Integrated Device Technology, Inc.	9,236,967	Methods of Packet-Based Synchronization in Non-Stationary Network Environments
Integrated Device Technology, Inc.	9,240,792	Monolithic Clock Generator and Timing/Frequency Reference
Integrated Device Technology, Inc.	9,240,803	Compression of Baseband Signals in Base Transceiver System Processors
Integrated Device Technology, Inc.	9,240,892	Method and Apparatus for Reduction of Communications Media Energy Consumption

Integrated Device Technology, Inc.	9,264,027	Process Compensated Delay
Integrated Device Technology, Inc.	9,270,280	Half Integer Frequency Fractional Divider That Support 50% Duty Cycle Signal Generation
Integrated Device Technology, Inc.	9,281,831	Digital Extraction and Correction of the Linearity of a Residue Amplifier in a Pipeline ADC
Integrated Device Technology, Inc.	9,306,537	INTEGRATED CIRCUIT DEVICE SUBSTRATES HAVING PACKAGED CRYSTAL RESONATORS THEREON
Integrated Device Technology, Inc.	9,313,300	Methods and Apparatus for a Unified Compression Framework of Baseband Signals
Integrated Device Technology, Inc.	9,332,629	Flip Chip Bump Array with Superior Signal Performance
Integrated Device Technology, Inc.	9,336,896	SYSTEM AND METHOD FOR VOLTAGE REGULATION OF ONE-TIME-PROGRAMMABLE (OTP) MEMORY PROGRAMMING VOLTAGE
Integrated Device Technology, Inc.	9,362,924	Method and Apparatus for Fast Frequency Acquisition in PLL System
Integrated Device Technology, Inc.	9,362,928	LOW-SPURIOUS FRACTIONAL N-FREQUENCY DIVIDER AND METHOD OF USE
Integrated Device Technology, Inc.	9,369,139	Fractional Reference-Injection PLL
Integrated Device Technology, Inc.	9,369,149	Method and Apparatus for Efficient Baseband Unit Processing in a Communication System
Integrated Device Technology, Inc.	9,369,270	DUAL-COUPLED PHASE-LOCKED LOOPS FOR CLOCK AND PACKET-BASED SYNCHRONIZATION
Integrated Device Technology, Inc.	9,374,078	Multi-Bit Cell Attenuator
Integrated Device Technology, Inc.	9,397,151	Packaged Integrated Circuits Having High-Q Inductors Therein and Methods of Forming Same
Integrated Device Technology, Inc.	9,398,489	Methods and Apparatus for Context Based Data Compression in a Communication System
Integrated Device Technology, Inc.	9,407,268	LOW VOLTAGE DIFFERENTIAL SIGNALING (LVDS) DRIVER WITH DIFFERENTIAL OUTPUT SIGNAL AMPLITUDE REGULATION
Integrated Device Technology, Inc.	9,419,588	Output Driver Having Output Impedance Adaptable to Supply Voltage and Method of Use
Integrated Device Technology, Inc.	9,431,838	Apparatuses and Related Methods for Charging Control of a Switching Voltage Regulator
Integrated Device Technology, Inc.	9,431,955	Monolithic Composite Resonator Devices with Reduced Sensitivity to Acceleration and Vibration
Integrated Device Technology, Inc.	9,431,968	METHODS AND APPARATUSES FOR SLEW RATE ENHANCEMENT OF AMPLIFIERS
Integrated Device Technology, Inc.	9,438,252	INTEGRATED CIRCUITS HAVING LOW POWER, LOW INTERFERENCE AND PROGRAMMABLE DELAY GENERATORS THEREIN AND METHODS OF OPERATING SAME

Integrated Device Technology, Inc.	9,442,167	METHOD AND APPARATUS FOR USING TESTER CHANNEL AS DEVICE POWER SUPPLY
Integrated Device Technology, Inc.	9,444,566	METHODS OF PERFORMING TIME-OF-DAY SYNCHRONIZATION IN PACKET PROCESSING NETWORKS
Integrated Device Technology, Inc.	9,445,536	Crystal Oscillator Fabrication Methods Using Dual-Deposition of Mounting Cement and Dual-Curing Techniques
Integrated Device Technology, Inc.	9,450,648	Apparatus, System, and Method for Detecting a Foreign Object in an Inductive Wireless Power Transfer System
Integrated Device Technology, Inc.	9,455,045	Controlling Operation of a Timing Device Using an OTP NVM to Store Timing Device Configurations in a RAM
Integrated Device Technology, Inc.	9,478,599	Integrated Circuit Device Substrates Having Packaged Inductors Thereon
Integrated Device Technology, Inc.	9,479,177	Self-Calibrating Fractional Divider Circuits
Integrated Device Technology, Inc.	9,479,182	Methods and Apparatus for Synchronizing Operations Using Separate Asynchronous Signals
Integrated Device Technology, Inc.	9,485,053	Long-Distance RapidIO Packet Delivery
Integrated Device Technology, Inc.	9,485,688	METHOD AND APPARATUS FOR CONTROLLING ERROR AND IDENTIFYING BURSTS IN A DATA COMPRESSION SYSTEM
Integrated Device Technology, Inc.	9,490,805	Low Power Driver With Programmable Output Impedance
Integrated Device Technology, Inc.	9,490,828	Integrated Circuits Having Multiple Digitally-Controlled Oscillators (DCOs) Therein that are Slaved to the Same Loop Filter
Integrated Device Technology, Inc.	9,495,285	INITIATING OPERATION OF A TIMING DEVICE USING A READ ONLY MEMORY (ROM) OR A ONE TIME PROGRAMMABLE NON VOLATILE MEMORY (OTP NVM)
Integrated Device Technology, Inc.	9,500,707	Method and Apparatus for Using Tester Channel as Device Power Supply
Integrated Device Technology, Inc.	9,503,059	Integrated Circuit Devices Having Oscillator Circuits Therein that Support Fixed Frequency Generation Over Process-Voltage-Termperature (PVT) Variations
Integrated Device Technology, Inc.	9,506,817	TEMPERATURE DETECTION METHOD AND DEVICE WITH IMPROVED ACCURACY AND CONVERSION TIME
Integrated Device Technology, Inc.	9,515,510	Apparatuses and Methods for Over-Temperature Protection of Energy Storage Devices
Integrated Device Technology, Inc.	9,525,408	High Speed, Low Power, Isolated Multiplexer
Integrated Device Technology, Inc.	9,542,991	Single-Ended Signal Equalization with a Programmable 1-Tap Decision Feedback Equalizer
Integrated Device Technology, Inc.	9,543,960	Multi-Stage Frequency Dividers Having Duty Cycle Correction Circuits Therein

Integrated Device Technology, Inc.	9,551,805	APPARATUS, SYSTEM, AND METHOD FOR DETECTING A FOREIGN OBJECT IN AN INDUCTIVE WIRELESS POWER TRANSFER SYSTEM VIA COUPLING COEFFICIENT MEASUREMENT
Integrated Device Technology, Inc.	9,552,870	MEMORY INCLUDES TRANSMITTER FOR DATA SYNCHRONIZATION TRANSMISSION AFTER A MODE SWITCH AND METHOD THEREOF
Integrated Device Technology, Inc.	9,553,485	APPARATUS, SYSTEM, AND METHOD FOR DETECTING A FOREIGN OBJECT IN AN INDUCTIVE WIRELESS POWER TRANSFER SYSTEM BASED ON INPUT POWER
Integrated Device Technology, Inc.	9,553,570	Crystal-Less Jitter Attenuator
Integrated Device Technology, Inc.	9,553,602	METHODS AND SYSTEMS FOR ANALOG-TO-DIGITAL CONVERSION (ADC) USING AN ULTRA SMALL CAPACITOR ARRAY WITH FULL RANGE AND SUB-RANGE MODES
Integrated Device Technology, Inc.	9,553,954	METHOD AND APPARATUS UTILIZING PACKET SEGMENT COMPRESSION PARAMETERS FOR COMPRESSION IN A COMMUNICATION SYSTEM
Integrated Device Technology, Inc.	9,577,438	Wireless Power System
Integrated Device Technology, Inc.	9,581,973	DUAL MODE CLOCK USING A COMMON RESONATOR AND ASSOCIATED METHOD OF USE
Integrated Device Technology, Inc.	9,583,155	Single-Ended Signal Slicer with a Wide Input Voltage Range
Integrated Device Technology, Inc.	9,583,175	RECEIVER EQUALIZATION CIRCUIT WITH CROSS COUPLED TRANSISTORS AND/OR RC IMPEDANCE
Integrated Device Technology, Inc.	6,115,586	MULTIPLE LOOP RADIO FREQUENCY SYNTHESIZER (ICS case)
Integrated Device Technology, Inc.	7,046,093	DYNAMIC PHASE-LOCKED LOOP CIRCUITS AND METHODS OF OPERATION THEREOF
Integrated Device Technology, Inc.	7,292,064	MINIMIZING TIMING SKEW AMONG CHIP LEVEL OUTPUTS FOR REGISTERED OUTPUT SIGNALS
Integrated Device Technology, Inc.	7,298,221	Phase-Locked Loop Circuits With Current Mode Loop Filters
Integrated Device Technology, Inc.	7,308,596	METHOD AND APPARATUS FOR CLOCK AND PLL N-DIVIDER SWITCH
Integrated Device Technology, Inc.	7,375,558	Method and Apparatus for Pre-clocking
Integrated Device Technology, Inc.	7,400,178	DATA OUTPUT CLOCK SELECTION CIRCUIT FOR QUAD-DATA RATE INTERFACE
Integrated Device Technology, Inc.	7,558,151	Methods and Circuits for DDR-2 Memory Device Read Data Resynchronization
Integrated Device Technology, Inc.	7,564,268	Low power logic output buffer
Integrated Device Technology, Inc.	7,567,100	INPUT CLOCK DETECTION CIRCUIT FOR POWERING DOWN A PLL-BASED SYSTEM

Integrated Device Technology, Inc.	7,573,303	Digital Controlled System On-Chip (SOC) Clock Generator
Integrated Device Technology, Inc.	7,586,347	Clock Generator with Self-Bias Bandwidth Control
Integrated Device Technology, Inc.	7,598,775	Phase and Frequency Detector with Zero Static Phase Error
Integrated Device Technology, Inc.	7,629,816	Method and Apparatus for Pre-clocking
Integrated Device Technology, Inc.	7,702,061	HIGH SPEED HYBRID STRUCTURE COUNTER HAVING SYNCHRONOUS TIMING AND ASYNCHRONOUS COUNTER CELLS
Integrated Device Technology, Inc.	7,710,165	VOLTAGE-TO-CURRENT CONVERTER
Integrated Device Technology, Inc.	7,724,860	Auto-adaptive Digital Phase-locked Loop for Large Frequency Multiplication Factors
Integrated Device Technology, Inc.	7,734,002	PHASE DIFFERENCE DETECTOR HAVING CONCURRENT FINE AND COARSE CAPABILITIES
Integrated Device Technology, Inc.	7,737,739	Phase Step Clock Generator
Integrated Device Technology, Inc.	7,750,696	Phase-Locked Loop
Integrated Device Technology, Inc.	7,782,143	PHASE LOCKED LOOP AND DELAY LOCKED LOOP WITH CHOPPER STABILIZED PHASE OFFSET
Integrated Device Technology, Inc.	7,782,151	VCO Digital Range Selection
Integrated Device Technology, Inc.	7,816,959	Clock Circuit For Reducing Long Term Jitter
Integrated Device Technology, Inc.	7,886,176	DDR Memory System for Measuring a Clock Signal by Identifying a Delay Value Corresponding to a Changed Logic State During Clock Signal Transitions
Integrated Device Technology, Inc.	7,928,773	Multiple Frequency Synchronized Phase Clock Generator
Integrated Device Technology, Inc.	7,928,779	METHODS AND APPARATUSES FOR INCREMENTAL BANDWIDTH CHANGES RESPONSIVE TO FREQUENCY CHANGES OF A PHASE-LOCKED LOOP
Integrated Device Technology, Inc.	7,956,693	METHOD AND APPARATUS FOR ADJUSTING PLL AND/OR DLL TIMING OFFSETS
Integrated Device Technology, Inc.	8,009,719	Digital Spread Spectrum Method Based On Precise Phase Delta-Sigma Algorithm
Integrated Device Technology, Inc.	8,018,289	Holdover Circuit for Phase-Lock Loop
Integrated Device Technology, Inc.	8,022,738	Apparatus and Method for Detecting the Loss of an Input Clock Signal for a Phase-Locked Loop
Integrated Device Technology, Inc.	8,073,090	Synchronous De-Skew with Programmable Latency for Multi-Lane High Speed Serial Interface
Integrated Device Technology, Inc.	8,093,930	HIGH FREQUENCY FRACTIONAL-N DIVIDER

Integrated Device Technology, Inc.	8,164,367	SPREAD SPECTRUM CLOCK GENERATION TECHNIQUE FOR IMAGING APPLICATIONS
Integrated Device Technology, Inc.	8,259,888	Method of Processing Signal Data With Corrected Clock Phase Offset
Integrated Device Technology, Inc.	8,284,816	PUSH-PULL SPREAD SPECTRUM CLOCK SIGNAL GENERATOR
Integrated Device Technology, Inc.	8,312,241	SERIAL BUFFER TO SUPPORT REQUEST PACKETS WITH OUT OF ORDER RESPONSE PACKETS
Integrated Device Technology, Inc.	8,447,902	METHOD AND APPARATUS FOR PREDICTIVE SWITCHING
Integrated Device Technology, Inc.	8,509,372	Multi-Band Clock Generator with Adaptive Frequency Calibration and Enhanced Frequency Locking
Integrated Device Technology, Inc.	8,516,163	Hardware-Based Concurrent Direct Memory Access (DMA) Engines on Serial Rapid Input/Output SRIO Interface
Integrated Device Technology, Inc.	8,680,910	Method and Apparatus for Glitch-Free Switching of Multiple Phase Clock
ChipX Incorporated	7675811	METHOD AND APPARATUS FOR DQS POST DETECTION AND DRIFT COMPENSATION IN A DOUBLE DATA RATE (DDR) PHYSICAL INTERFACE
Endwave Corporation	6002305	TRANSITION BETWEEN CIRCUIT TRANSMISSION LINE AND MICROWAVE WAVEGUIDE
Endwave Corporation	6052582	SECTORIZED COMMUNICATION SYSTEM
Endwave Corporation	6064253	MULTIPLE STAGE SELF-BIASING RF TRANSISTOR CIRCUIT
Endwave Corporation	6094114	SLOTLINE-TO-SLOTLINE MOUNTED FLIP CHIP
Endwave Corporation	6600384	IMPEDANCE-COMPENSATING CIRCUIT
Endwave Corporation	7109583	MOUNTING WITH AUXILIARY BUMPS
Endwave Corporation	7119633	COMPENSATED INTERDIGITATED COUPLER
Endwave Corporation	7145414	TRANSMISSION LINE ORIENTATION TRANSITION
Endwave Corporation	7276988	MULTI-SUBSTRATE MICROSTRIP TO WAVEGUIDE TRANSITION
Endwave Corporation	7348666	CHIP-TO-CHIP TRENCH CIRCUIT STRUCTURE
Endwave Corporation	7588966	CHIP MOUNTING WITH FLOWABLE LAYER
Endwave Corporation	7710219	MERGED-FILTER MULTIPLEXER
GigPeak, Inc.	9275690	POWER MANAGEMENT IN AN ELECTRONIC SYSTEM

		THROUGH REDUCING ENERGY USAGE OF A BATTERY AND/OR CONTROLLING AN OUTPUT POWER OF AN AMPLIFIER THEREOF
GigPeak, Inc.	9509351	SIMULTANEOUS ACCOMMODATION OF A LOW POWER SIGNAL AND AN INTERFERING SIGNAL IN A RADIO FREQUENCY (RF) RECEIVER
Endwave Corporation	RE35,869	MINIATURE ACTIVE CONVERSION BETWEEN MICROSTRIP AND COPLANAR WAVE GUIDE
GigPeak, Inc.	US 8860481 B1	Reducing Charge Imbalance In A Charge Pump Of A Phase Locked Loop (Pll) Through Maintaining An Output Node Thereof At A Same Voltage As A Bypass Node Thereof
GigPeak, Inc.	8626098	AUTOMATIC GAIN CONTROL WITH PROGRAMMABLE ATTACK AND DECAY TIMES
GigPeak, Inc.	7760781	Current Control Mechanism for Low Voltage Application
GigPeak, Inc.	8008904	Voltage and Temperature Invariant Gain Control Circuit
GigPeak, Inc.	8054128	Current Control Mechanism For Low Voltage Applications
Magnum Semiconductor, Inc.	7,894,681	SEQUENTIAL DECODING OF PROGRESSIVE CODED JPEGS
Magnum Semiconductor, Inc.	8,378,867	AUDIO CLOCKING IN VIDEO APPLICATIONS
Magnum Semiconductor, Inc.	8,487,797	AUDIO CLOCKING IN VIDEO APPLICATIONS
Magnum Semiconductor, Inc.	9,118,425	TRANSPORT STREAM MULTIPLEXERS AND METHODS FOR PROVIDING PACKETS ON A TRANSPORT STREAM
Magnum Semiconductor, Inc.	9,154,782	APPARATUSES AND METHODS FOR ADJUSTING COEFFICIENTS USING DEAD ZONES
Magnum Semiconductor, Inc.	9,258,517	METHODS AND APPARATUSES FOR ADAPTIVELY FILTERING VIDEO SIGNALS
Magnum Semiconductor, Inc.	9,277,254	TRANSPORT STREAM MULTIPLEXERS AND METHODS FOR PROVIDING PACKETS ON A TRANSPORT STREAM
Magnum Semiconductor, Inc.	9,392,286	APPARATUSES AND METHODS FOR PROVIDING QUANTIZED COEFFICIENTS FOR VIDEO ENCODING
Magnum Semiconductor, Inc.	6108047	VARIABLE-SIZE SPATIAL AND TEMPORAL VIDEO SCALER
Magnum Semiconductor, Inc.	6192075	Single-Pass Variable Bit-Rate Control for Digital Video Coding
Magnum Semiconductor, Inc.	6320905	Postprocessing System for Removing Blocking Artifacts in Block-Based Codecs
Magnum Semiconductor, Inc.	6584156	LSI Architecture and Implementation of MPEG Video Codec

Magnum Semiconductor, Inc.	6950605	A METHOD AND APPARATUS FOR RECORDABLE FOR RECORDABLE REAL-TIME AUDIO/VIDEO INFORMATION ONTO RECORDABLE COMPACT DISK DRIVES"
Magnum Semiconductor Inc.	7391468	TELECINE CONVERSION DETECTION FOR PROGRESSIVE SCAN PLAYBACK
Magnum Semiconductor Inc.	7414671	SYSTEMS AND METHODS FOR DISPLAY OBJECT EDGE DETECTION AND PIXEL DATA INTERPOLATION IN VIDEO
Magnum Semiconductor Inc.	7420626	SYSTEMS AND METHODS FOR DETECTING A CHANGE IN A SEQUENCE OF INTERLACED DATA FIELDS GENERATE
Magnum Semiconductor Inc.	7450184	CIRCUITS AND METHODS FOR DETECTING 2:2 ENCODED VIDEO AND SYSTEMS UTILIZING THE SAME
Magnum Semiconductor Inc.	7522214	CIRCUITS AND METHODS FOR DEINTERLACING VIDEO DISPLAY DATA AND SYSTEMS USING THE SAME
Magnum Semiconductor Inc.	7538824	SYSTEMS AND METHODS FOR REDUCING NOISE DURING VIDEO DEINTERLACING
Magnum Semiconductor Inc.	7574580	INTELLIGENT CACHING SCHEME FOR STREAMING FILE SYSTEMS
Magnum Semiconductor Inc.	7587131	AUDIO CLOCKING IN VIDEO APPLICATIONS
Magnum Semiconductor Inc.	7613615	CIRCUITS, SYSTEMS, AND METHODS FOR REAL- TIME DE SHUFFLING OF SHUFFLED AUDIO DATA
Magnum Semiconductor Inc.	7646968	END-USER CONFIGURABLE DIGITAL VERSATILE DISK MENUS AND METHODS FOR GENERATING THE SAME
Magnum Semiconductor Inc.	7974523	OPTIMAL BUFFERING AND SCHEDULING STRATEGY FOR SMOOTH REVERSE IN A DVD PLAYER OR THE LIKE
GigPeak, Inc.	9531070	Extending Beamforming Capability Of A Coupled Voltage Controlled Oscillator (Vco) Array During Local Oscillator (Lo) Signal Generation Through Accommodating Differential Coupling Between Vcos Thereof
GigPeak, Inc.	9384789	Power Management In An Electronic System Through Reducing Energy Usage Of A Battery And/Or Controlling An Output Power Of An Amplifier Thereof
GigPeak, Inc.	9384786	Power Management In An Electronic System Through Reducing Energy Usage Of A Battery And/Or Controlling An Output Power Of An Amplifier Thereof
GigPeak, Inc.	9184498	Extending Beamforming Capability Of A Coupled Voltage Controlled Oscillator (Vco) Array During Local Oscillator (Lo) Signal Generation Through Fine Control Of A Tunable Frequency Of A Tank Circuit Of A Vco Thereof
Integrated Device Technology, Inc.	8680910	Method And Apparatus For Glitch-Free Switching Of Multiple Phase Clock
Integrated Device Technology, Inc.	8516163	Hardware-Based Concurrent Direct Memory Access (Dma) Engines On Serial Rapid Input/Output Srio Interface
Integrated Device Technology, Inc.	8509372	Multi-Band Clock Generator With Adaptive Frequency Calibration And Enhanced Frequency Locking
Integrated Device Technology, Inc.	8447902	Method And Apparatus For Predictive Switching

Integrated Device Technology, Inc.	8426233	Methods Of Packaging Microelectromechanical Resonators
Integrated Device Technology, Inc.	8312241	Serial Buffer To Support Request Packets With Out Of Order Response Packets
Integrated Device Technology, Inc.	8284816	Push-Pull Spread Spectrum Clock Signal Generator
Integrated Device Technology, Inc.	8259888	Method Of Processing Signal Data With Corrected Clock Phase Offset
Integrated Device Technology, Inc.	8164367	Spread Spectrum Clock Generation Technique For Imaging Applications
Integrated Device Technology, Inc.	8093930	High Frequency Fractional-N Divider
Integrated Device Technology, Inc.	8073090	Synchronous De-Skew With Programmable Latency For Multi-Lane High Speed Serial Interface
Integrated Device Technology, Inc.	8022738	Apparatus And Method For Detecting The Loss Of An Input Clock Signal For A Phase-Locked Loop
Integrated Device Technology, Inc.	8018289	Holdover Circuit For Phase-Lock Loop
Integrated Device Technology, Inc.	8009719	Digital Spread Spectrum Method Based On Precise Phase Delta-Sigma Algorithm
Integrated Device Technology, Inc.	8004339	Apparatuses And Methods For A Level Shifter With Reduced Shoot-Through Current
Integrated Device Technology, Inc.	7956693	Method And Apparatus For Adjusting Pll And/Or Dll Timing Offsets
Integrated Device Technology, Inc.	7928779	Methods And Apparatuses For Incremental Bandwidth Changes Responsive To Frequency Changes Of A Phase-Locked Loop
Integrated Device Technology, Inc.	7928773	Multiple Frequency Synchronized Phase Clock Generator
Integrated Device Technology, Inc.	7886176	Ddr Memory System For Measuring A Clock Signal By Identifying A Delay Value Corresponding To A Changed Logic State During Clock Signal Transitions
Integrated Device Technology, Inc.	7816959	Clock Circuit For Reducing Long Term Jitter
Endwave Corporation	7813145	Circuit Structure With Multifunction Circuit Cover
Integrated Device Technology, Inc.	7782151	Vco Digital Range Selection
Integrated Device Technology, Inc.	7782143	Phase Locked Loop And Delay Locked Loop With Chopper Stabilized Phase Offset
Integrated Device Technology, Inc.	7750696	Phase-Locked Loop
Integrated Device Technology, Inc.	7737739	Phase Step Clock Generator

Integrated Device Technology, Inc.	7734002	Phase Difference Detector Having Concurrent Fine And Coarse Capabilities
Integrated Device Technology, Inc.	7724860	Auto-Adaptive Digital Phase-Locked Loop For Large Frequency Multiplication Factors
Integrated Device Technology, Inc.	7710165	Voltage-To-Current Converter
Integrated Device Technology, Inc.	7629816	Method And Apparatus For Pre-Clocking
Integrated Device Technology, Inc.	7598775	Phase And Frequency Detector With Zero Static Phase Error
Integrated Device Technology, Inc.	7586347	Clock Generator With Self-Bias Bandwidth Control
Integrated Device Technology, Inc.	7573303	Digitally Controlled System On-Chip (Soc) Clock Generator
Integrated Device Technology, Inc.	7567100	Input Clock Detection Circuit For Powering Down A Pll-Based System
Integrated Device Technology, Inc.	7564268	Low Power Logic Output Buffer
Integrated Device Technology, Inc.	7558151	Methods And Circuits For Ddr-2 Memory Device Read Data Resynchronization
Integrated Device Technology, Inc.	7545231	Transconductance And Current Modulation For Resonant Frequency Control And Selection
Integrated Device Technology, Inc.	7545227	Low-Latency Start-Up For A Monolithic Clock Generator And Timing/Frequency Reference
Integrated Device Technology, Inc.	7522395	Electrostatic Discharge And Electrical Overstress Protection Circuit
Integrated Device Technology, Inc.	7400178	Data Output Clock Selection Circuit For Quad-Data Rate Interface
Integrated Device Technology, Inc.	7375558	Method And Apparatus For Pre-Clocking
Integrated Devices Technology Inc.	7308596	Controlling A Clock Divider By Selecting A Preset Value
Integrated Device Technology, Inc.	7298221	Phase-Locked Loop Circuits With Current Mode Loop Filters
Integrated Device Technology, Inc.	7292064	Minimizing Timing Skew Among Chip Level Outputs For Registered Output Signals
Integrated Device Technology, Inc.	7046093	Dynamic Phase-Locked Loop Circuits And Methods Of Operation Thereof
Integrated Device Technology, Inc.	6954113	Programmable Oscillator Circuit
Integrated Device Technology, Inc.	6894356	Sram System Having Very Lightly Doped Sram Load Transistors For Improving Sram Cell Stability And Method For Making The Same

Integrated Device Technology, Inc.	6763406	Noise Reduction System And Method For Reducing Switching Noise In An Interface To A Large Width Bus
Integrated Device Technology, Inc.	6664860	Programmable Oscillator Circuit And Method
Integrated Device Technology, Inc.	6614798	Asynchronous Fifo Increment And Decrement Control For Interfaces That Operate At Differing Clock Frequencies
Integrated Device Technology, Inc.	6381684	Quad Data Rate Ram
Integrated Device Technology, Inc.	6307757	High Frequency Switch-Mode Dc Powered Computer System
Integrated Device Technology, Inc.	6216239	Testing Method And Apparatus For Identifying Disturbed Cells Within A Memory Cell Array
Integrated Device Technology, Inc.	6215708	Charge Pump For Improving Memory Cell Low Vcc Performance Without Increasing Gate Oxide Thickness
Integrated Device Technology, Inc.	6115586	Multiple Loop Radio Frequency Synthesizer
Integrated Device Technology, Inc.	6067319	Method For Equalization Of A Quadrature Amplitude Modulated Signal
Magnum Semiconductor Inc.	6011870	Multiple Stage And Low-Complexity Motion Estimation For Interframe Video Coding
Integrated Device Technology, Inc.	5997174	Method For Determining A Thermal Parameter Of A Device By Measuring Thermal Resistance Of A Substrate Carrying The Device
Integrated Device Technology, Inc.	5968193	Dual Site Loadboard Tester
Integrated Device Technology, Inc.	5798532	Apparatus For Determining Warp In Semiconductor Wafer Cassettes
Integrated Device Technology, Inc.	RE36907	Leadframe With Power And Ground Planes
Integrated Device Technology, Inc.	6188290	Method Of Initializing An Oscillator Circuit
Integrated Device Technology, Inc.	9590637	HIGH-SPEED PROGRAMMABLE FREQUENCY DIVIDER WITH 50% OUTPUT DUTY CYCLE
Integrated Device Technology, Inc.	9589626	Single-Ended Memory Signal Equalization at Power Up

Applications:

Owner	Application No.	Title
Integrated Device Technology, Inc.	14/700,075	Acoustic-Wave Filter Based Voltage-Controlled Oscillator and Method of Use
Integrated Device Technology, Inc.	15/050,646	ADAPTIVE HIGH-SPEED CURRENT-STEERING LOGIC (HCSL) DRIVERS

Integrated Device Technology, Inc.	15/067,025	ADAPTIVE RESONANT TOPOLOGY APPLICATION IN WEARABLE DEVICES
Integrated Device Technology, Inc.	13/247,654	Apparatus and Method for a Switching Power Converter
Integrated Device Technology, Inc.	14/594,064	Apparatus, System, and Method for Back-Channel Communication in an Inductive Wireless Power Transfer System
Integrated Device Technology, Inc.	14/109,743	Apparatuses and Methods for Over-Current Protection of DC-DC Voltage Converters
Integrated Device Technology, Inc.	14/670,385	APPARATUSES AND RELATED METHODS FOR A WIRELESS POWER RECEIVER USING COUPLED INDUCTORS
Integrated Device Technology, Inc.	14/168,965	Apparatuses and Related Methods for Communication with a Wireless Power Receiver
Integrated Device Technology, Inc.	14/499,009	Apparatuses and Related Methods for Detecting Coil Alignment with a Wireless Power Receiver
Integrated Device Technology, Inc.	14/603,275	Apparatuses and Related Methods for Detecting Magnetic Flux Field Characteristics with a Wireless Power Receiver
Integrated Device Technology, Inc.	14/603,296	Apparatuses and Related Methods for Detecting Magnetic Flux Field Characteristics With a Wireless Power Transmitter
Integrated Device Technology, Inc.	14/494,536	Apparatuses and Related Methods for Generating Wireless Status Indications for a Wireless Power Transfer System
Integrated Device Technology, Inc.	13/801,953	Apparatuses and Related Methods for Modulating Power of a Wireless Power Receiver
Integrated Device Technology, Inc.	14/670,371	Apparatuses and Wireless Power Transmitters Having Multiple Transmit Coils and Related Method
Integrated Device Technology, Inc.	13/434,658	APPARATUSES HAVING DIFFERENT MODES OF OPERATION FOR INDUCTIVE WIRELESS POWER TRANSFER AND RELATED METHOD
Integrated Device Technology, Inc.	15/089,138	Arbitrary Delay Buffer
Integrated Device Technology, Inc.	15/059,609	ASYMMETRIC ON-STATE RESISTANCE DRIVER OPTIMIZED FOR MULTI-DROP DDR4
Integrated Device Technology, Inc.	14/956,955	ASYMMETRICAL EMPHASIS IN A MEMORY DATA BUS DRIVER
Integrated Device Technology, Inc.	15/064,369	Buffer with Programmable Input/Output Phase Relationship
Integrated Device Technology, Inc.	15/084,067	Bulk Acoustic Wave Resonator Based Fractional Frequency Synthesizer and Method of Use
Integrated Device Technology, Inc.	15/169,997	CALIBRATION METHOD AND APPARATUS FOR PHASE LOCKED LOOP CIRCUIT
Integrated Device Technology, Inc.	15/367,742	COMMAND SEQUENCE RESPONSE IN A MEMORY DATA BUFFER

Integrated Device Technology, Inc.	15/367,672	COMPENSATION OF DETERMINISTIC CROSSTALK IN MEMORY SYSTEM
Integrated Device Technology, Inc.	14/297,598	Constant Impedance Switch
Integrated Device Technology, Inc.	14/964,953	Design for Testability/Manufacturing Method to Adjust Output Skew Timing
Integrated Device Technology, Inc.	15/265,125	DUAL USE BOOTSTRAP DRIVER
Integrated Device Technology, Inc.	14/675,591	Enhanced Data Buffer & Intelligent NV Controller for Simultaneous DRAM and Flash Memory Access
Integrated Device Technology, Inc.	15/226,286	Fast-Response Hybrid Lock Detector
Integrated Device Technology, Inc.	15/090,818	Flexible Point-To-Point Memory Topology
Integrated Device Technology, Inc.	15/185,378	FRACTIONAL DIVIDER USING A CALIBRATED DIGITAL-TO-TIME CONVERTER
Integrated Device Technology, Inc.	15/347,050	FREQUENCY DIVIDER WITH SELECTABLE FREQUENCY AND DUTY CYCLE
Integrated Device Technology, Inc.	15/210,204	FREQUENCY SYNTHESIZER WITH MICROCODE CONTROL
Integrated Device Technology, Inc.	15/408,655	FREQUENCY SYNTHESIZER WITH TUNABLE ACCURACY
Integrated Device Technology, Inc.	15/405,089	GAS SENSOR
Integrated Device Technology, Inc.	15/257,026	HIERARCHICAL CACHING AND ANALYTICS
Integrated Device Technology, Inc.	14/688,835	High Efficiency Wireless Power System
Integrated Device Technology, Inc.	15/440,463	High frequency wireless power rectifier startup circuit design
Integrated Device Technology, Inc.	14/673,518	High Q Power Inductors Using Magnetic Substrates
Integrated Device Technology, Inc.	14/042,658	HIGH SPEED, LOW POWER, ISOLATED BUFFER
Integrated Device Technology, Inc.	15/367,586	INFERRING SAMPLED DATA IN DECISION FEEDBACK EQUALIZER AT RESTART OF FORWARDED CLOCK IN MEMORY SYSTEM
Integrated Device Technology, Inc.	15/083,068	Integrated Low Voltage Differential Signaling (LVDS) and High-Speed Current Steering Logic (HCSL) Circuit and Method of Use
Integrated Device Technology, Inc.	14/675,498	Low Latency Interconnect Integrated Event Handling
Integrated Device Technology, Inc.	13/870,912	Low Noise Variable Gain Amplifier Utilizing Variable Feedback Techniques with Constant Input/Output Impedance
Integrated Device Technology, Inc.	15/388,224	MEMORY CHANNEL SELECTION CONTROL

Integrated Device Technology, Inc.	13/244,429	Method and Apparatus for a Transmission Gate for Multi-GB/s Application
Integrated Device Technology, Inc.	62/298,341	Method and Apparatus for flexible Coherent and Scale-out Computing architecture
Integrated Device Technology, Inc.	14/514,736	Method and Apparatus for Operating of a PCIe Retimer Over Optical Cable
Integrated Device Technology, Inc.	13/804,326	Method and Apparatus for Process Compensated On-Die Constant Delay
Integrated Device Technology, Inc.	14/673,818	Methods and Apparatus for Efficient Network Analytics and Computing Card
Integrated Device Technology, Inc.	14/673,724	Methods and Apparatus for IO, Processing and Memory bandwidth optimization for Analytics Systems
Integrated Device Technology, Inc.	14/975,226	Methods and Apparatus for Transmitting Data Over a Clock Signal
Integrated Device Technology, Inc.	15/054,043	METHODS AND APPARATUSES FOR A UNIFIED COMPRESSION FRAMEWORK OF BASEBAND SIGNALS
Integrated Device Technology, Inc.	14/498,730	METHODS AND APPARATUSES FOR POWER CONTROL DURING BACKSCATTER MODULATION IN WIRELESS POWER RECEIVERS
Integrated Device Technology, Inc.	14/992,486	Methods of Estimating Frequency Skew in Networks Using Timestamped Packets
Integrated Device Technology, Inc.	14/015,573	Methods of Performing Packet-Based M-Estimation of Clock Frequency Skew in Network Environments
Integrated Device Technology, Inc.	15/136,962	Multi-Bit Cell Attenuator
Integrated Device Technology, Inc.	15/268,328	Multimode Wireless Power Receiver Circuit
Integrated Device Technology, Inc.	13/958,389	Multimode Wireless Power Receivers and Related Methods
Integrated Device Technology, Inc.	15/405,861	Multimode Wireless Power Transmitters and Related Methods
Integrated Device Technology, Inc.	14/046,628	Multimode Wireless Power Transmitters and Related Methods
Integrated Device Technology, Inc.	12/976,853	Novel Adaptive Equalization Technique For SerDes Links
Integrated Device Technology, Inc.	15/367,968	NULLIFYING INCORRECT SAMPLED DATA CONTRIBUTION IN DECISION FEEDBACK EQUALIZER AT RESTART OF FORWARDED CLOCK IN MEMORY SYSTEM
Integrated Device Technology, Inc.	15/178,650	On-Die Verification of Resistor Fabricated In CMOS Process
Integrated Device Technology, Inc.	14/576,288	Overvoltage Protection Circuits and Method of Operating Same

Integrated Device Technology, Inc.	14/871,411	PACKAGED OSCILLATORS WITH BUILT-IN SELF-TEST CIRCUITS THAT SUPPORT RESONATOR TESTING WITH REDUCED PIN COUNT
Integrated Device Technology, Inc.	15/015,094	Phase Locked Loop (PLL) Timing Device Evaluation System and Method for Evaluating PLL Timing Devices
Integrated Device Technology, Inc.	62/270,883	Phase, Amplitude and Gate-Bias Optimizer for Doherty Amplifier
Integrated Device Technology, Inc.	14/675,369	Process Compensated Delay
Integrated Device Technology, Inc.	15/081,723	Programmable Low Power High-Speed Current Steering Logic (LPHCSL) Driver and Method of Use
Integrated Device Technology, Inc.	62/281,332	Pulse Width Modulation (PWM) to align clocks across multiple separated cards within a Communication System
Integrated Device Technology, Inc.	15/364,602	Pulse Width Modulation (PWM) to Align Clocks Across Multiple Separated Cards Within a Communication System
Integrated Device Technology, Inc.	14/671,785	Reduced VSWR Switching
Integrated Device Technology, Inc.	15/439,190	Resynchronization of a clock associated with each data bit in a double data rate memory system
Integrated Device Technology, Inc.	15/247,854	Scalable Coherent Apparatus and Method
Integrated Device Technology, Inc.	15/000,214	Signal Driver Slew Rate Control
Integrated Device Technology, Inc.	14/714,034	Spread Spectrum Clock Modulator with Variable Profile Modulation Signal
Integrated Device Technology, Inc.	14/498,941	SUPPRESSION OF AUDIBLE HARMONICS IN WIRELESS POWER RECEIVERS
Integrated Device Technology, Inc.	15/386,095	SYNCHRONIZATION AFTER A MEMORY MODE SWITCH
Integrated Device Technology, Inc.	14/958,701	System and Method for Deskewing Output Clock Signals
Integrated Device Technology, Inc.	14/967,050	System and Method for Improving Dicing Quality for Bonded Wafer Pairs
Integrated Device Technology, Inc.	14/967,007	System and Method for Providing Hermetically Sealed Packages with Consistent Vacuum Cavity
Integrated Device Technology, Inc.	14/109,356	System and Method for Remote Direct Memory Access (RDMA) in RapidIO Based Communication Systems
Integrated Device Technology, Inc.	14/710,479	SYSTEM AND METHOD FOR SYNCHRONOUS RECTIFICATION WITH ENHANCED DETECTION OF SMALL CURRENTS
Integrated Device Technology, Inc.	PCT/US2016/059177	SYSTEM AND METHOD FOR WIRELESS POWER TRANSFER USING A POWER CONVERTER WITH A BYPASS MODE
Integrated Device Technology, Inc.	14/702,464	System and Method for Wireless Power Transfer Using a Two Half-Bridge to One Full-Bridge Switchover

Integrated Device Technology, Inc.	14/752,719	System and Method for Wireless Power Transfer Using Automatic Power Supply Selection
Integrated Device Technology, Inc.	14/830,698	Timing Device Having Multi-Purpose PIN with Proactive Function
Integrated Device Technology, Inc.	14/975,530	Wireless Power Receiver with Magnetic Data Transaction Capability
Integrated Device Technology, Inc.	14/872,483	Wireless Power Transmitter
Integrated Device Technology, Inc.	14/075,749	WIRELESS POWER TRANSMITTER HAVING LOW NOISE AND HIGH EFFICIENCY, AND RELATED METHODS
GigPeak, Inc.	14/948,052	Current steering in high sensitivity transimpedance amplifiers
GigPeak, Inc.	15252877	Adjustable Termination Circuit for High Speed Laser Driver
Magnum Semiconductor, Inc.	13/434,736 9491475	APPARATUSES AND METHODS FOR PROVIDING QUANTIZED COEFFICIENTS FOR VIDEO ENCODING
Magnum Semiconductor, Inc.	13/454,669	APPARATUSES AND METHODS FOR BITSTREAM BITSTUFFING
Magnum Semiconductor, Inc.	13/627,776	APPARATUSES AND METHODS FOR OPTIMIZING RATE-DISTORTION OF SYNTAX ELEMENTS
Magnum Semiconductor, Inc.	13/889,778	SYSTEMS, APPARATUSES, AND METHODS FOR TRANSCODING A BITSTREAM
Magnum Semiconductor, Inc.	14/161,930	METHODS AND APPARATUSES FOR CONTENT-ADAPTIVE QUANTIZATION PARAMETER MODULATION TO IMPROVE VIDEO QUALITY IN LOSSY VIDEO CODING
Magnum Semiconductor, Inc.	14/495,583	APPARATUSES AND METHODS FOR FILTERING NOISE FROM A VIDEO SIGNAL
Magnum Semiconductor, Inc.	14/316,329	METHODS AND APPARATUSES FOR EDGE PRESERVING AND/OR EDGE ENHANCING SPATIAL FILTER
Magnum Semiconductor, Inc.	14/746,146	Methods And Apparatuses For Filtering Of Ringing Artifacts Post Decoding
Magnum Semiconductor, Inc.	14/747,879	Apparatuses And Methods For Performing Information Extraction And Insertion On Bitstreams
Magnum Semiconductor, Inc.	14/717,931	Method For Time-Dependent Visual Quality Encoding For Broadcast Services
Magnum Semiconductor, Inc.	13/743,091	METHODS AND APPARATUSES FOR PROVIDING AN ADAPTIVE REDUCED RESOLUTION UPDATE MODE
Magnum Semiconductor, Inc.	13/627,776	APPARATUSES AND METHODS FOR OPTIMIZING RATE-DISTORTION OF SYNTAX ELEMENTS
Magnum Semiconductor, Inc.	14/326,211	METHODS AND APPARATUSES FOR STRIPE-BASED TEMPORAL AND SPATIAL VIDEO PROCESSING
Magnum Semiconductor, Inc.	14/592,539	APPARATUSES AND METHODS FOR EFFICIENT RANDOM NOISE ENCODING

Magnum Semiconductor, Inc.	14/705,658	METHOD AND APPARATUS FOR PARALLEL DECODING
GigPeak, Inc.	15144193	Gaas/Sige-Bicmos-Based Transceiver System-In-Package For E-Band Frequency Applications
GigPeak, Inc.	13629633	Automatic Gain Control With Programmable Attack And Decay Times
GigPeak, Inc.	14215518	Extending Beamforming Capability Of A Coupled Voltage Controlled Oscillator (Vco) Array During Local Oscillator (Lo) Signal Generation Through Frequency Multiplication
GigPeak, Inc.	14215650	Extending Beamforming Capability Of A Coupled Voltage Controlled Oscillator (Vco) Array During Local Oscillator (Lo) Signal Generation Through A Circular Configuration Thereof
GigPeak, Inc.	14215778	Phase Shift Based Improved Reference Input Frequency Signal Injection Into A Coupled Voltage Controlled Oscillator (Vco) Array During Local Oscillator (Lo) Signal Generation To Reduce A Phase-Steering Requirement During Beamforming
GigPeak, Inc.	14217298	Adaptive Transmit Array For Beam-Steering
GigPeak, Inc.	14217351	Automatic High-Resolution Adaptive Beam-Steering
Integrated Device Technology, Inc.	14164939	Monolithic Clock Generator And Timing/Frequency Reference
Integrated Device Technology, Inc.	14190890	Process Compensated Delay
Integrated Device Technology, Inc.	14498941	Suppression Of Audible Harmonics In Wireless Power Receivers
Integrated Device Technology, Inc.	14671785	Reduced Vswr Switching
Integrated Device Technology, Inc.	15089413	System And Method For Wireless Power Transfer Using Over-Voltage Protection
Magnum Semiconductor Inc.	13467624	Apparatuses And Methods For Estimating Bitstream Bit Counts
Magnum Semiconductor Inc.	14133261	Apparatuses And Methods For Optimizing Rate-Distortion Costs In Video Encoding

*Reflects recorded name with USPTO.