PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4367983

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
VENTURE LENDING & LEASING VI, INC.	04/06/2017
VENTURE LENDING & LEASING VII, INC.	04/06/2017

RECEIVING PARTY DATA

Name:	D-WAVE SYSTEMS INC.
Street Address:	3033 BETA AVENUE
City:	BURNABY, BRITISH COLUMBIA
State/Country:	CANADA
Postal Code:	V5G 4M9

PROPERTY NUMBERS Total: 145

Property Type	Number
Application Number:	09452479
Application Number:	09855817
Application Number:	09479336
Application Number:	09855487
Application Number:	09637514
Application Number:	09810818
Application Number:	10058181
Application Number:	09839636
Application Number:	09839637
Application Number:	09823895
Application Number:	10192623
Application Number:	10117696
Application Number:	09875776
Application Number:	10155746
Application Number:	10194704
Application Number:	09872495
Application Number:	10791617
Application Number:	10232136
Application Number:	10231385

PATENT REEL: 042252 FRAME: 0256

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Property Type	Number
Application Number:	10025848
Application Number:	10025818
Application Number:	10351631
Application Number:	10351632
Application Number:	10321065
Application Number:	10121817
Application Number:	10121810
Application Number:	10121800
Application Number:	10321941
Application Number:	10746992
Application Number:	10134665
Application Number:	10445096
Application Number:	10419024
Application Number:	10801335
Application Number:	10801340
Application Number:	10798737
Application Number:	10801336
Application Number:	10719925
Application Number:	10845638
Application Number:	10934049
Application Number:	12845352
Application Number:	11092953
Application Number:	11093205
Application Number:	11625702
Application Number:	11089650
Application Number:	11089653
Application Number:	11267459
Application Number:	11267478
Application Number:	11317838
Application Number:	11608214
Application Number:	12397999
Application Number:	13210169
Application Number:	13608836
Application Number:	14175731
Application Number:	11247857
Application Number:	12575345
Application Number:	11411051
Application Number:	12618554

Property Type	Number
Application Number:	11850437
Application Number:	11484368
Application Number:	12848764
Application Number:	12849588
Application Number:	11932248
Application Number:	13156172
Application Number:	11765361
Application Number:	13462494
Application Number:	11777910
Application Number:	11932261
Application Number:	12946643
Application Number:	12238147
Application Number:	11948817
Application Number:	12703534
Application Number:	12194282
Application Number:	12120354
Application Number:	13325785
Application Number:	11950276
Application Number:	12944509
Application Number:	13228219
Application Number:	12109847
Application Number:	12909682
Application Number:	12013192
Application Number:	12016801
Application Number:	12016709
Application Number:	12113753
Application Number:	12098348
Application Number:	12098347
Application Number:	13539039
Application Number:	12106024
Application Number:	13284418
Application Number:	12135899
Application Number:	12256330
Application Number:	12256332
Application Number:	13416794
Application Number:	12193601
Application Number:	12236040
Application Number:	12473970

Property Type	Number
Application Number:	13529664
Application Number:	12247475
Application Number:	12266378
Application Number:	12865341
Application Number:	12262417
Application Number:	12811067
Application Number:	13863218
Application Number:	12242133
Application Number:	12991888
Application Number:	12483971
Application Number:	12922626
Application Number:	12934254
Application Number:	13611672
Application Number:	14273200
Application Number:	12945717
Application Number:	12991891
Application Number:	13958339
Application Number:	12991889
Application Number:	12503671
Application Number:	12992049
Application Number:	12991893
Application Number:	12992047
Application Number:	14186895
Application Number:	12992057
Application Number:	12944518
Application Number:	14255561
Application Number:	13011697
Application Number:	13707210
Application Number:	13050742
Application Number:	13863962
Application Number:	13808006
Application Number:	13300169
Application Number:	13806404
Application Number:	13615075
Application Number:	14383837
Application Number:	13673578
Application Number:	13678266
Application Number:	14050062

Property Type	Number
Application Number:	13796949
Application Number:	14109604
Application Number:	14175722
Application Number:	14086697
Application Number:	14109657
Application Number:	14109663
Application Number:	14250041
Application Number:	14163838
Application Number:	14173101
Application Number:	14280204
Application Number:	14316372
Application Number:	14284138

CORRESPONDENCE DATA

Fax Number: (206)682-6031

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 206-622-4900 dons@seedip.com

Correspondent Name: SEED IP LAW GROUP LLP

Address Line 1: 701 FIFTH AVENUE

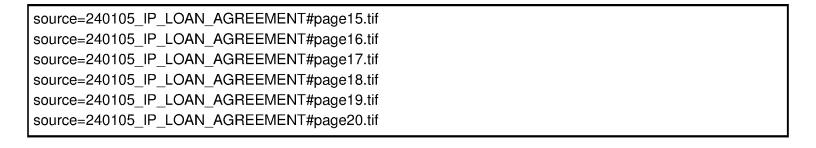
Address Line 2: SUITE 5400

Address Line 4: SEATTLE, WASHINGTON 98104

ATTORNEY DOCKET NUMBER:	240105.076
NAME OF SUBMITTER:	FRANK ABRAMONTE
SIGNATURE:	/Frank Abramonte/
DATE SIGNED:	04/13/2017

Total Attachments: 20

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VENTURE LENDING & LEASING VI, INC. VENTURE LENDING & LEASING VII, INC.

104 La Mesa Drive, Suite 102 Portola Valley, CA 94028

April 6, 2017

D-Wave Systems, Inc. 3033 Beta Ave. Burnaby, BC V5G4M9

Ladies and Gentlemen:

D-Wave Systems, Inc., a British Colombia corporation ("Borrower"), has paid in full all indebtedness under that certain Loan and Security Agreements, dated as of March 29, 2007, between Borrower and Venture Lending & Leasing IV, Inc., and Venture Lending & Leasing V, Inc., Loan and Security Agreements, dated as of December 15, 2009, between Borrower and Venture Lending & Leasing V, Inc., Loan and Security Agreements, dated as of August 31, 2010, between Borrower and Venture Lending & Leasing V, Inc., and Venture Lending & Leasing VI, Inc., Loan and Security Agreements, dated as of February 18, 2014, between Borrower and Venture Lending & Leasing VI, Inc., and Venture Lending & Leasing VII, Inc., (all as "Lender") as supplemented by the Supplements thereto of even date therewith (as so supplemented, the "Loan Agreements"; the terms defined therein and not otherwise defined herein being used herein as therein defined).

In connection therewith, Lender acknowledges and agrees that:

- (1) all of Borrower's outstanding debts, liabilities and obligations to Lender under the Loan Agreements and any other related loan and collateral security documents that may have been executed by Borrower (collectively, the "Loan Documents"; provided, however, "Loan Documents" shall not include any warrant instrument issued by Borrower to Lender's parent company or any other equity securities of Borrower acquired by Lender's parent company) have been satisfied in full and all Persons obligated therefor, whether by guaranty or otherwise, have been released from all liability therefor and Borrower has been released and discharged from any and all obligations, covenants and Agreements under the Loan Documents without further action from any Person;
- (2) the Commitment to make Loans under the Loan Agreements, to the extent not previously expired, shall hereby be terminated and be of no further force or effect, Lender shall hereby be released from any and all obligations, covenants and Agreements under the Loan Documents, and Lender shall not have any further obligation to make any Loans under the Loan Agreements;

- (3) all Liens of any kind, nature, or description, whenever and however arising, which Lender may have on any of the assets and property, real or personal, tangible or intangible, of Borrower; and any of its Subsidiaries granted in favor of Lender, including, without limitation, Liens created by, arising under, or granted to Lender pursuant to the Loan Documents, shall hereby automatically terminate and be satisfied and released;
- (4) all Collateral pledged under the Loan Documents (including stock certificates, related stock powers, promissory notes, instruments and chattel paper) that remains in the possession of Lender shall be returned to Borrower;
- (5) Borrower and its attorneys and designees shall hereby be authorized to file such necessary and appropriate UCC termination statements and such other releases, discharges and instruments necessary or appropriate to effect such release, and to record release of liens in patents, trademarks and copyrights for filing in the applicable filing and recording offices; and
- (6) Lender shall deliver notices to terminate any control Agreements (including, without limitation, deposit and securities account control agreements) relating to any assets and property in which Borrower has granted a security interest or lien under any of the Loan Documents.

This letter may not be amended, modified or waived except in a writing signed by Lender. In the event of any conflict, inconsistency, or incongruity between any provision of this letter and any provision of the Pay-Off Letter, the provisions of the Pay-Off Letter shall govern and control.

This letter shall be governed by, and shall be construed and enforced in accordance with, the internal laws of the State of California, without regard to conflict of laws principles. This letter may be executed in two or more counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument. Any party may execute this letter by facsimile signature or scanned signature in PDF (or like) format, and any such facsimile signature or scanned signature shall be deemed an original signature and each of the parties is hereby authorized to rely thereon.

Notwithstanding anything to the contrary set forth in this letter: (i) those obligations, liabilities, covenants and terms that are expressly specified in any Loan Document as surviving that respective Agreements' termination, including without limitation, Borrower's indemnity obligations set forth in the Loan Agreements, shall continue to survive notwithstanding anything to the contrary set forth herein; and (ii) nothing set forth herein shall affect or be deemed to affect those obligations, liabilities, covenants and terms set forth in any warrant instrument issued to Lender's parent company or set forth in any other equity securities of Borrower acquired by Lender's parent company.

Very truly yours,

VENTURE LENDING & LEASING VI, INC. VENTURE LENDING & LEASING VII, INC.

both as Lender

By:

Name: Rudy Ruano Title: Investment Partner

Acknowledged and agreed as of the date first written above:

D-WAVE SYSTEMS, INC.,

as Borrower

Name: Shelley Bubb

Title: Sr. Durch of Firena Accounting & Firmulal Reporting

PATENT

REEL: 042252 FRAME: 0264

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement (this "Agreement") is made as of January 21, 2015, by and between D-WAVE SYSTEMS INC., a corporation incorporated under the Canada Business Corporations Act, ("Grantor"), and VENTURE LENDING & LEASING VI, INC. ("VLL6") and VENTURE LENDING & LEASING VII, INC. ("VLL7"), both Maryland corporations (sometimes referred to herein individually or together as "Secured Party").

RECITALS

- A. Pursuant to (i) that certain Loan and Security Agreement dated as of February 18, 2014 between Grantor, as borrower, and VLL6, as lender, and (ii) that certain Loan and Security Agreement as of February 18, 2014 between Grantor, as borrower, and VLL7, as lender, as such agreements may from time to time be amended, restated, supplemented or otherwise modified (individually and together, the "Loan Agreement"), Secured Party has agreed to make certain advances of money and to extend certain financial accommodations to Grantor (the "Loans") in the amounts and manner set forth in the Loan Agreement. All capitalized terms used herein without definition shall have the meanings ascribed to them in the Loan Agreement.
- B. Secured Party is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Secured Party a security interest in substantially all of Grantor's personal property whether presently existing or hereafter acquired. To that end, Grantor has executed in favor of Secured Party the Loan Agreement granting a security interest in all Collateral, and is executing this Agreement with respect to certain items of Intellectual Property, in particular.

NOW, THEREFORE, THE PARTIES HERETO AGREE AS FOLLOWS:

- 1. <u>Grant of Security Interest</u>. As collateral security for the prompt and complete payment and performance of all of Grantor's present or future Obligations, Grantor hereby grants a security interest and mortgage to Secured Party, as security, in and to Grantor's entire right, title and interest in, to and under the following Intellectual Property, now solely owned or hereafter acquired by Grantor or in which Grantor now holds or hereafter acquires sole ownership of (all of which shall collectively be called the "<u>Collateral</u>" for purposes of this Agreement):
- (a) Any and all copyrights, whether registered or unregistered, held pursuant to the laws of the United States, any State thereof or of any other country; all registrations, applications and recordings in the United States Copyright Office or in any similar office or agency of the United States, and State thereof or any other country; all continuations, renewals, or extensions thereof; and any registrations to be issued under any pending applications, including without limitation those set forth on Exhibit A attached hereto (collectively, the "Copyrights");
- (b) All letters patent of, or rights corresponding thereto in, the United States or any other country, all registrations and recordings thereof, and all applications for letters patent of, or rights corresponding thereto in, the United States or any other country, including, without limitation, registrations; recordings and applications in the United States Patent and Trademark Office or in any similar office or agency of the United States, any State thereof or any other country; all reissues, continuations, continuations-in-part or extensions thereof; all petty patents, divisionals, and patents of addition; and all patents to be issued under any such applications, including without limitation the patents and patent applications set forth on Exhibit B attached hereto (collectively, the "Patents");
- (c) All trademarks, trade names, corporate names, business names, trade styles, service marks, logos, other source or business identifiers, prints and labels on which any of the foregoing have appeared or

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appear, designs and general intangibles of like nature, now existing or hereafter adopted or acquired, all registrations and recordings thereof, and any applications in connection therewith, including, without limitation, registrations, recordings and applications in the United States Patent and Trademark Office or in any similar office or agency of the United States, any State thereof or any other country or any political subdivision thereof, and reissues, extensions or renewals thereof, and the entire goodwill of the business of Grantor connected with and symbolized by such trademarks, including without limitation those set forth on Exhibit C attached hereto (collectively, the "Trademarks");

- (d) Any and all claims for damages by way of past, present and future infringement of any of the rights included above, with the right, but not the obligation, to sue for and collect such damages for said use or infringement of the intellectual property rights identified above;
- (e) All licenses or other rights to use any of the Copyrights, Patents or Trademarks, and all license fees and royalties arising from such use to the extent permitted by such license or rights;
- (f) All amendments, renewals and extensions of any of the Copyrights, Trademarks or Patents; and
- (g) All proceeds and products of the foregoing, including without limitation all payments under insurance or any indemnity or warranty payable in respect of any of the foregoing.

Notwithstanding the foregoing the term "Collateral" shall not include: (a) "intent-to-use" trademarks at all times prior to the first use thereof, whether by the actual use thereof in commerce, the recording of a statement of use with the United States Patent and Trademark Office or otherwise, but only to the extent the granting of a security interest in such "intent to use" trademarks would be contrary to applicable law or (b) any contract, instrument or chattel paper in which Grantor has any right, title or interest if and to the extent such contract, instrument or chattel paper includes a provision containing a restriction on assignment such that the creation of a security interest in the right, title or interest of Grantor therein would be prohibited and would, in and of itself, cause or result in a default thereunder enabling another person party to such contract, instrument or chattel paper to enforce any remedy with respect thereto; provided, however, that the foregoing exclusion shall not apply if (i) such prohibition has been waived or such other person has otherwise consented to the creation hereunder of a security interest in such contract, instrument or chattel paper, or (ii) such prohibition would be rendered ineffective pursuant to Sections 9-407(a) or 9-408(a) of the UCC, as applicable and as then in effect in any relevant jurisdiction, or any other applicable law (including the Bankruptcy Code or principles of equity); provided further that immediately upon the ineffectiveness, lapse or termination of any such provision, the term "Collateral" shall include, and Grantor shall be deemed to have granted a security interest in, all its rights, title and interests in and to such contract, instrument or chattel paper as if such provision had never been in effect; and provided further that the foregoing exclusion shall in no way be construed so as to limit, impair or otherwise affect Secured Party's unconditional continuing security interest in and to all rights, title and interests of Grantor in or to any payment obligations or other rights to receive monies due or to become due under any such contract, instrument or chattel paper and in any such monies and other proceeds of such contract, instrument or chattel paper.

- 2. <u>Covenants and Warranties</u>. Grantor represents, warrants, covenants and agrees as follows:
 - (a) Grantor is now the sole owner of the Collateral, except for Permitted Liens;
- (b) During the term of this Agreement, Grantor will not transfer or otherwise encumber any interest in the Collateral, except for Permitted Liens;

- (c) To its knowledge, each of the Patents is valid and enforceable, and no part of the Collateral has been judged invalid or unenforceable, in whole or in part, and no claim has been made that any part of the Collateral violates the rights of any third party;
- (d) Grantor shall deliver to Secured Party within thirty (30) days of the last day of each fiscal quarter, a report signed by Grantor, in form reasonably acceptable to Secured Party, listing any applications or registrations that Grantor has made or filed in respect of any patents, copyrights or trademarks and the status of any outstanding applications or registrations. Grantor shall promptly advise Secured Party of any material change in the composition of the Collateral, including but not limited to any subsequent ownership right of the Grantor in or to any Trademark, Patent or Copyright not specified in this Agreement;
- (e) Grantor shall use reasonable commercial efforts to (i) protect, defend and maintain the validity and enforceability of the Trademarks, Patents and Copyrights (ii) detect infringements of the Trademarks, Patents and Copyrights and promptly advise Secured Party in writing of material infringements detected and (iii) not allow any material Trademarks, Patents or Copyrights to be abandoned, forfeited or dedicated to the public without the written consent of Secured Party, which consent shall not be unreasonably withheld:
- (f) Grantor shall apply for registration (to the extent not already registered) with the United States Patent and Trademark Office or the United States Copyright Office, as applicable: (i) those intellectual property rights listed on Exhibits A, B and C hereto within thirty (30) days of the date of this Agreement; and (ii) those additional intellectual property rights developed or acquired by Grantor from time to time in connection with any product or service, prior to the sale or licensing of such product or the rendering of such service to any third party (including without limitation revisions or additions to the intellectual property rights listed on such Exhibits A, B and C), except with respect to such rights that Grantor determines in its sole but reasonable commercial judgment need not be registered to protect its own business interests. Grantor shall, from time to time, execute and file such other instruments, and take such further actions as Secured Party may reasonably request from time to time to perfect or continue the perfection of Secured Party's interest in the Collateral. Grantor shall give Secured Party notice of all such applications or registrations; and
- (g) Grantor shall not enter into any agreement that would materially impair or conflict with Grantor's obligations hereunder without Secured Party's prior written consent, which consent shall not be unreasonably withheld. Grantor shall not permit the inclusion in any material contract to which it becomes a party of any provisions that could or might in any way prevent the creation of a security interest in Grantor's rights and interests in any property included within the definition of the Collateral acquired under such contracts.

3. Further Assurances.

- (a) On a continuing basis, Grantor will make, execute, acknowledge and deliver, and file and record in the proper filing and recording places in the United States, all such instruments, including appropriate financing and continuation statements and collateral agreements and filings with the United States Patent and Trademark Office and the Register of Copyrights, and take all such action as may reasonably be deemed necessary or advisable, or as reasonably requested by Secured Party, to perfect Secured Party's security interest in all Copyrights, Patents and Trademarks and otherwise to carry out the intent and purposes of this Agreement, or for assuring and confirming to Secured Party the grant or perfection of a security interest in all Collateral.
- (b) Promptly following any change in the composition of the Collateral, Grantor agrees to execute any amendment or supplement to Exhibits A, B and C to include reference to any right, title or interest in any Copyrights, Patents or Trademarks acquired by Grantor or to delete any reference to any right, title or interest in any Copyrights, Patents or Trademarks in which Grantor no longer has or claims any right, title or interest, as Secured Party may request. Grantor shall reimburse Secured Party, no later than ten (10) Business Days following

Secured Party's demand therefor, for Secured Party's attorneys' fees, costs and expenses incurred in connection therewith.

- 4. <u>Events of Default</u>. The occurrence of any of the following shall constitute an Event of Default under this Agreement:
 - (a) An Event of Default under the Loan Agreement; or
- (b) Grantor breaches any warranty or agreement made by Grantor in this Agreement and, as to any breach that is capable of cure, Grantor fails to cure such breach within thirty (30) days of the sooner to occur of Grantor's receipt of notice of such breach from Secured Party or the date on which such breach first becomes known to Grantor.
- 5. <u>Amendments</u>. This Agreement may be amended only by a written instrument signed by both parties hereto, except for amendments permitted under Section 3 hereof to be made by Secured Party alone.
- 6. <u>Counterparts</u>. This Agreement may be executed in two or more counterparts, each of which shall be deemed an original but all of which together shall constitute the same instrument.
- 7. Several Nature of Secured Party's Obligations and Rights: Pari Passu Security Interests. This Agreement is and shall be interpreted for all purposes as separate and distinct agreements between Grantor and VLL6, on the one hand, and Grantor and VLL7, on the other hand, and nothing in this Agreement shall be deemed a joint venture, partnership or other association between VLL6 and VLL7. Each reference in this Agreement to "Secured Party" shall mean and refer to each of VLL6 and VLL7, singly and independent of one another. Without limiting the generality of the foregoing, the covenants and other obligations of "Secured Party" under this Agreement are several and not joint obligations of VLL6 and VLL7, and all rights and remedies of "Secured Party" under this Agreement may be exercised by VLL6 and/or VLL7 independently of one another. The security interests granted by Grantor to each of VLL6 and VLL7 hereunder and under the Loan Agreement shall be deemed to have been granted and perfected at the same time and shall be of equal priority.

[Signature Pages Follow]

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[Signature page to Intellectual Property Security Agreement]

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	GRANYOR:
Address of Granton	D-WAVE SYSTEMS INC.
3033 Beta Avenue Burnaby, BC V5G 4M9 Canada Attn:	By; <u>U-Gall</u> Name: <u>14,00000 14/414</u>
Address of Secured Party:	SECURED PARTY: VENTURE LENDING & LEASING VI, INC;
104 La Mesa Drive, Suite 102 Portola Valley, CA 94028 Aitn: Chief Pinancial Officer	By: Name:
Address of Secured Party:	VENTURE LENDING & LEASING VII, INC.
104 La Mesa Drive, Suite 102 Portola Valley, CA 94028	B y:
Atm: Chief Financial Officer	Name:

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řes:

[Signature page to Intellectual Property Security Agreement]

IN WITNESS WHEREOF, the parties hereto have executed this Agreement on the day and year first above written. GRANTOR: D-WAVE SYSTEMS INC. Address of Grantor: 3033 Beta Avenue By: Burnaby, BC V5G 4M9 Canada Name: Atte Its: SECURED PARTY: VENTURE LEMBING & LEASING VI, INC. Address of Secured Party: 104 La Mesa Drive, Suite 102 Portola Valley, CA 94028 Name: Rudy Ruano Attn: Chief Financial Officer Its: Investment Pariner Address of Secured Party: 104 La Mesa Drive, Suite 102 Portola Vailey, CA 94028 Rudy Rusno Name: Attn: Chief Financial Officer Investment Partner its:

EXHIBIT A

Copyrights

<u>Description</u>
No registered copyrights

Registration Number

Registration Date

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EXHIBIT B

Patents

Description	Registration/Serial Number	Registration/Application Date
Permanent Readout Superconducting Qubit	09/452,749	1999-Dec-01
Quantum Computing Method Using Magnetic Flux States At A Josephson Junction	09/855,817	2001-May-14
Qubit Using A Josephson Junction Between S- Wave And D-Wave Superconductors	09/479,336	2000-Jan-07
Quantum Computing Method Using Josephson Junction Between S-Wave And D-Wave Superconductors	09/855,487	2001-May-14
Shaped Josephson Junction Qubits Superconducting DOT/Anti-DOT Flux Qubit	09/637,514	2000-Aug-11
Based On Time-Reversal Symmetry Breaking Effects	09/810,818	2001-Mar-16
Method Of Forming Superconducting DOT/Antidot Flux Qubits Based On Time- Reversal Symmetry Breaking Effects	10/058,181	2002-Jan-25
Quantum Bit With A Multi-Terminal Junction And Loop With A Phase Shift	09/839,636	2001-Apr-20
Quantum Bit With A Multi-Terminal Junction And Loop With A Phase Shift	09/839,637	2001-Apr-20
High Sensitivity, Directional Dc-Squid Magnetometer	09/823,895	2001-Mar-31
High Sensitivity, Directional Dc-Squid Magnetometer	10/192,623	2002-Jul-09
Method Of Fluxon Injection Into Annular Josephson Junction	10/117,696	2002-Apr-04
Four Terminal Readout System For Reading The State Of A Phase Qubit.	09/875,776	2001-Jun-05
Four Terminal Readout System For Reading The State Of A Phase Qubit.	10/155,746	2002-May-24
Four Terminal Readout System For Reading The State Of A Phase Qubit	10/194,704	2002-Jul-12
Quantum Processing System For A Superconducting Qubit	09/872,495	2001-Jun-01
Methods For Controlling Qubits	10/791,617	2004-Mar-02
Superconducting Low Inductance Qubit	10/232,136	2002-Aug-29
Trilayer Heterostructure Josephson Junctions	10/231,385	2002-Aug-29
Finger Squid Qubit Device	10/025,848	2001-Dec-17
Finger Squid Qubit Device	10/025,818	2001-Dec-18
Finger Squid Qubit Device	10/351,631	2003-Jan-23
Finger Squid Qubit Device	10/351,632	2003-Jan-23
Characterization And Measurement Of	10/321,065	2002-Dec-16
Superconducting Structures		
Quantum Phase-Charge Coupled Device	10/121,817	2002-Apr-12
Quantum Phase-Charge Coupled Device	10/121,810	2002-Apr-12
Quantum Phase-Charge Coupled Device	10/121,800	2002-Apr-12

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Multi-Junction Phase Qubit	10/321,941	2002-Dec-17
Extra-Substrate Control System	10/746,992	2003-Dec-24
Extra Substrate Control System	10/134,665	2002-Apr-26
Sub-Flux Quantum Generator	10/445,096	2003-May-23
Resonant Controlled Qubit System	10/419,024	2003-Apr-17
Resonant Controlled Qubit System	10/801,335	2004-Mar-15
Resonant Controlled Qubit System	10/801,340	2004-Mar-15
Resonant Controlled Qubit System	10/798,737	2004-Mar-10
Resonant Controlled Qubit System	10/801,336	2004-Mar-15
Quantum Logic Using Three Energy Levels	10/719,925	2003-Nov-20
Conditional Rabi Oscillation Readout For Quantum Computing	10/845,638	2004-May-14
Superconducting Phase-Charge Qubits	10/934,049	2004-Sep-03
Methods Of Adiabatic Quantum Computing	200780003666.0	2007-Jan-22
Adiabatic Quantum Computation With		
Superconducting Qubits	3914/CHENP/2008	2007-Jan-22
	2008-7020947	2007-Jan-22
Methods Of Adiabatic Quantum Computing	2006-1020341	2007-3411-22
Adiabatic Quantum Computation With	12/845,352	2010-Jul-28
Superconducting Qubits		
Adiabatic Quantum Computation With	11/092,953	2005-Mar-28
Superconducting Qubits		
Adiabatic Quantum Computation With Superconducting Qubits	11/093,205	2005-Mar-28
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Methods Of Adiabatic Quantum Computation	11/625,702	2007-Jan-22
Comprising Of Hamiltonian Scaling	11/089,650	2005-Mar-25
Methods For Quantum Processing	11/089,653	2005-Mar-25
Bus Architectures For Quantum Processing	137009,053	2000-19101-20
Superconducting Qubits Having A Plurality Of	11/267,459	2005-Nov-04
Capacitive Couplings		
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Capacitive Couplings		
Analog Processor Comprising Quantum	2005-318843	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	2,592,084	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	2,853,583	2014-Jun-7
Devices		
Analog Processor Comprising Quantum	200580044348.X	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	05 849 198.6	2005-Dec-23
Devices Commission Operation		
Analog Processor Comprising Quantum	2007-547127	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	10-2007-7014632	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	200704685-7	2005-Dec-23
Devices		
Analog Processor Comprising Quantum	11/317,838	2005-Dec-22
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Analog Processor Comprising Quantum	11/608,214	2006-Dec-07
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Analog Processor Comprising Quantum		
Devices	13/210,169	2011-Aug-15
Analog Processor Comprising Quantum		
Devices	13/608,836	2012-Sep-10
Analog Processor Comprising Quantum		2011010
Devices	14/175,731	2014-Feb-07
Coupling Methods And Architectures For	0007 001700	2207 5 20
Information Processing	2005-321780	2005-Dec-30
Coupling Methods And Architectures For	220520045626	2006 12 20
Information Processing	200580045676.1	2005-Dec-30
Coupling Methods And Architectures For	05823448.5	2005-Dec-30
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Coupling Methods And Architectures For	2007-548651	2005-Dec-30
Information Processing	2007-348031	2000-50-50
Coupling Methods And Architectures For	10-2007-7014589	2005-Dec-30
Information Processing	10-2.00 (-101436)	2000-1000-00
Coupling Methods And Architectures For	200704709-5	2005-Dec-30
Information Processing	200,04,05	2000 200
Coupling Methods And Architectures For	11/247,857	2005-Oct-10
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Information Processing		
Qubit State Copying	2006-255515	2006-Apr-26
Qubit State Copying	2,606,286	2006-Apr-26
Qubit State Copying	200680016776.6	2006-Apr-26
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Qubit State Copying	2007-7026785	2006-Apr-26
Qubit State Copying	200717146-5	2006-Apr-26
Qubit State Copying	11/411,051	2006-Apr-25
Systems, Devices, And Methods For	12/618,554	2009-Nov-13
Controllably Coupling Qubits		
Method And System For Solving Integer	11/850,437	2007-Sep-05
Programming And Discrete Optimization Problems Using Analog Processors	11/000,407	200 (**S0g**05
Systems, Methods And Apparatus For		
Factoring Numbers	11/484,368	2006-Jui-10
Systems And Methods For Factoring Numbers	12/848,764	2010-Aug-02
Systems And Methods For Solving		-
Computational Problems	12/849,588	2010-Aug-03
Graph Embedding Techniques	11/932,248	2007-Oct-31
Graph Embedding Techniques	13/156,172	2011-Aug-06
Systems, Devices And Method For Solving		
Computational Problems	11/765,361	2007-Jun-19
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Computational Problems	13/462,494	2012-May-02
Systems, Methods And Apparatus For Quasi-	11/227-016	2002 1.3 12
Adiabatic Quantum Computation	11/777,910	2007-Jul-13
Processing Relational Database Problems Using	11/932,261	2007-Oct-31

Analog Processors		
Processing Relational Database Problems Using	201045 540	1000 Jan 00
Analog Processors	12/946,643	1900-Jan-00
Systems, Devices And Methods For	10/000 147	2008-Sep-25
Controllably Coupling Qubits	12/238,147	2006-3¢p-25
Superconducting Shield For Use With An	2 442 446	2007-Nov-30
Integrated Circuit For Quantum Computing	2,667,640	2007~1808~30
Superconducting Shielding For Use With An	21/049 917	2007-Nov-30
Integrated Circuit For Quantum Computing	11/948,817	2007-3004-30
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Integrated Circuit For Quantum Computing	12/703,534	2010-10-10
Quantum Processor	12/194,282	2008-Aug-19
Systems, Methods And Apparatus For Digital-		
To-Analog Conversion Of Superconducting	12/120,354	2008-May-14
Magnetic Flux Signals		
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Programming Of Quantum Processor Elements	2001327130	mony orang are
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Programming Of Quantum Processor Elements	2,502,010	
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Programming Of Quantum Processor Elements		
Systems, Methods And Apparatus For Local	10-2009-7011585	2009-Jun-04
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Programming Quantum Processor Elements		
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Programming Of Quantum Processor Elements		
Systems Methods And Apparatus For Local	13/228,219	2011-Sep-08
Programming Of Quantum Processor Elements		
Architecture For Local Programming Of	12/109,847	2008-Apr-25
Quantum Processor Elements Using Latching	12/109,047	2000-1101-20
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Interconnected Processor Topology	*	
Input/Output System And Devices For Use	12/016,801	2008-Jan-18
With Superconducting Devices		
Systems, Methods And Apparatus For Electrical Filters	12/016,709	2008-Jan-18
Systems, Devices, And Methods For		
Controllably Coupling Qubits	12/113,753	2008-May-01
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Systems, Methods And Apparatus For Anti- Symmetric Qubit-Coupling	12/098,347	2008-Apr-04
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Systems, Methods And Apparatus For Automatic Image Recognition	200880012343.2	2008-Apr-18
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Systems, Methods, And Apparatus For Solving Problems	13/284,418	2011-Oct-28
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Controlling The Elements Of Superconducting Processors	12/193,601	2008-Aug-18
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Systems, Methods And Apparatus For Adiabatic Quantum Computation And Quantum Annealing	12/473,970	2009-May-28
Method And Apparatus For Evolving A Quantum System Using A Mixed Initial Hamiltonian Comprising Both Diagonal And Off-Diagonal Terms	13/529,664	2012-Jun-21
Systems, Methods, And Apparatus For Multilayer Superconducting Printed Circuit Boards	12/247,475	2008-Oct-08

Systems, Devices And Methods For Analog Processing	12/266,378		2008-Nov-06
Magnetic Vacuum Systems And Devices For Use With Superconducting Based Computing Systems	12/865,341		2009-Jan-30
Systems, Methods, And Apparatus For Combined Superconducting Magnetic Shielding And Radiation Shielding	12/262,417		2008-Oct-31
Systems, Methods And Apparatus For Cryogenic Refrigeration	12/811,067	.\$ _. ,	2010-Jun-28
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Systems, Devices And Methods For Controllably Coupling Qubits	12/242,133		2008-Sep-30
Systems, Methods And Apparatus For Calibrating, Controlling And Operating A Quantum Processor	2,724,617		2009-May-19
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Systems, Devices And Methods For Analog Processing	2,719,343		2009-Mar-23
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Processing Quantum And Digital Processor Hybrid	14/273,200		2014-May-08
Systems And Methods To Solve Problems	12/945,717		2009-Jun-09
Systems, Methods And Apparatus For Active	2,736,116		2009-Sep-03

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Compensation Of Quantum Processor Elements	200980141676.X	2009-Sep-03
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Compensation Of Quantum Processor Elements	2011-020207	2009-360-00
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Compensation Of Quantum Processor Elements	(2017) (50)	2007 DOP 00
Systems, Methods And Apparatus For Active		
Compensation	13/958,339	2013-Aug-02
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Systems, Methods And Apparatus For	12/991,889	2009-Jun-02
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Input/Output System And Devices For Use	12/503,671	2009-Jul-15
With Superconducting Devices	,	
Systems And Methods For Fabrication Of	2,751,897	2010-Feb-25
Superconducting Integrated Circuits		
Systems And Methods For Fabrication Of	201080009224.9	2010-Feb-25
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Systems And Methods For Fabrication Of	10 746 830.8	2010-Feb-25
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Superconducting Integrated Circuits	12/992,049	2010-Feb-25
Systems, Methods And Apparatus For	18. 1800 D. 11. 11.	2002 0 . 08
Measuring Magnetic Fields	2,738,669	2009-Oct-08
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Measuring Magnetic Fields	2011-531176	2009-Oct-08
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Measuring Magnetic Fields	12/771,873	2009"000"00
Systems And Methods For Solving	12/992,047	2010-Jun-16
Computational Problems	(Mi 2) by OTT	2010 0001 10
Systems And Methods For Solving	14/186,895	2014-Feb-21
Computational Problems	2 / 1 d 0 g 0 2 0	
Systems And Methods For Realizing Fault	10/000 222	2212.1 22
Tolerance In Physical Quantum Computing	12/992,057	2010-Jun-23
Hardware		
Systems And Methods For Superconducting	2,786,281	2011-Jan-14
Integrated Circuits		
Systems And Methods For Superconducting Integrated Circuits	201180006131.5	2011-Jan-14
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Integrated Circuits	2012-549120	2011-Jan-14
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Integrated Circuits	12/944,518	2010-Nov-11
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Integrated Circuits	14/255,561	2014-Apr-17

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Systems And Methods For Superconducting Flux Qubit Readout	2,814,865	2011-Nov-10
Systems And Methods For Superconducting Plux Qubit Readout	11 839 080.6	2013-Jun-06
Systems And Methods For Superconducting Flux Qubit Readout	13/808,006	2011-Nov-10
Methods For Solving Computational Problems Using A Quantum Processor	13/300,169	2011-Nov-18
Quantum Processing Of Functional Neural Imaging Data	PCT/US2013/061459	2013-Sep-24
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Systems And Methods For Optimization Of Investment Portfolios	13/673,578	2012-Nov-09
Systems And Methods For Solving Computational Problems	13/678,266	2012-Nov-15
Systems And Devices For Quantum Processor Architectures	14/050,062	2013-Oct-09
Systems And Methods For Solving Combinatorial Problems	13/796,949	2013-Mar-12
Systems And Methods For Testing And Packaging A Superconducting Chip	14/109,604	2013-Dec-17
Systems And Methods For Calibrating The Elements Of A Quantum Processor	14/175,722	2014-Feb-07
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Refrigeration Systems And Methods For Cryogenic	14/086,697	2013-Nov-21
Refrigeration Systems And Methods That Formulate	PCT/US2013/071226	2013-Nov-21
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Quantum Computing System Systems And Methods For Real-Time Quantum	14/250,041	2014-Apr-10 2014-Jan-24
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Systems And Methods For Error Correction In	14/173,101	2014-Feb-05	
Quantum Computation			
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Quantum Processor Based Systems And			
Methods That Minimize A Continuous Variable	14/280,204	2014-May-16	
Objective Function			
Systems And Methods For Quantum Processing	14/316,372	00113 00	
Of Data, For Example Imaging Data		2014-Jun-26	
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EXHIBIT C

Trademarks

Description

U.S. Registration/Application Number

Registration/Application Date

"D-WAVE" (Wordmark)

77/368,747

10-Jan-08

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