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PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

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PROPERTY NUMBERS Total: 3

Property Type	Number
Patent Number:	7122775
Patent Number:	6841770
Patent Number:	7351946

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PATENT

504365101 REEL: 042356 FRAME: 0200

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DATE SIGNED:	05/11/2017	
Total Attachments: 1 source=(I) Assignment - Inv to HitLtd and ULSI and Tohbu#page1.tif		

PATENT REEL: 042356 FRAME: 0201

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., corporations organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan; 22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan and 1-1, Nishiyokotemachi, Takasaki-shi, Gunma, Japan, respectively, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire owner ship of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, fawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd.

Signed on the date(s) indicated aside signatures:

INVENTOR/S)

(発明者アルネームサイン)		(署名日)
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	Tatsuji Matsuura (Tatsuji MATSUURA)	14/February/2002
5)		<i>y</i>
6)		
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10)		

Date Signed