

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT4414905

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
INTEL CORPORATION	03/27/2017
RECEIVING PARTY DATA	
Name:	MICRON TECHNOLOGY, INC.
Street Address:	8000 S. FEDERAL WAY
City:	BOISE
State/Country:	IDAHO
Postal Code:	83707
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	13716287
CORRESPONDENCE DATA	
Fax Number:	(509)838-3424
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	509-624-4276
Email:	docketing@wellsstjohn.com
Correspondent Name:	WELLS ST. JOHN P.S.
Address Line 1:	601 W. MAIN AVENUE
Address Line 2:	SUITE 600
Address Line 4:	SPOKANE, WASHINGTON 99201
ATTORNEY DOCKET NUMBER:	MI22-5781
NAME OF SUBMITTER:	JILL COVINGTON
SIGNATURE:	/Jill Covington/
DATE SIGNED:	05/15/2017
Total Attachments: 4	
source=ExecutedAssignment#page1.tif	
source=ExecutedAssignment#page2.tif	
source=ExecutedAssignment#page3.tif	
source=ExecutedAssignment#page4.tif	

Intel-Micron Assignment

Micron Technology, Inc., a Delaware corporation, with an office at 8000 S. Federal Way, Boise, ID 83707 (hereinafter "Micron") and Intel Corporation, a Delaware corporation, with an office at 2200 Mission College Blvd., Santa Clara, CA 95052 (hereinafter "Intel") executed an Amended and Restated Technology License Agreement on April 6, 2012 (hereinafter "the TLA").

In accordance with the TLA, Micron and Intel periodically meet to select Pooled Inventions according to a Draft (capitalized terms in this sentence and the remainder of this paragraph having, for each referenced agreement, the meaning ascribed to them in that agreement). In accordance with the TLA, the Patent Rights in the Pooled Inventions allocated to Intel as a result of each Draft that occurred under the applicable agreement are to be solely and exclusively owned by Intel. Also in accordance with the TLA, if Intel elects to forego or abandon prosecution of any patent relating to any Pooled Invention owned by Intel pursuant to the Draft, Intel has agreed to offer to transfer to Micron, without charge, ownership of such Pooled Invention.

The Invention(s) described in Micron Invention Disclosure Docket Number 2012-0493, entitled "METHOD FOR BUILDING 3D NAND STRUCTURES USING METALLIC SILICIDE SOURCE AS ETCH STOP LAYER" (referred to hereinafter as the "2012-0493 Pooled Invention"), was allocated to Intel as a result of a Draft. With respect to the 2012-0493 Pooled Invention, Intel filed U.S. Patent Application Serial No. 13/716,287, entitled "THREE DIMENSIONAL MEMORY", on December 17, 2012, naming Zhenyu Lu, Hongbin Zhu, Gordon A. Haller, Roger W. Lindsay, Andrew Bicksler, Brian J. Cleereman, and Minsoo Lee as inventors (referred to hereinafter as the "Intel Application"), which is a Patent Right that has been allocated to Intel as a result of a Draft that occurred. Intel notified Micron of its plan to abandon the Intel Application on or about January 29, 2015. On or about February 27, 2015, Micron notified Intel that it would take over prosecution of the Intel Application and thereafter, through its attorneys and agents at Wells St. John P.S., has prosecuted the Intel Application with the United States Patent and Trademark Office.

For good and valuable consideration, the receipt of which is hereby acknowledged, to the extent not already assigned to Micron under the TLA, or another instrument or agreement, Intel hereby assigns to Micron all right, title and interest Intel owns or controls, in the 2012-0493 Pooled Invention and the Intel Application, and any and all divisionals, continuations, reissues,

reexaminations, extensions, foreign counterparts or equivalents of the Intel Application, wherever and whenever existing.

Micron and Intel agree that nothing in this Assignment shall in any way be interpreted to modify, replace or supersede, whether by implication, estoppel or otherwise, the TLA.

In Witness Whereof, Intel, by its duly authorized representative, has executed this Assignment.

Date: 3/27/2017 By: Jeffrey S. Draeger
Printed/Typed Name

Title: Vice President,
Intel Law & Policy Group

LEGAL OK
CF 3/27


Signature


A notary public or other officer completing this certificate verifies only the identity of the individual who signed the document to which this certificate is attached, and not the truthfulness, accuracy, or validity of that document.

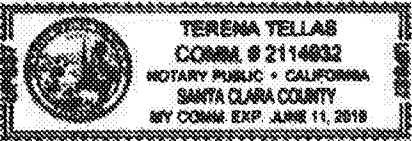
STATE OF CALIFORNIA)
) ss.
COUNTY OF SANTA CLARA)

On March 27th, 2017, before me, TERENA TELLAS, NOTARY PUBLIC, personally appeared JEFFREY S. DRAEGER, who proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity, and that by his signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

I certify under penalty of perjury under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.
(Seal)


TERENA TELLAS
Notary Public in and for the state of CALIFORNIA
My appointment expires: June 2019



ACCEPTED:

Date: May 10, 2017 By: Joel Poppen
Printed/Typed Name

Title: Vice President, Legal Affairs, General Counsel
and Corporate Secretary


Signature

REVIEWED
BITT Legal

SNB