

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4409568

SUBMISSION TYPE:	NEW ASSIGNMENT		
NATURE OF CONVEYANCE:	MERGER AND CHANGE OF NAME		
EFFECTIVE DATE:	04/30/2015		
SEQUENCE:	2		
CONVEYING PARTY DATA			
	Name	Execution Date	
	ENTROPIC COMMUNICATIONS, INC.	04/30/2015	
	EXCALIBUR SUBSIDIARY, LLC	04/30/2015	
NEWLY MERGED ENTITY DATA			
	Name	Execution Date	
	ENTROPIC COMMUNICATIONS, LLC	04/30/2015	
MERGED ENTITY'S NEW NAME (RECEIVING PARTY)			
Name:	ENTROPIC COMMUNICATIONS, LLC		
Street Address:	5966 LA PLACE COURT		
Internal Address:	SUITE 100		
City:	CARLSBAD		
State/Country:	CALIFORNIA		
Postal Code:	92008		
PROPERTY NUMBERS Total: 4			
	Property Type	Number	
	Application Number:	14829036	
	Application Number:	15202215	
	Patent Number:	9438464	
	Patent Number:	9543893	
CORRESPONDENCE DATA			
Fax Number:	(312)775-8100		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>			
Phone:	312-775-8000		
Email:	dwilson@mcandrews-ip.com		
Correspondent Name:	MCANDREWS, HELD & MALLOY, LTD.		
Address Line 1:	500 W. MADISON STREET		
Address Line 2:	34TH FLOOR		

PATENT

Address Line 4: CHICAGO, ILLINOIS 60661

NAME OF SUBMITTER: RONALD H. SPUHLER

SIGNATURE: /Ronald H. Spuhler/

DATE SIGNED: 05/11/2017

Total Attachments: 46

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Delaware

PAGE 1

The First State

I, JEFFREY W. BULLOCK, SECRETARY OF STATE OF THE STATE OF DELAWARE, DO HEREBY CERTIFY THE ATTACHED IS A TRUE AND CORRECT COPY OF THE CERTIFICATE OF MERGER, WHICH MERGES:

"ENTROPIC COMMUNICATIONS, INC.", A DELAWARE CORPORATION,
WITH AND INTO "EXCALIBUR SUBSIDIARY, LLC" UNDER THE NAME OF
"ENTROPIC COMMUNICATIONS, LLC", A LIMITED LIABILITY COMPANY
ORGANIZED AND EXISTING UNDER THE LAWS OF THE STATE OF DELAWARE,
AS RECEIVED AND FILED IN THIS OFFICE THE THIRTIETH DAY OF APRIL,
A.D. 2015, AT 5:21 O'CLOCK P.M.

A FILED COPY OF THIS CERTIFICATE HAS BEEN FORWARDED TO THE
NEW CASTLE COUNTY RECORDER OF DEEDS.

5685495 8100M

150595604

You may verify this certificate online
at corp.delaware.gov/authver.shtml




Jeffrey W. Bullock, Secretary of State
AUTHENTICATION: 2339189

DATE: 04-30-15

PATENT
REEL: 042446 FRAME: 0430

CERTIFICATE OF MERGER
MERGING
ENTROPIC COMMUNICATIONS, INC., A DELAWARE CORPORATION
WITH AND INTO
EXCALIBUR SUBSIDIARY, LLC, A DELAWARE LIMITED LIABILITY COMPANY

Pursuant to Section 264 of the General Corporation Law of the State of Delaware and
Section 18-209 of the Delaware Limited Liability Company Act

Entropic Communications, Inc., a Delaware corporation (the "**Corporation**"), and Excalibur Subsidiary, LLC, a Delaware limited liability company (the "**LLC**"), do hereby certify as follows:

FIRST: The LLC is a Delaware limited liability company duly formed and existing under the laws of the State of Delaware and the Corporation is a Delaware corporation duly organized and existing under the laws of the State of Delaware.

SECOND: The Agreement and Plan of Merger and Reorganization, dated as of February 3, 2015, by and among the Corporation, MaxLinear, Inc., a Delaware corporation, Excalibur Acquisition Corporation, a Delaware corporation and the LLC (the "**Merger Agreement**"), setting forth the terms and conditions of the merger of the Corporation with and into the LLC (the "**Merger**"), has been approved, adopted, certified, executed and acknowledged by the LLC and the Corporation in accordance with Section 264(c) (and with respect to the Corporation, by written consent of its sole stockholder in accordance with Section 228) of the General Corporation Law of the State of Delaware and Section 18-209 of the Delaware Limited Liability Company Act.

THIRD: The name of the surviving limited liability company in the Merger (the "**Surviving LLC**") is Excalibur Subsidiary, LLC, which name shall be amended as set forth in Article Fourth below.

FOURTH: The Certificate of Formation of the LLC as in effect immediately prior to the Merger shall be amended by deleting Section FIRST thereto and replacing it in its entirety with the following:

"1. The name of the limited liability company is "Entropic Communications, LLC."

FIFTH: The executed Merger Agreement is on file at the principal place of business and office of the Surviving LLC at the following address:

5966 La Place Ct., Suite 100

Carlsbad, CA 92008

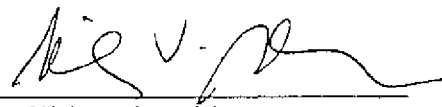
SIXTH: A copy of the Merger Agreement will be furnished by the Surviving LLC, on request and without cost, to any stockholder of the Corporation or any member of the LLC.

SEVENTH: This Certificate of Merger shall be effective upon filing.

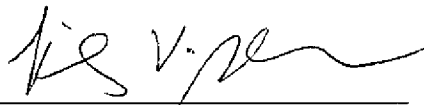
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IN WITNESS WHEREOF, Entropic Communications, Inc., a Delaware corporation, and
Excalibur Subsidiary, LLC, a Delaware limited liability company, have caused this Certificate of
Merger to be executed by an authorized person April 30, 2015.

ENTROPIC COMMUNICATIONS, INC.,
a Delaware corporation

By: 
Name: Kishore Seendripu
Title: Chief Executive Officer

EXCALIBUR SUBSIDIARY, LLC
a Delaware limited liability company

By: 
Name: Kishore Seendripu
Title: Chief Executive Officer

[Signature Page to Certificate of Merger]

SCHEDULE A

Application Number	Filing Date	Patent Number	Title
08/148144	4-Nov-93	5469227	VIDEO PROCESSING SYSTEM
08/342981	21-Nov-94	5537071	NONLINEAR CIRCUIT
08/382677	2-Feb-95	5554950	DELAY LINE PROVIDING AN ADJUSTABLE DELAY IN RESPONSE TO BINARY INPUT SIGNALS
08/383392	3-Feb-95	5612863	SORTING SEQUENTIAL DATA PRIOR TO DISTRIBUTION OVER PARALLEL PROCESSORS IN RANDOM ACCESS MANNER
08/413061	29-Mar-95	5648819	METHOD AND CIRCUIT FOR PRESUMING MOVEMENT BETWEEN IMAGES COMPOSED OF INTERLACED FIELD
08/608146	28-Feb-96	5654629	CURRENT MIRROR IN MOS TECHNOLOGY COMPRISING CASCADE STAGES WITH WIDE DRIVE RANGES
08/575801	22-Dec-95	5719593	SINGLE FRAME BUFFER IMAGE PROCESSING SYSTEM
08/631296	9-Apr-96	5742196	LEVEL-SHIFTING CIRCUIT AND HIGH-SIDE DRIVER INCLUDING SUCH A LEVEL-SHIFTING CIRCUIT
08/599842	12-Feb-96	5754252	MULTI-STANDARD TELEVISION RECEIVER
08/792924	21-Jan-97	5767739	DIGITAL DEMODULATOR FOR QUADRATURE AMPLITUDE AND PHASE MODULATED SIGNALS
08/861605	22-May-97	5805029	DIGITALLY CONTROLLED QUARTZ OSCILLATOR AND MONOLITHIC INTEGRATED OSCILLATOR CIRCUIT
08/754144	22-Nov-96	5822437	SIGNAL MODIFICATION CIRCUIT
08/519142	24-Aug-95	5874928	METHOD AND APPARATUS FOR DRIVING A PLURALITY OF DISPLAYS SIMULTANEOUSLY
08/729672	3-Oct-96	5887036	LOGIC BLOCK FOR A VITERBI DECODER

Application Number	Filing Date	Patent Number	Title
08/725113	2-Oct-96	5907371	CONNECTOR SWITCH APPARATUS WHICH CAN PROVIDE EITHER A SATELLITE OR CABLE TELEVISION SIGNAL OR A GHOST-FREE OFF-AIR TEL
08/798345	10-Feb-97	5933198	COMB FILTER ARRANGEMENT
08/988548	10-Dec-97	5936660	DIGITAL VIDEO CONVERTER BOX FOR SUBSCRIBER/HOME WITH MULTIPLE TELEVISION SETS
08/683995	19-Jul-96	5959689	MULTI-MEDIA PROCESSOR ARCHITECTURE WITH HIGH PERFORMANCE-DENSITY
08/805767	25-Feb-97	5978037	CIRCUIT FOR DECODING AUXILIARY DATA IN A BROADCAST SIGNAL
08/990279	15-Dec-97	5986691	CABLE MODEM OPTIMIZED FOR HIGH-SPEED DATA TRANSMISSION FROM THE HOME TO THE CABLE HEAD
09/030485	25-Feb-98	6008693	FM-SOUND DEMODULATOR FOR TV SOUND SIGNALS AND METHOD OF DETECTING SOUND CARRIERS
08/561572	21-Nov-95	6008849	METHOD AND SYSTEM FOR DECODING CODED VIDEO SIGNALS
09/021260	10-Feb-98	6020882	TELEVISION ACCESS CONTROL SYSTEM
08/802042	18-Feb-97	6043844	PERCEPTUALLY MOTIVATED TRELLIS BASED RATE CONTROL METHOD AND APPARATUS FOR LOW BIT RATE VIDEO CODING
08/838488	7-Apr-97	6044114	METHOD OF IMAGE SEGMENTATION
08/854922	13-May-97	6122381	STEREOPHONIC SOUND SYSTEM
08/841913	17-Apr-97	6133949	MEASURING CIRCUIT
08/814555	11-Mar-97	6141386	ASYMMETRIC FILTER COMBINATION FOR A DIGITAL TRANSMISSION SYSTEM
09/061465	16-Apr-98	6141646	DIGITAL SOUND PROCESSOR FOR PROCESSING MULTIPLE STANDARD SOUND SIGNALS AND CAPABLE OF GENERATING ADDITIONAL AUDIO SIGN

Application Number	Filing Date	Patent Number	Title
09/251502	17-Feb-99	6151580	AUDIO PLAYBACK APPARATUS
09/272508	19-Mar-99	6166526	DC-DC CONVERTER
08/883732	27-Jun-97	6208173	PEAK DETECTOR
08/963931	4-Nov-97	6208350	METHOD AND APPARATUS FOR PROCESSING DVD VIDEO
09/517727	2-Mar-00	6211629	CUT-OFF LEVEL SETTING
09/326508	4-Jun-99	6218791	DEFLECTION CORRECTION
09/323564	1-Jun-99	6262774	VIDEO SEQUENCE CONVERSION METHOD
09/349594	9-Jul-99	6281813	METHOD AND CIRCUIT FOR DECODING AN ANALOG AUDIO SIGNAL USING THE BTSC STANDARD
09/497003	2-Feb-00	6348819	DRIVING CIRCUIT FOR A SEMICONDUCTOR SWITCHING ELEMENT
09/259715	1-Mar-99	6396545	METHOD FOR DIGITAL SYNCHRONIZATION OF VIDEO SIGNALS
09/389827	3-Sep-99	6407727	DISPLAY DEVICE
09/490587	25-Jan-00	6411338	PRODUCING A SERIES OF IMAGES FOR DISPLAY ON A DISPLAY DEVICE
09/299525	26-Apr-99	6414724	TELETEXT WITH TRANSPARENT FUNCTION
09/384809	27-Aug-99	6421090	MOTION AND EDGE ADAPTIVE DEINTERLACING
09/205492	4-Dec-98	6434146	USE OF SEQUENCING INFORMATION IN A LOCAL HEADER THAT ALLOWS PROPER SYNCHRONIZATION OF PACKETS TO SUBSIDIARY INTERFACES
09/205482	4-Dec-98	6434170	MEMORY-BASED CIRCULAR QUEUE WITH LOCAL DESCRIPTIVE INFORMATION TO IMPLEMENT A STORAGE AREA FOR FILTERING MPEG-2 PACKET
08/397021	1-Mar-95	6456679	CHARACTER GENERATOR COMPRISING A STARTSTOP OSCILLATOR

Application Number	Filing Date	Patent Number	Title
09/554634	18-Nov-98	6489958	METHOD AND DEVICE FOR GRAPHIC REPRESENTATION OF AN OBJECT DEFINED BY A PLURALITY OF TRIANGLES ON A DISPLAY SURFACE
09/932473	17-Aug-01	6492913	METHOD AND CIRCUIT FOR DECODING AN ANALOG AUDIO SIGNAL USING THE BTSC STANDARD
09/563301	2-May-00	6498609	CHROMINANCE SIGNAL INTERPOLATION
09/272760	19-Mar-99	6501809	PRODUCING SMOOTHED CLOCK AND DATA SIGNALS FROM GAPPED CLOCK AND DATA SIGNALS
09/370271	9-Aug-99	6522115	PULSE-WIDTH-MODULATED DC-DC CONVERTER WITH A RAMP GENERATOR
09/677823	30-Sep-00	6538658	METHOD AND APPARATUS FOR PROCESSING DVD VIDEO
09/443054	18-Nov-99	6552738	USER INTERFACE FOR CONTROL OF A DISPLAY DEVICE
09/492962	28-Jan-00	6606724	ITERATIVE DECODER EMPLOYING MULTIPLE EXTERNAL CODE ERROR CHECKS TO LOWER THE ERROR FLOOR
09/709997	10-Nov-00	6646688	HIGH QUALITY VIDEO AND GRAPHICS PIPELINE
09/722805	27-Nov-00	6665018	DATA BIT RETRIEVAL DEVICE FOR DATA SIGNAL
09/516465	1-Mar-00	6665752	CABLE MODEM HAVING A PROGRAMMABLE MEDIA ACCESS CONTROLLER
09/829790	10-Apr-01	6671318	DETERMINING THE DEGREE OF RESEMBLANCE BETWEEN A DATA SAMPLE AND INTERPOLATIONS OF OTHER DATA SAMPLES
09/555940	3-Nov-98	6675283	HIERARCHICAL CONNECTION OF PLURALITY OF FUNCTIONAL UNITS WITH FASTER NEIGHBOR FIRST LEVEL AND SLOWER DISTANT SECOND LE

Application Number	Filing Date	Patent Number	Title
09/663668	18-Sep-00	6707854	METHOD OF GLOBAL MOTION ESTIMATION
09/895965	29-Jun-01	6714257	COLOR KEY PRESERVATION DURING SAMPLE RATE CONVERSION
09/588266	5-Jun-00	6724817	ADAPTIVE IMAGE DATA COMPRESSION
09/272632	18-Mar-99	6724829	AUTOMATIC POWER CONTROL IN A DATA TRANSMISSION SYSTEM
09/450643	22-Nov-99	6727959	SYSTEM OF AND METHOD FOR GAMMA CORRECTION OF REAL-TIME VIDEO
09/640734	17-Aug-00	6738840	MULTIPROCESSOR DEVICE HAVING AN INTERFACE FOR A COLLECTIVE MEMORY
09/568287	10-May-00	6754281	PROGRAMMABLE OFDM DIGITAL DEMODULATOR
10/297730	8-Apr-02	6762643	HIGH DUTY CYCLE OFFSET COMPENSATION FOR OPERATIONAL AMPLIFIERS
09/788171	14-Feb-01	6769093	SYNCHRONIZATION OF A COMMUNICATIONS SYSTEM
09/035431	5-Mar-98	6774883	ELECTRO-OPTICAL DISPLAY DEVICE WITH TEMPERATURE DETECTION AND VOLTAGE CORRECTION
09/605967	28-Jun-00	6774918	VIDEO OVERLAY PROCESSOR WITH REDUCED MEMORY AND BUS PERFORMANCE REQUIREMENTS
09/736891	13-Dec-00	6785700	IMPLEMENTATION OF WAVELET FUNCTIONS IN HARDWARE
09/716167	17-Nov-00	6792115	DEVICE FOR CREATING HARMONICS IN AN AUDIO SIGNAL
09/715877	17-Nov-00	6795512	SYMBOL RELIABILITY DETERMINATION AND SYMBOL PRE-SELECTION BASED ON RELIABILITY CRITERIA
09/665629	19-Sep-00	6801591	CLOCK RECOVERY
10/204066	18-Dec-01	6803897	DISPLAY DEVICE WITH FREELY PROGRAMMABLE MULTIPLEX RATE

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09/865958	25-May-01	6810502	ITERATIVE DECODER EMPLOYING MULTIPLE EXTERNAL CODE ERROR CHECKS TO LOWER THE ERROR FLOOR
09/785035	16-Feb-01	6816940	CABLE MODEM HAVING A PROGRAMMABLE MEDIA ACCESS CONTROLLER
10/246169	18-Sep-02	6842124	IMPROVED VARIABLE LENGTH DECODER
09/973309	9-Oct-01	6847406	HIGH QUALITY, COST-EFFECTIVE FILM-TO-VIDEO CONVERTER FOR HIGH DEFINITION TELEVISION
09/822454	30-Mar-01	6856649	INITIALIZATION SCHEME FOR A HYBRID FREQUENCY-TIME DOMAIN EQUALIZER
09/729652	4-Dec-00	6856656	ITERATIVE CARRIER PHASE TRACKING DECODING SYSTEM
09/649931	29-Aug-00	6865635	MULTIPROCESSOR DEVICE HAVING AN INTERFACE FOR A COLLECTIVE MEMORY
10/013492	13-Dec-01	6865711	SYSTEM OF AND METHOD FOR DECODING TRELLIS CODES
09/998055	29-Nov-01	6882332	DISPLAY DEVICE WITH ADAPTIVE SELECTION OF THE NUMBER OF SIMULTANEOUSLY DISPLAYED ROWS
09/668231	22-Sep-00	6882690	SOFT TRELLIS SLICER FOR IMPROVING THE PERFORMANCE OF A DECISION-DIRECTED PHASE TRACKER
09/806603	15-Sep-99	6909466	CIRCUIT FOR TRANSFORMING AN IMAGE RATE
10/144432	13-May-02	6917387	ARRANGEMENT FOR TIME-CORRECT COMBINATION OF TWO DATA STREAMS
09/815149	22-Mar-01	6922438	SYSTEM FOR MPSK SYMBOL HYPOTHESIS TESTING
09/919579	30-Jul-01	6938063	PROGRAMMABLE FILTER ARCHITECTURE

Application Number	Filing Date	Patent Number	Title
09/713412	15-Nov-00	6940523	ON THE FLY DATA TRANSFER BETWEEN RGB AND YCRCB COLOR SPACES FOR DCT INTERFACE
10/071711	8-Feb-02	6940557	ADAPTIVE INTERLACE-TO-PROGRESSIVE SCAN CONVERSION ALGORITHM
10/401105	27-Mar-03	6954093	CLOCKING SCHEME AND CLOCK SYSTEM FOR A MONOLITHIC INTEGRATED CIRCUIT
10/013490	13-Dec-01	6973615	SYSTEM OF AND METHOD FOR DECODING TRELLIS CODES
10/132746	25-Apr-02	6975359	METHOD AND SYSTEM FOR MOTION AND EDGE-ADAPTIVE SIGNAL FRAME RATE UP-CONVERSION
10/404516	1-Apr-03	6980059	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT
09/812437	20-Mar-01	6993071	LOW-COST HIGH-SPEED MULTIPLIER/ACCUMULATOR UNIT FOR DECISION FEEDBACK EQUALIZERS
10/404511	1-Apr-03	6995617	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT
09/455662	7-Dec-99	6996175	MOTION VECTOR ESTIMATION
09/624522	24-Jul-00	6996177	MOTION ESTIMATION
10/044877	26-Oct-01	6999514	MOTION COMPENSATION WITH SUBBLOCK SCANNING
10/090916	5-Mar-02	7006150	INTERFACE MODULE FOR TV SETS
10/495956	24-Oct-02	7009451	IMAGE DISPLAY APPARATUS AND HIGH VOLTAGE DRIVER CIRCUIT
09/855628	15-May-01	7010039	MOTION ESTIMATOR FOR REDUCED HALOS IN MOTION COMPENSATED PICTURE RATE UP-CONVERSION

Application Number	Filing Date	Patent Number	Title
10/234784	4-Sep-02	7010042	IMAGE PROCESSOR AND IMAGE DISPLAY APPARATUS PROVIDED WITH SUCH IMAGE PROCESSOR
09/516284	1-Mar-00	7010802	PROGRAMMABLE PATTERN MATCH ENGINE
09/981143	16-Oct-01	7039113	SELECTIVE DECODING OF ENHANCED VIDEO STREAM
10/117827	8-Apr-02	7050114	PICTURE SIGNAL CONTRAST CONTROL
10/784019	20-Feb-04	7064501	SAWTOOTH LINE CIRCUIT FOR A CATHODE RAY TUBE
09/859684	17-May-01	7064793	METHOD AND APPARATUS FOR MEASURING THE NOISE CONTAINED IN A PICTURE
10/239445	20-Mar-01	7065028	METHOD AND DEVICE FOR GENERATING A CLOCK SIGNAL THAT IS COUPLED TO A REFERENCE SIGNAL
10/893673	15-Jul-04	7065703	SYNCHRONIZATION OF A COMMUNICATIONS SYSTEM
10/007999	13-Nov-01	7072392	EQUALIZER FOR TIME DOMAIN SIGNAL PROCESSING
10/207311	29-Jul-02	7084929	VIDEO DATA FILTERING ARRANGEMENT AND METHOD
10/636307	7-Aug-03	7092033	A GRADIENT BASED METHOD FOR THE INTERPOLATION OF PICTURE ELEMENTS
10/108232	26-Mar-02	7098964	APPARATUS AND METHOD FOR SLOPE-ENHANCEMENT OF SIGNAL TRANSITIONS IN TELEVISION PICTURES
10/350303	22-Jan-03	7103831	BURST RELIABILITY AND ERROR LOCATOR FOR TRELLIS CODES
10/105731	22-Mar-02	7106810	METHOD AND APPARATUS FOR A DEMODULATOR CIRCUIT
10/525861	21-Aug-03	7113021	VOLTAGE DOUBLER CIRCUIT
10/003062	2-Nov-01	7116712	APPARATUS AND METHOD FOR PARALLEL MULTIMEDIA PROCESSING
10/701047	4-Nov-03	7130344	ADAPTIVE EQUALIZER HAVING A VARIABLE STEP SIZE INFLUENCED BY OUTPUT FROM A TRELLIS DECODER

Application Number	Filing Date	Patent Number	Title
90/008389	6-Feb-07	7130576	SIGNAL SELECTOR AND COMBINER FOR BROADBAND CONTENT DISTRIBUTION
10/289011	6-Nov-02	7130576	SIGNAL SELECTOR AND COMBINER FOR BROADBAND CONTENT DISTRIBUTION
10/115267	2-Apr-02	7133478	NOISE DETECTING AND SUPPRESSION DEVICE AND METHOD THEREFOR
10/622073	17-Jul-03	7142249	APPARATUS AND METHOD FOR INTERPOLATING A PIXEL FROM AN INTERMEDIATE LINE OF A FIELD
10/832475	26-Apr-04	7145604	METHOD AND DEVICE FOR DETERMINING THE SPACING BETWEEN A FIRST AND A SECOND SIGNAL SEQUENCE
10/621884	17-Jul-03	7151575	WIRELESS EXTENSION TO COAXIAL TELEVISION SIGNAL
10/386094	10-Mar-03	7154957	BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
09/357006	19-Jul-99	7162038	CIRCUIT FOR SELECTING AUDIO SOURCES
09/876547	7-Jun-01	7190744	ERROR GENERATION FOR ADAPTIVE EQUALIZER
10/170165	12-Jun-02	7206361	METHOD AND APPARATUS FOR A MULTICARRIER RECEIVER CIRCUIT WITH GUARD INTERVAL SIZE DETECTION
11/080776	14-Mar-05	7221372	VERFAHREN ZUR ANALYSE UND MODIFIKATION EINES FOOTPRINTS
10/180402	26-Jun-02	7221726	ARRANGEMENT FOR GENERATING A DECODER CLOCK SIGNAL
10/247977	20-Sep-02	7222336	A METHOD OF COMPILING BYTECODE TO NATIVE CODE
11/219980	6-Sep-05	7224235	HARMONIC SUPPRESSION MIXER AND TUNER
10/368017	14-Feb-03	7231005	SYMBOL RELIABILITY DETERMINATION AND SYMBOL PRE-SELECTION BASED ON RELIABILITY CRITERIA

Application Number	Filing Date	Patent Number	Title
11/202636	11-Aug-05	7233271	NOISE SHAPER CIRCUIT AND METHOD FOR REDUCING SWITCHING NOISE
10/071771	6-Feb-02	7236757	HIGH-SPEED COMMUNICATIONS TRANSCEIVER
11/287630	28-Nov-05	7245242	DECODING SYSTEMS AND METHODS
10/773750	6-Feb-04	7248890	CHANNEL POWER BALANCING IN A MULTI-CHANNEL TRANSCEIVER SYSTEM
10/510587	31-Mar-03	7249244	DATA PROCESSING SYSTEM WITH MULTIPLE REGISTER BANKS
11/067075	25-Feb-05	7251703	METHOD OF BUS CONFIGURATION TO ENABLE DEVICE BRIDGING OVER DISSIMILAR BUSES
11/098743	4-Apr-05	7257499	METHOD AND DEVICE FOR DETERMINING A FREQUENCY FOR SAMPLING AN ANALOG SIGNAL
11/300047	14-Dec-05	7257505	SYSTEM FOR CALIBRATING INTEGRATED CIRCUIT FILTERS
10/495947	28-Oct-02	7262807	SIGNAL PROCESSING DEVICE FOR PROVIDING MULTIPLE OUTPUT IMAGES IN ONE PASS
11/164768	5-Dec-05	7271640	INTEGRATED CROSSPOINT SWITCH WITH BAND TRANSLATION
10/404553	1-Apr-03	7272203	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT
10/871881	18-Jun-04	7274402	METHOD AND APPARATUS FOR MOTION-VECTOR-AIDED PIXEL INTERPOLATION
10/222219	16-Aug-02	7277964	DATA ROUTING AND PROCESSING DEVICE
10/322834	18-Dec-02	7295518	BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
09/904432	11-Jul-01	7295623	HIGH-SPEED COMMUNICATIONS TRANSCEIVER

Application Number	Filing Date	Patent Number	Title
11/194085	29-Jul-05	7301379	SYSTEMS AND METHOD FOR A DELAY LOCKED LOOP WITH FALSE-LOCK DETECTION
10/104911	22-Mar-02	7301570	IMAGE CORRECTION BY COMBINING RESULTS OF MULTIPLE LINEAR FILTERS, WHERE EXTREME LINEAR FILTER VALUES ARE ELIMINATED
09/870926	30-May-01	7310369	SNR-RELATED PARAMETER ESTIMATION METHOD AND SYSTEM
10/892738	16-Jul-04	7310768	ITERATIVE DECODER EMPLOYING MULTIPLE EXTERNAL CODE ERROR CHECKS TO LOWER THE ERROR FLOOR
10/250704	8-Jan-02	7315331	METHOD AND DEVICE FOR CONVERTING VIDEO SIGNALS
10/183798	27-Jun-02	7315622	ROBUST METHOD FOR ACHIEVING AUDIO/VIDEO SYNCHRONIZATION IN MPEG DECODERS IN PERSONAL VIDEO RECORDING APPLICATIONS
10/892531	14-Jul-04	7317758	METHOD AND CIRCUIT FOR EFFECTIVELY CONVERTING PCM DATA TO PWM DATA
10/407404	4-Apr-03	7321642	TRANPOSED STRUCTURE FOR A DECISION FEEDBACK EQUALIZER COMBINED WITH A TRELLIS DECODER
10/779317	13-Feb-04	7330141	METHOD AND CIRCUIT FOR COMPENSATING NON-LINEARITIES OF AN ADC
10/276847	17-May-01	7339412	DIGITAL CLOCK GENERATOR
10/044876	26-Oct-01	7352814	VIDEO ARTIFACT IDENTIFICATION AND COUNTING
11/461534	1-Aug-06	7355483	MULTIPLE FREQUENCY SOURCE SYSTEM AND METHOD OF OPERATION
10/548742	27-Feb-04	7355917	TWO-DIMENSIONAL DATA MEMORY
10/498293	5-Dec-02	7356670	DATA PROCESSING SYSTEM

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10/682652	9-Oct-03	7369834	IMPEDANCE-MATCHED IQ NETWORK FOR AN IMAGE REJECTION CIRCUIT
10/407610	4-Apr-03	7376181	TRANPOSED STRUCTURE FOR A DECISION FEEDBACK EQUALIZER COMBINED WITH A TRELLIS DECODER
10/245984	18-Sep-02	7376286	BLOCK-BASED ROTATION OF ARBITRARY-SHAPED IMAGES
11/098587	4-Apr-05	7391476	METHOD AND DEVICE FOR INTERPOLATION OF INTERMEDIATE ROW IMAGE POINT OF HALF-IMAGE
11/439658	24-May-06	7397311	METHOD OF COUPLING OSCILLATORS TO REDUCE SPURIOUS SIGNALS IN RECEIVER CIRCUITS
11/398431	5-Apr-06	7397873	ADAPTIVE SIGNAL WEIGHTING SYSTEM
10/310255	4-Dec-02	7403752	HIGH-SPEED COMMUNICATIONS TRANSCEIVER
08/984735	4-Dec-97	7406492	DIGITAL FILTER COMBINATION FOR INTERPOLATION
11/626153	23-Jan-07	7408491	METHOD FOR DETERMINING AND METHOD FOR COMPENSATING A CHARACTERISTIC CURVE OF AN A/D CONVERTER, CIRCUIT CONFIGURATION F
10/284121	29-Oct-02	7415075	MULTI-RATE ENCODING AND DECODING SYSTEM
10/152159	20-May-02	7418379	DEVICE FOR IMPROVING THE INTELLIGIBILITY OF AUDIO SIGNALS CONTAINING SPEECH
10/789059	27-Feb-04	7428222	COMMUNICATIONS BRIDGE BETWEEN INCOMPATIBLE NETWORKS
10/215609	9-Aug-02	7428238	BROADBAND NETWORK USING VARIOUS WIRING CHANNELS
10/534161	27-Oct-03	7443375	DISPLAY DEVICE WITH PIXEL INVERSION
11/027952	31-Dec-04	7477871	SIGNAL SELECTOR AND COMBINER FOR BROADBAND CONTENT DISTRIBUTION

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09/442868	18-Nov-99	7486283	METHOD AND APPARATUS FOR COMMUNICATING DIGITAL DATA FROM A COMPUTER SYSTEM TO A DISPLAY DEVICE
11/137129	25-May-05	7496165	METHOD AND SYSTEM FOR MOTION-COMPENSATED ESTIMATION OF THE NOISE IN MOBILE WIRELESS TRANSMISSION SYSTEMS
10/496555	25-Nov-02	7499105	COMPENSATION CIRCUIT FOR FREQUENCY FILTERS
11/938283	11-Nov-07	7499397	BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
11/318265	23-Dec-05	7504890	DATA-DIRECTED FREQUENCY-AND-PHASE LOCK LOOP FOR DECODING AN OFFSET-QAM MODULATED SIGNAL HAVING A PILOT
11/760303	8-Jun-07	7504971	DECODING SYSTEMS AND METHODS
10/310729	5-Dec-02	7515145	ARRANGEMENT FOR DRIVING A DISPLAY DEVICE
10/542893	16-Dec-03	7519230	BACKGROUND MOTION VECTOR DETECTION
11/298147	9-Dec-05	7519339	TUNER DESIGN SYSTEM FOR LOSSLESS INTERCONNECT OF MULTIPLE TUNERS
11/078050	11-Mar-05	7519348	HARMONIC SUPPRESSION MIXER AND TUNER
11/027999	31-Dec-04	7522875	SIGNAL SELECTOR AND COMBINER FOR BROADBAND CONTENT DISTRIBUTION
10/552800	13-Apr-04	7525553	COMPUTER GRAPHICS PROCESSOR AND METHOD FOR GENERATING A COMPUTER GRAPHICS IMAGE
11/537628	30-Sep-06	7526264	INTEGRATED CROSSPOINT SWITCH WITH BAND TRANSLATION
11/461530	1-Aug-06	7528665	MULTIPLE FREQUENCY SOURCE SYSTEM AND METHOD OF OPERATION

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11/297123	7-Dec-05	7532216	METHOD OF SCALING A GRAPHIC CHARACTER
10/564921	21-Jul-04	7532220	SYSTEM FOR ADAPTIVE RESAMPLING IN TEXTURE MAPPING
10/569681	23-Aug-04	7536031	TEMPORAL INTERPOLATION OF A PIXEL ON BASIS OF OCCLUSION DETECTION
10/183779	27-Jun-02	7539391	TRICK PLAY FOR AUDIO/VIDEO/DATA STREAMS WITH CONDITIONAL ACCESS
10/552105	2-Apr-04	7542041	RUNTIME CONFIGURABLE VIRTUAL VIDEO PIPELINE
11/229297	16-Sep-05	7542411	ECHO PROFILE PROBE
11/553456	26-Oct-06	7542715	SIGNAL SELECTOR AND COMBINER FOR BROADBAND CONTENT DISTRIBUTION
11/547818	8-Apr-05	7545888	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
11/758562	5-Jun-07	7548136	DISTORTION REDUCTION FOR VARIABLE CAPACITANCE DEVICES
10/677110	1-Oct-03	7549156	FAST CHANNEL SCANNING AND ACQUISITION SYSTEM AND METHOD FOR CABLE MODEM APPLICATIONS
10/540103	8-Dec-03	7551185	APPARATUS FOR RE-ORDERING VIDEO DATA FOR DISPLAYS USING TWO TRANSPOSE STEPS AND STORAGE OF INTERMEDIATE PARTIALLY RE-O
10/474120	8-Apr-02	7552159	TRANSFORM CALCULATION DEVICE
10/507808	11-Mar-03	7552343	CONDITIONAL ACCESS CONTROL
10/536647	26-Nov-03	7552352	SYNCHRONIZATION OF SIGNALS
11/538627	4-Oct-06	7558551	INTEGRATED CROSSPOINT SWITCH WITH BAND TRANSLATION
10/561462	8-Jun-04	7561208	METHOD AND DECODER FOR COMPOSING A SCENE

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11/944320	21-Nov-07	7568147	ITERATIVE DECODER EMPLOYING MULTIPLE EXTERNAL CODE ERROR CHECKS TO LOWER THE ERROR FLOOR
11/071537	3-Mar-05	7570245	CONTROL UNIT AND METHOD FOR REDUCING INTERFERENCE PATTERNS IN THE DISPLAY OF AN IMAGE ON A SCREEN
11/300046	14-Dec-05	7570710	IN-PHASE AND QUADRATURE-PHASE SIGNAL AMPLITUDE AND PHASE CALIBRATION
10/506836	12-Feb-03	7571450	A SYSTEM FOR AND METHOD OF DISPLAYING INFORMATION
11/938770	12-Nov-07	7573822	BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
10/552778	8-Apr-04	7594046	DATA PROCESSING IN WHICH CONCURRENTLY EXECUTED PROCESSES COMMUNICATE VIA A FIFO BUFFER
09/910412	21-Jul-01	7594249	NETWORK INTERFACE AND BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
11/578132	8-Apr-05	7599433	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
10/775532	10-Feb-04	7620254	APPARATUS AND METHOD FOR MOTION-VECTOR-AIDED INTERPOLATION OF A PIXEL OF AN INTERMEDIATE IMAGE OF AN IMAGE SEQUENCE
11/071879	3-Mar-05	7643039	METHOD AND APPARATUS FOR CONVERTING A COLOR IMAGE
11/547817	8-Apr-05	7653126	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
11/461533	1-Aug-06	7653370	MULTIPLE FREQUENCY SOURCE SYSTEM AND METHOD OF OPERATION

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10/539196	17-Dec-03	7673140	DEDICATED ENCRYPTED VIRTUAL CHANNEL IN A MULTI-CHANNEL SERIAL COMMUNICATIONS INTERFACE
11/960670	19-Dec-07	7676199	METHOD AND APPARATUS FOR THE SMOOTH TRANSITION OF RETURN LOSS
11/136295	24-May-05	7680219	METHOD AND CIRCUIT FOR DECODING A BIT SEQUENCE FROM QPSK OR QAM SYMBOLS
10/867335	14-Jun-04	7685497	METHOD AND APPARATUS FOR EFFICIENT COMPUTATION OF CHECK EQUATIONS IN PERIODICAL LOW DENSITY PARITY CHECK (LDPC) CODES
11/254546	20-Oct-05	7693327	IMAGE RENDITION USING SEQUENTIAL COLOR RENDITION
10/533019	22-Oct-03	7701450	LINE SCANNING IN A DISPLAY
11/013963	16-Dec-04	7711044	NOISE REDUCTION SYSTEMS AND METHODS
11/127805	12-May-05	7711073	METHOD AND CIRCUIT ARRANGEMENT FOR DETERMINING THE FREQUENCY OF A RECEIVED SIGNAL FOR DEMODULATION OF RECEIVED SYMBOLS
12/163962	27-Jun-08	7714760	QUADRATURE SAMPLING ADC
11/194107	28-Jul-05	7720177	SYSTEM AND METHOD FOR DETECTING KNOWN SEQUENCE IN TRANSMITTED SEQUENCE
10/561658	14-Jun-04	7720296	METHOD FOR DETERMINING A DISPLACEMENT VECTOR IN AN IMAGE PROCESSING OPERATION
10/734535	11-Dec-03	7724639	METHOD OF BIT ALLOCATION IN A MULTICARRIER SYMBOL TO ACHIEVE NON-PERIODIC FREQUENCY DIVERSITY
10/522084	8-Jul-03	7725515	CIRCUIT DE FILTRAGE DE DONNÉES NUMÉRIQUES
11/904328	26-Sep-07	7728743	DEVICE AND METHOD FOR POLYPHASE RESAMPLING
11/210296	23-Aug-05	7734089	METHOD FOR REDUCING MOSQUITO NOISE

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11/595546	10-Nov-06	7734984	ERASURES ASSISTED BLOCK CODE DECODER AND RELATED METHOD
11/229196	16-Sep-05	7738355	PACKET DATA TRANSMISSION WITH OPTIMUM PREAMBLE LENGTH
10/567224	14-Jul-04	7746929	VIDEO ENCODING AND DECODING METHODS AND CORRESPONDING DEVICES
11/391903	28-Mar-06	7746969	HIGH DEFINITION MULTI-MEDIA INTERFACE
10/045929	26-Oct-01	7750979	PIXEL-DATA LINE BUFFER APPROACH HAVING VARIABLE SAMPLING PATTERNS
10/193659	10-Jul-02	7761717	MEMORY DEVICE WITH DATA PROTECTION IN A PROCESSOR
11/303449	16-Dec-05	7770090	MULTI-DEMODULATOR / FEC EFFICIENT ARCHITECTURE
11/489091	19-Jul-06	7778482	METHOD AND SYSTEM FOR REDUCING MOSQUITO NOISE IN A DIGITAL IMAGE
11/555685	1-Nov-06	7783958	BROADBAND SATELLITE SYSTEM FOR THE SIMULTANEOUS RECEPTION OF MULTIPLE CHANNELS
12/341550	22-Dec-08	7800521	NONLINEAR COMPENSATION IN ANALOG TO DIGITAL CONVERTERS
12/388627	19-Feb-09	7808408	A METHOD TO ESTIMATE THE SKEWS BETWEEN TWO ANALOG-TO-DIGITAL CONVERTERS
12/167621	3-Jul-08	7821914	A METHOD OF GENERATION AND SET OF IMPLEMENTATION EFFICIENT PREAMBLES FOR OFDM SYSTEMS
11/547733	8-Apr-05	7822112	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
11/507369	21-Aug-06	7822602	ADAPTIVE REDUCTION OF NOISE SIGNALS AND BACKGROUND SIGNALS IN A SPEECH-PROCESSING SYSTEM

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11/845054	25-Aug-07	7825745	VARIABLE BANDWIDTH SILICON DUPLEXER
11/540484	28-Sep-06	7843509	ITERATIVE IMAGE INTERPOLATION METHOD
11/134995	23-May-05	7864904	METHOD AND DATA PROCESSING DEVICE FOR PROCESSING DIGITAL DATA OF A SIGNAL
10/580672	6-Dec-04	7868949	SCHALTUNGSANORDNUNG UND VERFAHREN ZUM EINRASTEN AUF UND/ODER ZUM VERARBEITEN VON DATEN, INSBESONDERE AUDIO-, T[ELE]V[I
10/548743	5-Mar-04	7880784	ARRANGEMENT FOR GENERATING A 3D VIDEO SIGNAL
11/570800	21-Jun-05	7881500	MOTION ESTIMATION WITH VIDEO MODE DETECTION
11/547937	8-Apr-05	7885325	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
10/889975	12-Jul-04	7889759	NETWORK INTERFACE AND BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
11/444976	1-Jun-06	7894529	METHOD AND DEVICE FOR DETERMINING MOTION VECTORS
12/510442	28-Jul-09	7894547	IN-PHASE AND QUADRATURE-PHASE SIGNAL AMPLITUDE AND PHASE CALIBRATION
10/520316	1-Jul-03	7894609	STEREO SIGNAL PROCESSING APPARATUS
11/685196	12-Mar-07	7916815	METHOD AND APPARATUS FOR A MULTICARRIER RECEIVER CIRCUIT WITH GUARD INTERVAL SIZE DETECTION
11/416831	3-May-06	7924348	METHOD AND APPARATUS FOR DISTRIBUTING MULTIPLE SIGNAL INPUTS TO MULTIPLE INTEGRATED CIRCUITS

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10/237814	9-Sep-02	7929609	MOTION ESTIMATION AND/OR COMPENSATION
11/133146	19-May-05	7930174	DEVICE AND METHOD FOR SUPPRESSING NOISE
12/206465	8-Sep-08	7933281	COMMUNICATIONS BRIDGE BETWEEN INCOMPATIBLE NETWORKS
11/101384	7-Apr-05	7933332	METHOD AND APPARATUS FOR DETERMINATION OF MOTION VECTOR ALLOCATED TO IMAGE REGION OF IMAGE
10/184518	28-Jun-02	7933411	METHOD OF CONSTRUCTING MPEG PROGRAM STREAMS FROM ENCRYPTED MPEG TRANSPORT STREAMS
12/490317	24-Jun-09	7936206	HIGHLY LINEAR CMOS CAPACTIVE VOLTAGE DIVIDER WITH CONSTANT INPUT CAPACITANCE
12/179454	24-Jul-08	7936669	QOS IN MII MODE WITH TRAFFIC SHAPING BY THE ETHERNET DRIVER.
12/098982	7-Apr-08	7936701	FREQUENCY SCANNING FOR FORMATION OF COMMUNICATION NETWORK OF NODES
10/925365	23-Aug-04	7937738	VARIABLE MODULATION UNICAST SYSTEM
11/763359	14-Jun-07	7941091	MULTI-STAGE SIGNAL COMBINER NETWORK
09/989248	20-Nov-01	7944966	VIDEO DECODING METHOD AND CORRESPONDING DECODER
10/531931	1-Oct-03	7949205	IMAGE PROCESSING UNIT WITH FALL-BACK
11/303876	16-Dec-05	7958424	MULTI-DEMODULATOR / FEC EFFICIENT ARCHITECTURE
10/185905	28-Jun-02	7961251	METHOD AND APPARATUS FOR CONVERSION OF VIDEO FORMATS TO 120 HZ 4 TO 1 INTERLACED FORMATS
10/563849	1-Jul-04	7974342	MOTION-COMPENSATED IMAGE SIGNAL INTERPOLATION

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11/618337	29-Dec-06	7982799	METHOD AND DEVICE FOR INTERPOLATION OF AN IMAGE INFORMATION VALUE FOR PIXEL OF AN INTERLINE
12/473656	28-May-09	7995459	ECHO PROFILE PROBE
11/547816	8-Apr-05	7995648	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
10/494503	4-May-04	7995793	OCCCLUSION DETECTOR FOR AND METHOD OF DETECTING OCCCLUSION AREAS
12/015774	17-Jan-08	8009725	SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
11/851453	7-Sep-07	8019172	METHOD AND DEVICE FOR INCREASING THE RESOLUTION OF A DATA SEQUENCE
12/015773	17-Jan-08	8023912	SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
11/894671	21-Aug-07	8031812	PROCESS AND CIRCUIT ARRANGEMENT FOR DECIDING A SYMBOL AT RECEIPT OF A SIGNAL COUPLED WITH A QUADRATURE SIGNAL PAIR
12/173685	15-Jul-08	8040962	CHANNEL RESPONSE CALCULATION IN AN OFDM RECEIVER
11/579357	29-Apr-05	8059133	GRAPHICS PIPELINE FOR RENDERING GRAPHICS
10/567397	14-Jul-04	8059715	VIDEO ENCODING AND DECODING METHODS AND CORRESPONDING DEVICES
12/009135	16-Jan-08	8064545	DEVICE AND METHOD FOR DETERMINING A CONSTELLATION OF A QUADRATURE AMPLITUDE MODULATED SIGNAL
12/144319	23-Jun-08	8064600	ENCODED DIGITAL VIDEO CONTENT PROTECTION BETWEEN TRANSPORT DEMULTIPLEXER AND DECODER
12/781749	17-May-10	8065593	ERASURES ASSISTED BLOCK CODE DECODER AND RELATED METHOD

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11/631207	30-Jun-05	8068176	NOISE ELIMINATION DEVICE FOR THE DETECTION OF THE VERTICAL SYNC PULSE IN VIDEO SIGNALS
10/501424	12-Dec-02	8073054	UNIT FOR AND METHOD OF ESTIMATING A CURRENT MOTION VECTOR
10/555518	3-May-04	8073055	BIASED MOTION VECTOR INTERPOLATION FOR REDUCED VIDEO ARTIFACTS
11/572914	18-Jul-05	8081258	EXTRACTION OF DATA FROM A TELEVISION SIGNAL USING AN ADAPTABLE SLICING LEVEL
11/292939	2-Dec-05	8085802	PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
12/016998	19-Jan-08	8086170	SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
12/749011	29-Mar-10	8095743	MULTIPROCESSOR ARCHITECTURE AND METHOD FOR CONTROLLING MEMORY ACCESS IN A MULTIPROCESSOR ARCHITECTURE
10/577105	19-Oct-04	8102915	MOTION VECTOR FIELDS REFINEMENT TO TRACK SMALL FAST MOVING OBJECTS
12/202321	31-Aug-08	8115781	SYSTEMS AND METHODS OF IMAGE PROCESSING INCLUDING REMOVAL OF DISCONTINUOUS RAMP AND/OR SMOOTHING FEATURES
11/575487	16-Sep-04	8115874	MEMORY OPTIMIZATION FOR VIDEO PROCESSING
12/301229	29-Jun-07	8121550	SATELITE CROSS POLARIZATION CANCELING
10/555262	28-Apr-04	8126051	VIDEO ENCODING AND DECODING METHODS AND CORRESPONDING ENCODING AND DECODING DEVICES
11/547741	8-Apr-05	8126083	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCYOF A SAMPLING DEVICE

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10/537889	12-Dec-03	8135073	ENHANCING VIDEO IMAGES DEPENDING ON PRIOR IMAGE ENHANCEMENTS
11/563479	27-Nov-06	8139154	APPARATUS AND METHOD FOR INTERPOLATING A PIXEL FROM AN INTERMEDIATE LINE OF A FIELD
12/297175	16-Apr-07	8150351	MULTI-MODE LOW NOISE AMPLIFIER
10/556616	6-May-04	8155459	VIDEO PROCESSING DEVICE WITH LOW BANDWIDTH REQUIREMENTS
12/027216	6-Feb-08	8176181	WAVE MECHANISM FOR MOCA NETWORK
12/355234	16-Jan-09	8176353	VERFAHREN ZUR BERTRAGUNG VON DATEN ZWISCHEN WENIGSTENS ZWEI TAKTDOM NEN
12/557288	10-Sep-09	8179920	HIGH EFFICIENCY PREAMBLES FOR COMMUNICATIONS SYSTEMS OVER PSEUDO-STATIONARY COMMUNICATION CHANNELS.
10/637173	7-Aug-03	8181208	MEDIA SERVER AND NETWORK FOR COAXIAL CABLE
12/145288	24-Jun-08	8184663	MULTI-FORMAT STREAM RE-MULTIPLEXER FOR MULTI-PASS, MULTI-STREAM, MULTIPLEXED TRANSPORT STREAM PROCESSING
12/063855	7-Aug-06	8184689	VIDEO ENCODING AND DECODING PRESERVING CACHE LOCALITIES
10/554530	22-Apr-04	8184922	DIGITAL FILTER CONFIGURATION
11/874096	17-Oct-07	8189105	SYSTEMS AND METHODS OF MOTION AND EDGE ADAPTIVE PROCESSING INCLUDING MOTION COMPENSATION FEATURES
13/098739	2-May-11	8198935	HIGHLY LINEAR CMOS CAPACTIVE VOLTAGE DIVIDER WITH CONSTANT INPUT CAPACITANCE

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12/324657	26-Nov-08	8204102	SIMPLE OFDM EQUALIZER WITH REDUCED SENSITIVITY TO IQ IMBALANCE
12/407506	19-Mar-09	8212546	WIDEBAND CMOS RF RMS POWER DETECTION SCHEME
12/822977	24-Jun-10	8223046	ADC AND DAC WITH ACCELERATED SAMPLING
10/675566	30-Sep-03	8223775	ARCHITECTURE FOR A FLEXIBLE AND HIGH-PERFORMANCE GATEWAY CABLE MODEM
12/117890	9-May-08	8228910	PACKET AGGREGATION FOR BROADBAND NETWORK
12/571201	30-Sep-09	8237595	METHOD AND APPARATUS FOR BANDPASS DIGITAL TO ANALOG CONVERTER
11/715820	8-Mar-06	8238505	METHOD AND CIRCUIT FOR LINE-COUPLED CLOCK GENERATION
12/429908	24-Apr-09	8239631	SYSTEM AND METHOD FOR REPLACING DATA IN A CACHE
12/241629	30-Sep-08	8266265	CHANNEL BONDING WITH C.LINK+
10/536732	11-Nov-03	8266444	CHIP INTEGRATED PROTECTION MEANS
11/995984	10-Jul-06	8279943	METHOD FOR RECEPTION OF DVB-H SIGNALS AND DVB-H RECEIVER
12/375563	2-Aug-06	8279989	DEVICE AND PROCESS FOR DATA RATE ACQUISITION
12/635649	10-Dec-09	8284690	OFDM PARAMETERS.
13/220530	29-Aug-11	8300681	SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
11/212122	25-Aug-05	8311090	CODING OF A PLURALITY OF BINARY INPUT DATA WORDS INTO ONE CODEWORD
12/347841	31-Dec-08	8311112	SYSTEM AND METHOD FOR VIDEO COMPRESSION USING PREDICTIVE CODING
12/552240	1-Sep-09	8315272	SYSTEMS AND METHODS FOR DIGITAL INTERFACE TRANSLATION

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10/585200	20-Dec-04	8319890	ARRANGEMENT FOR GENERATING A 3:2 PULL-DOWN SWITCH-OFF SIGNAL FOR A VIDEO COMPRESSION ENCODER
12/161570	18-Jan-07	8320451	REPLACEMENT OF FRAME DATA IN A VIDEO STREAM SIGNAL
12/339249	19-Dec-08	8320478	SYSTEM AND METHOD FOR GENERATING A SIGNAL WITH A RANDOM LOW PEAK TO AVERAGE POWER RATIO WAVEFORM FOR AN ORTHOGONAL FRE
12/580227	15-Oct-09	8320566	SILENT PROBES IN A COMMUNICATION NETWORK
12/917149	1-Nov-10	8324984	VARIABLE BANDWIDTH SILICON DUPLEXER
12/341920	22-Dec-08	8325858	ACCURATE ESTIMATION OF NOISE VARIANCE COMBINED WITH A NARROW-BAND INTERFERENCE DETECTION APPLIED TO LOG-LIKELIHOOD COM
12/228248	11-Aug-08	8335392	METHOD FOR REDUCING IMAGE ARTIFACTS
12/688535	15-Jan-10	8340125	METHOD AND APPARATUS FOR BLOCK ACKNOWLEDGEMENT IN A COMMUNICATION NETWORK.
11/302876	14-Dec-05	8340616	LOW NOISE AMPLIFIER AND TRACKING FILTER FOR TUNER
12/377976	6-Aug-07	8345734	TIME ERROR ESTIMATION FOR DATA SYMBOLS
12/414892	31-Mar-09	8345798	DIGITAL CHANNEL STACKING SWITCH
12/162825	31-Jan-07	8350963	VIDEO PROCESSING DEVICE AND METHOD OF PROCESSING VIDEO DATA
12/245498	3-Oct-08	8351368	METHOD, SYSTEM, AND APPARATUS FOR EXTENDED RATE/RANGE COMMUNICATION OVER A COMMUNICATION NETWORK

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12/895817	30-Sep-10	8352256	ADAPTIVE REDUCTION OF NOISE SIGNALS AND BACKGROUND SIGNALS IN A SPEECH-PROCESSING SYSTEM
12/027228	6-Feb-08	8352569	WAVE MECHANISM FOR MOCA NETWORK
10/537673	3-Dec-03	8362716	DRIVE APPARATUS FOR FRAME DEFLECTION AND METHOD
12/580127	15-Oct-09	8363681	SILENT PROBES IN A COMMUNICATION NETWORK
11/722557	19-Dec-05	8363714	VIDEO STREAM MODIFIER
13/203666	26-Feb-10	8373796	DETECTING OCCLUSION
10/505499	5-Feb-03	8374465	METHOD AND APPARATUS FOR FIELD RATE UP-CONVERSION
13/084453	11-Apr-11	8384464	LOW JITTER CLOCK INTERPOLATOR
12/258178	24-Oct-08	8385421	METHOD FOR ESTIMATING THE MOTION IN IMAGE PROCESSING
10/537875	12-Dec-03	8385426	METHOD FOR A MOSAIC PROGRAM GUIDE
13/171257	28-Jun-11	8390378	TECHNIQUES OF BROADBAND INPUT MATCHING WITH NOISE AND NON-LINEARITY CANCELLATION IN AMPLIFIER/POWER AMPLIFIER (PA).
10/518772	17-Jun-03	8400435	CIRCUIT ARRANGEMENT FOR A DISPLAY DEVICE WHICH CAN BE OPERATED IN A PARTIAL MODE
10/483024	21-Jun-02	8406302	UNIT FOR AND METHOD OF MOTION ESTIMATION AND IMAGE PROCESSING APPARATUS PROVIDED WITH SUCH MOTION ESTIMATION UNIT
12/297500	29-Mar-07	8406305	METHOD AND SYSTEM FOR CREATING AN INTERPOLATED IMAGE USING UP-CONVERSION VECTOR WITH UNCOVERING-COVERING DETECTION
10/584501	21-Dec-04	8411099	COMPUTER GRAPHICS PROCESSOR AND METHOD OF RENDERING IMAGES

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13/090907	20-Apr-11	8411565	BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
12/163787	27-Jun-08	8412759	ACTIVE DIPLEXERS
12/058280	28-Mar-08	8416344	ITERATIVE METHOD FOR INTERPOLATING IMAGE INFORMATION VALUES
12/709435	19-Feb-10	8416685	METHOD FOR MAXIMIZING THE EFFICIENCY OF PACKET AGGREGATION AND THE PQOS BANDWIDTH UTILIZATION
12/581063	16-Oct-09	8418036	SILENT PROBES IN A COMMUNICATION NETWORK
12/531637	18-Mar-08	8418043	ERROR DETECTION
12/165528	30-Jun-08	8427944	LAYER 2 MULTICASTING FOR AN ACCESS NETWORK OVER COAX USING OPTIMIZED BITLOADING.
12/346910	31-Dec-08	8428145	SYSTEM AND METHOD FOR PROVIDING FAST TRICK MODES
13/274794	17-Oct-11	8432999	CHANNEL RESPONSE CALCULATION IN AN OFDM RECEIVER
12/516785	20-Nov-07	8441498	DEVICE AND METHOD FOR PROCESSION COLOR IMAGE DATA
12/063074	3-Aug-06	8441579	MOTION JUDDER CANCELLATION IN IMAGE SEQUENCES
11/915489	19-May-06	8442118	CALCULATING TRANSFORMATION PARAMETERS FOR IMAGE PROCESSING
12/331292	9-Dec-08	8443270	HARDWARE REUSE IN LDPC FOR MUTIPLE ACCESS.
09/966038	28-Sep-01	8446530	DYNAMIC SAMPLING
12/258142	24-Oct-08	8446950	METHOD OF ESTIMATING THE MOTION IN IMAGE PROCESSING
11/996470	12-Jul-06	8451758	METHOD AND DEVICE FOR OPERATING OF TWO WIRELESS SERVICES
12/304336	12-Jun-07	8451900	GLOBAL MOTION ESTIMATION
10/211895	2-Aug-02	8451968	CHARACTER GENERATOR COMPRISING A STARTSTOP OSCILLATOR

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10/522463	9-Jul-03	8452827	DATA PROCESSING CIRCUIT
12/406513	18-Mar-09	8457424	METHOD FOR TESTING A MOTION VECTOR
12/165510	30-Jun-08	8462898	BLIND COMPENSATION AND CORRECTION OF TRANSMITTER IQ IMBALANCE AT THE RECEIVER ON AN OFDM COMMUNICATION SYSTEM
10/587604	20-Jan-05	8466924	DISPLAYING ON A MATRIX DISPLAY
10/585443	20-Mar-08	8467452	DISTRIBUTING CANDIDATE VECTORS BASED ON LOCAL MOTION COMPLEXITY
12/676358	28-May-10	8467604	COLOR ENHANCEMENT
12/688559	15-Jan-10	8468200	RETRANSMISSION ADMISSION MECHANISM IN MOCA NETWORK.
12/687772	14-Jan-10	8472475	A DATA PACKET AGGREGATION FORMAT FOR ENABLING RETRANSMISSION AND FRAGMENTATION IN MOCA
12/681366	1-Oct-08	8478065	PIXEL PROCESSING
12/689858	19-Jan-10	8483152	METHOD AND APPARATUS FOR USE OF OFMDA IN A COMMUNICATION NETWORK.
11/547819	8-Apr-05	8483317	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
12/158995	21-Dec-06	8484389	AN AV RENDERER PERIPHERAL WITH DUAL INTERRUPT LINES FOR STAGGERED INTERRUPTS
12/599048	14-May-08	8488887	METHOD OF DETERMINING AN IMAGE DISTRIBUTION FOR A LIGHT FIELD DATA STRUCTURE
11/292947	2-Dec-05	8498294	PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
12/346936	31-Dec-08	8514942	LOW-RESOLUTION VIDEO CODING CONTENT EXTRACTION
11/472588	22-Jun-06	8519928	METHOD AND SYSTEM FOR FRAME INSERTION IN A DIGITAL DISPLAY SYSTEM

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12/678283	9-Sep-08	8520849	ENCRYPTION BY PIXEL PROPERTY SEPARATION
12/717384	4-Mar-10	8522061	METHOD AND APPARATUS OF POWER MANAGEMENT
12/677508	5-Aug-08	8526502	METHOD AND APPARATUS FOR LINE BASED VERTICAL MOTION ESTIMATION AND COMPENSATION
12/747725	26-Dec-08	8531610	ARRANGEMENT AND APPROACH FOR IMAGE DATA PROCESSING
13/612613	12-Sep-12	8536916	DIGITALLY CONTROLLED OSCILLATOR WITH THERMOMETER SIGMA DELTA ENCODED FREQUENCY CONTROL WORD
10/524968	24-Jul-03	8537076	VIDEO CIRCUIT
11/233674	23-Sep-05	8537903	DE-BLOCKING AND DE-RINGING SYSTEMS AND METHODS
12/165426	30-Jun-08	8542683	MULTIPLE DESTINATION PACKET AGGREGATION WITH DYNAMICALLY OPTIMIZED BITLOADING.
12/346906	31-Dec-08	8542938	SYSTEM AND METHOD FOR INTRA-FRAME COMPRESSION USING PREDICTIVE CODING
12/848905	2-Aug-10	8549377	MULTI-DEMODULATOR / FEC EFFICIENT ARCHITECTURE
12/833827	9-Jul-10	8553727	METHOD AND APPARATUS FOR LDPC TRANSMISSION OVER A CHANNEL BONDED LINK.
12/789208	27-May-10	8559461	PACKET DATA TRANSMISSION WITH OPTIMUM PREAMBLE LENGTH
12/814406	11-Jun-10	8565313	DETERMINING A VECTOR FIELD FOR AN INTERMEDIATE IMAGE
12/862636	24-Aug-10	8566678	BROADBAND SATELLITE SYSTEM FOR THE SIMULTANEOUS RECEPTION OF MULTIPLE CHANNELS
13/076743	31-Mar-11	8582576	COMMUNICATIONS BRIDGE BETWEEN INCOMPATIBLE NETWORKS

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10/961863	8-Oct-04	8587722	SYSTEM AND METHOD FOR AUTOMATICALLY CONTROLLING THE PHASE OF A CLOCK SIGNAL FOR SAMPLING AN HDTV SIGNAL
13/032580	22-Feb-11	8588055	ECHO PROFILE PROBE
13/027030	14-Feb-11	8588250	NETWORK INTERFACE AND BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE
13/013795	25-Jan-11	8593983	SILENT SYMBOL (NOW INCLUDING SILENT SUBCARRIERS).
12/612994	5-Nov-09	8611218	PROGRAMMABLE PHY MARGIN.
11/547742	8-Apr-05	8611408	APPARATUS FOR AND METHOD OF CONTROLLING A SAMPLING FREQUENCY OF A SAMPLING DEVICE
13/192821	28-Jul-11	8615061	METHOD AND APPARATUS FOR CROSS POLARIZATION AND CROSS SATELLITE INTERFERENCE CANCELLATION
12/097638	8-Dec-06	8619016	APPARATUS AND METHOD FOR COLOR SHIFT COMPENSATION IN DISPLAYS
11/241748	29-Sep-05	8621539	PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
12/678285	12-Sep-08	8630507	IMAGE DATA PROCESSING AND ARRANGEMENTS THEREFOR
11/231349	19-Sep-05	8631450	PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
12/336975	17-Dec-08	8634498	EVM PROBES FOR OFDMA.
13/029957	17-Feb-11	8638130	LOW HEADROOM LINE DRIVER
13/402014	22-Feb-12	8638808	METHOD AND APPARATUS FOR LDPC TRANSMISSION OVER A CHANNEL BONDED LINK.
13/112429	20-May-11	8639179	SINGLE-CABLE AUTOMATIC IRD INSTALLATION PROCEDURE
13/403144	23-Feb-12	8677441	SCANNING ALGORITHM FOR MOCA DEVICES EMBEDDED IN DEVICES WITH TV TUNERS

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12/882879	15-Sep-10	8681890	REAL TIME MULTIPLEXED RECEIVER ANTENNA ARRAY ELEMENTS.
12/822578	24-Jun-10	8683256	DYNAMIC ATSC INTERFERENCE DETECTION & MITIGATION IN A MOCA NETWORK.
12/747171	10-Jun-10	8687123	VIDEO SIGNAL PROCESSING
11/142830	1-Jun-05	8687695	PROCEDURE FOR THE DETERMINATION OF MOVEMENT VECTORS
13/179843	11-Jul-11	8693419	SUB-CHANNELS
12/405006	16-Mar-09	8699502	MEDIA RECEIVER HUB
12/820382	22-Jun-10	8699704	A METHOD AND APPARATUS FOR SECURELY ADMITTING NODES INTO A NETWORK
11/574344	30-Aug-05	8711072	MOTION BLUR REDUCTION FOR LCD VIDEO/GRAPHICS PROCESSORS
13/466582	8-May-12	8711848	HIGH EFFICIENCY PREAMBLES FOR COMMUNICATIONS SYSTEMS OVER PSEUDO-STATIONARY COMMUNICATION CHANNELS.
12/398042	4-Mar-09	8718142	SYSTEM AND METHOD FOR FRAME RATE CONVERSION THAT UTILIZES MOTION ESTIMATION AND MOTION COMPENSATED TEMPORAL INTERPOLAT
13/099205	2-May-11	8719890	VARIABLE MODULATION UNICAST SYSTEM
13/145504	20-Jan-10	8731320	IMAGE PROCESSING USING A BILATERAL GRID
13/129751	16-Nov-09	8732409	CACHE MANAGEMENT POLICY AND CORRESPONDING DEVICE
13/608593	10-Sep-12	8738930	CHIP INTEGRATED PROTECTION MEANS
13/556040	23-Jul-12	8750298	PACKET AGGREGATION FOR BROADBAND NETWORK
12/276023	21-Nov-08	8755467	METHOD AND CIRCUIT SYSTEM FOR DECIDING A SYMBOL UPON RECEPTION OF RECEIVED SYMBOLS COUPLED WITH A QUADRATURE SIGNAL PA

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12/819106	18-Jun-10	8767607	METHOD OF MUTICASTING OVER MOCA.
13/141688	11-Dec-09	8773595	IMAGE PROCESSING
13/156585	9-Jun-11	8774030	CHANNEL ESTIMATION VIA PREAMBLE
12/681564	6-Oct-08	8774273	METHOD AND SYSTEM FOR DECODING DIGITAL VIDEO CONTENT INVOLVING ARBITRARILY ACCESSING AN ENCODED BITSTREAM
12/006092	28-Dec-07	8774323	DEVICE AND METHOD FOR DETERMINING A SYMBOL DURING RECEPTION OF A SIGNAL COUPLED WITH A QUADRATURE SIGNAL PAIR (I,Q)
12/558781	14-Sep-09	8787430	HIGH-SPEED COMMUNICATIONS TRANSCEIVER
13/685010	26-Nov-12	8787474	SYSTEM AND METHOD FOR GENERATING A SIGNAL WITH A RANDOM LOW PEAK TO AVERAGE POWER RATIO WAVEFORM FOR AN ORTHOGONAL FRE
13/325418	14-Dec-11	8792565	10 GBPS HOME COAX NETWORKING SYSTEM
12/067080	13-Sep-06	8797457	APPARATUS AND METHOD FOR FRAME RATE PRESERVING RE-SAMPLING OR RE-FORMATting OF A VIDEO STREAM
13/661778	26-Oct-12	8798135	VIDEO STREAM MODIFIER
12/936511	6-Mar-09	8804044	TEMPORAL FALLBACK FOR HIGH FRAME RATE PICTURE RATE CONVERSION
12/343941	24-Dec-08	8811411	PACKET AGGREGATION IN MII MODE
13/609060	10-Sep-12	8817860	PHASE TRACKING SCHEME FOR ADC-BASED TUNER
13/537727	29-Jun-12	8818319	SIGNAL DISTRIBUTION AND FILTERING IN LOW NOISE BLOCK DOWNCONVERTERS
14/081512	15-Nov-13	8824270	ECHO PROFILE PROBE
12/863408	26-Jan-09	8824568	DEVICE AND METHOD FOR DECODING DIGITAL TV BROADCAST

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13/213370	19-Aug-11	8831015	MOCA-WIFI TIME MULTIPLEXING
13/060389	5-Oct-11	8842219	FRAME RATE UP-CONVERSION
12/838248	16-Jul-10	8842742	FRAME-RATE CONVERSION
12/099665	8-Apr-08	8849969	ENHANCED LINK MAINTENANCE OPERATION FOR A NETWORK
12/955642	29-Nov-10	8861357	METHOD AND APPARATUS FOR SENDING UNICAST PQOS FLOW DFID INFORMATION TO A NETWORK COORDINATOR.
12/978237	23-Dec-10	8861363	METHOD AND SYSTEM FOR INTERFERENCE DETECTION AND MITIGATION
12/797808	10-Jun-10	8861605	IMAGE PROCESSING METHOD WITH MOTION ESTIMATION AND IMAGE PROCESSING ARRANGEMENT
12/964648	9-Dec-10	8873688	UNKNOWN RATE CLOCK RECOVERY FROM FIXED RATE TRANSMISSION SYSTEM
13/681150	19-Nov-12	8879608	SYSTEMS AND METHODS FOR DIGITAL INTERFACE TRANSLATION
13/970575	19-Aug-13	8884685	ADAPTIVE DVS (DYNAMIC VOLTAGE SCALING)
13/098889	2-May-11	8891363	QOS IN MII MODE WITH TRAFFIC SHAPING BY THE ETHERNET DRIVER.
13/335735	22-Dec-11	8891544	PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
13/334975	22-Dec-11	8892026	SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
12/098849	7-Apr-08	8902360	METHOD FOR DETECTING PICTURE SEQUENCES WITH FILES REPEATED LINE-BY-LINE
12/594312	31-Mar-08	8902992	DECODER FOR SELECTIVELY DECODING PREDETERMINED DATA UNITS FROM A CODED BIT STREAM
14/028449	16-Sep-13	8907708	DIGITALLY CONTROLLED OSCILLATOR WITH THERMOMETER SIGMA DELTA ENCODED FREQUENCY CONTROL WORD

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12/747885	26-Dec-08	8908100	ARRANGEMENT AND APPROACH FOR MOTION-BASED IMAGE DATA PROCESSING
13/471613	15-May-12	8913626	MIXED-SIGNAL PROCESSING ARCHITECTURES FOR MULTIMEDIA STREAMING AND MULTIPLEXING SYSTEMS
14/165005	27-Jan-14	8913635	METHOD AND APPARATUS FOR LDPC TRANSMISSION OVER A CHANNEL BONDED LINK.
12/165300	30-Jun-08	8948189	HEURISTIC SCHEDULING OF RESERVATION REQUEST OPPORTUNITIES IN A 16-NODE MESH NETWORKS WHICH USES A FULLY COORDINATED MA
13/596016	27-Aug-12	8948716	LOW COST HIGH FREQUENCY CONVERTERS (K2IF)
11/915288	23-May-06	8964116	SPATIAL AND TEMPORAL DE-INTERLACING WITH ERROR CRITERION
09/491032	25-Jan-00	RE39202	DIGITAL VIDEO CONVERTER BOX FOR SUBSCRIBER/HOME WITH MULTIPLE TELEVISION SETS
10/038277	20-Oct-01		METHOD FOR INTEGRATING THREE-DIMENSIONAL Y/C COMB LINE FILTER AND INTERLACE/PROGRESSIVE CONVERTER INTO SINGLE CHIP AND
10/531902	19-Sep-03		MANIPULATING SUB-PICTURES OF A COMPRESSED VIDEO SIGNAL
10/560713	29-Jun-04		TRICK PLAY USING CRT SCAN MODES
11/189009	25-Jul-05		SUPPRESSION OF QUANTISATION NOISE IN A DATA PROCESSOR
11/197561	3-Aug-05		SYSTEM AND METHOD FOR PROTECTING DATA STORED IN THE CONTROL REGISTERS OF AN INTEGRATED CIRCUIT
12/015760	17-Jan-08		SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING

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12/027202	6-Feb-08		WAVE MECHANISM FOR MOCA NETWORK
12/031496	14-Feb-08		WAVE MECHANISM FOR MOCA NETWORK
12/063854	10-Aug-06		VIDEO PROCESSING METHOD AND DEVICE FOR DEPTH EXTRACTION
12/139877	16-Jun-08		METHOD FOR PROCESSING AN IMAGE SEQUENCE HAVING CONSECUTIVE VIDEO IMAGES IN ORDER TO IMPROVE THE SPATIAL RESOLUTION
12/182100	29-Jul-08		DMA ACCELERATOR
12/245535	3-Oct-08		METHOD FOR EFFICIENT PACKET FRAMING IN A COMMUNICATION NETWORK
12/415875	31-Mar-09		MAP TRANSMISSION SCHEDULE AND FORMAT FOR QUICK ERROR RECOVERY IN MOCA
12/438316	20-Aug-07		MOTION-COMPENSATED PROCESSING OF IMAGE SIGNALS
12/446636	19-Oct-07		SYSTEM COMPRISING NODES WITH ACTIVE AND PASSIVE PORTS
12/477339	3-Jun-09		INTEGRATED CROSSPOINT SWITCH WITH BAND TRANSLATION
12/491782	25-Jun-09		A METHOD OF OPTIMIZING THE NUMBER OF CHANNEL ESTIMATION SYMBOLS BASED ON THE CHANNEL COHERENCE BANDWIDTH TO IMPROVE TH
12/570731	30-Sep-09		OFFSET LO REDUCE IMAGE BASED ARTIFACTS
12/579312	14-Oct-09		SILENT PROBES IN A COMMUNICATION NETWORK
12/670397	23-Jul-07		CARRIER RECOVERY METHOD AND CARRIER RECOVERY DEVICE FOR PSEUDO RANDOM NOISE BASED SYSTEM
12/688518	15-Jan-10		LAYER-2 DEVICE CAPABILITY DISCOVERY METHOD.

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12/822676	24-Jun-10		METHOD AND APPARATUS FOR NODE-BASED QOS MANAGEMENT.
12/889967	24-Sep-10		REAL TIME MULTIPLEXED TRANSMITTER ANTENNA ARRAY ELEMENTS.
12/895312	30-Sep-10		ARCHITECTURE OF STATIC QOS SYSTEM.
13/041662	7-Mar-11		MOCA2 NETWORK COORDINATORS COORDINATE SYNCHRONOUS OFDMA FOR UPSTREAM RESERVATION REQUESTS.
13/062898	11-Aug-09		SYSTEMS AND METHODS FOR PROVIDING FAST VIDEO CHANNEL SWITCHING
13/075719	30-Mar-11		MOCA2 NETWORK COORDINATORS COORDINATE SYNCHRONOUS OFDMA FOR UPSTREAM RESERVATION REQUESTS.
13/076387	30-Mar-11		METHOD AND APPARATUS FOR QUALITY-OF-SERVICE (QOS) MANAGEMENT
13/099079	2-May-11		FREQUENCY SCANNING FOR FORMATION OF COMMUNICATION NETWORK OF NODES
13/121347	30-Sep-09		PROFILE FOR FRAME RATE CONVERSION
13/123858	21-Oct-09		DEVICE AND METHOD FOR MOTION ESTIMATION AND COMPENSATION
13/355413	20-Jan-12		SYSTEMS AND METHODS FOR SELECTING DIGITAL CONTENT CHANNELS USING LOW NOISE BLOCK CONVERTERS INCLUDING DIGITAL CHANNELS
13/397443	15-Feb-12		OPTICAL CONVERTER WITH ADC BASED CHANNELIZER FOR OPTICAL LNB SYSTEM
13/448639	17-Apr-12		SW UPGRADE USING L2ME
13/539922	2-Jul-12		WIDEBAND CMOS RF RMS POWER DETECTION SCHEME

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13/557067	24-Jul-12		METHOD AND APPARATUS FOR BANDPASS DIGITAL TO ANALOG CONVERTER
13/566470	3-Aug-12		SWITCHABLE DIPLEXER WITH IMPROVED ISOLATIONS
13/566508	3-Aug-12		SWITCHABLE DIPLEXER WITH IMPROVED ISOLATIONS
13/566557	3-Aug-12		SWITCHABLE DIPLEXER WITH IMPROVED ISOLATIONS
13/584541	13-Aug-12		METHOD FOR SELECTING FREQUENCY BANDS IN A NETWORK DEVICE FOR MULTIPLE HOME NET-WORKS
13/647016	8-Oct-12		DISTRIBUTED CONTINUOUS ANTENNA
13/735957	7-Jan-13		METHOD, SYSTEM, AND APPARATUS FOR EXTENDED RATE/RANGE COMMUNICATION OVER A COMMUNICATION NETWORK
13/890115	8-May-13		TIME DIVISION DUPLEXING FOR EPoC
13/908768	3-Jun-13		LOW-RESOLUTION VIDEO CODING CONTENT EXTRACTION
13/947521	22-Jul-13		ADAPTIVE LDO MINIMIZING DISSIPATION
13/952308	26-Jul-13		HIGH EFFICIENCY SWITCHED CAPACITOR VOLTAGE REGULATOR.
13/956272	31-Jul-13		ADVANCED VOLTAGE REGULATION FOR INTEGRATED CIRCUITS
14/044521	2-Oct-13		TX ENERGY LEAK CANCELLATION IN RX
14/052399	11-Oct-13		TRANSMIT NOISE AND IMPEDANCE CHANGE MITIGATION IN WIRED COMMUNICATION SYSTEM
14/082544	18-Nov-13		NETWORK INTERFACE AND BROADBAND LOCAL AREA NETWORK USING COAXIAL CABLE

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14/082593	18-Nov-13		METHOD AND APPARATUS FOR CROSS POLARIZATION AND CROSS SATELLITE INTERFERENCE CANCELLATION
14/084278	19-Nov-13		DETECTION OF RVU CAPABLE TELEVISION SET
14/135561	19-Dec-13		SINGLE-CABLE AUTOMATIC IRD INSTALLATION PROCEDURE
14/154101	13-Jan-14		PHYSICAL LAYER TRANSMITTER FOR USE IN A BROADBAND LOCAL AREA NETWORK
14/158484	17-Jan-14		EVM PROBES FOR OFDMA.
14/175774	7-Feb-14		VIDEO DECODER MEMORY BANDWIDTH COMPRESSION
14/177707	11-Feb-14		EFFICIENT LDPC CODES
14/178603	12-Feb-14		SUB-CHANNELS
14/188328	24-Feb-14		A METHOD AND APPARATUS FOR SECURELY ADMITTING NODES INTO A NETWORK
14/232330	13-Jul-12		METHOD AND APPARATUS FOR ACCURATE MOTION ESTIMATION USING MULTIPLE FRAMES OF VIDEO SIGNAL CONTAINING ALIAS
14/262253	25-Apr-14		VARIABLE MODULATION UNICAST SYSTEM
14/267774	1-May-14		IMAGE PROCESSING USING A BILATERAL GRID
14/271060	6-May-14		ADAPTIVE AGC
14/280412	16-May-14		MECHANISM FOR SPECTRAL SPREADING OF A PULSE-DENSITY MODULATED WAVEFORM
14/285503	22-May-14		METHOD AND SYSTEM FOR INTERFERENCE DETECTION AND MITIGATION
14/290066	29-May-14		COGNITIVE ENERGY SAVING METHOD
14/290553	29-May-14		ADAPTIVE TX POWER CONTROL
14/292321	30-May-14		IC STANDBY POWERING
14/293725	2-Jun-14		ANALOG CHANNELIZER - BAND SWITCHING AND DIPLEXING

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14/295873	4-Jun-14		RECEIVED SIGNAL SPUR REJECTION
14/297344	5-Jun-14		TX SPECTRUM SPUR CANCELLATION
14/302152	11-Jun-14		USB TO COAX BRIDGE
14/303260	12-Jun-14		ENHANCED LINK MAINTENANCE OPERATION FOR A NETWORK
14/321208	1-Jul-14		FUSION2/MALAGA IQ CALIBRATION FUNCTIONAL SPECIFICATION
14/322514	2-Jul-14		REFERENCE ADC PER SLICE IN A TIME-INTERLEAVED ADC
14/334443	17-Jul-14		SYSTEMS AND METHODS FOR SELECTING DIGITAL CONTENT CHANNELS USING LOW NOISE BLOCK CONVERTERS INCLUDING DIGITAL CHANNELS
14/383623	8-Sep-14		ARCHITECTURE FOR UNIFYING AN EPON ACCESS NETWORK AND A COAX-BASED ACCESS NETWORK
14/386460	19-Sep-14		LIMITATION OF PHY CONCATENATION IN JAWS
14/394800	16-Apr-13		PROGRESSIVE MODULATION FOR DOWNSTREAM ACCESS
14/402840	21-Nov 14		TIME DIVISION DUPLEXING FOR EPoC
14/451359	4-Aug-14		SATELLITE SIGNAL FREQUENCY TRANSLATION AND STACKING
14/503821	1-Oct-14		DYNAMIC VOLTAGE SCALING METHOD AND ITS APPLICATIONS TOWARDS SOC POWER AND AREA OPTIMIZATION USING ADVANCED CMOS TECHNO
14/505200	2-Oct-14		EFFICIENT INTERFACE BROADBAND TRANSCEIVERS
14/505223	2-Oct-14		EFFICIENT INTERFACE BROADBAND TRANSCEIVERS
14/509905	8-Oct-14		QOS IN MII MODE WITH TRAFFIC SHAPING BY THE ETHERNET DRIVER.

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14/510971	9-Oct-14		METHOD AND APPARATUS FOR LDPC TRANSMISSION OVER A CHANNEL BONDED LINK.
14/511997	10-Oct-14		COMMUNICATION SYSTEM WITH EXPANDABLE CAPACITY
14/532918	4-Nov-14		DIRECT SIGNAL DIGITIZER
14/532935	4-Nov-14		DIRECT SIGNAL DIGITIZER
14/532959	4-Nov-14		DIRECT SIGNAL DIGITIZER
14/532973	4-Nov-14		DIRECT SIGNAL DIGITIZER
14/575638	18-Dec-14		LOW COST HIGH FREQUENCY CONVERTERS (K2IF)
14/577000	19-Dec-14		MULTI-BAND TRANSCEIVE FRONT-END ARCHITECTURE WITH REDUCED SWITCH INSERTION LOSS
61/973739	01-Apr-14		METHOD AND APPARATUS FOR CALIBRATION OF A TIME INTERLEAVED ADC
61/978438	11-Apr-14		METHOD AND APPARATUS FOR SPECTRUM SPREADING OF A PULSE-DENSITY MODULATED WAVEFORM
61/978642	11-Apr-14		METHOD AND APPARATUS FOR ADAPTIVE AUTOMATIC GAIN CONTROL
61/979402	14-Apr-14		COGNITIVE ENERGY SAVING METHOD AND APPARATUS
61/979862	15-Apr-14		METHOD AND APPARATUS FOR CANCELLATION OF SPURIOUS SIGNALS
61/981629	18-Apr-14		METHOD AND APPARATUS FOR BAND SELECTION, SWITCHING AND DIPLEXING
61/981633	18-Apr-14		SYSTEM AND METHOD FOR IQ IMBALANCE ESTIMATION
61/982827	22-Apr-14		METHOD AND APPARATUS FOR SPREADING ENERGY OF A PHASE LOCK LOOP
61/993938	15-May-14		MALAGA CABLE-DISCONNECT POWER
62/000894	20-May-14		METHOD AND APPARATUS FOR PROVIDING STANDBY POWER TO AN INTEGRATED CIRCUIT
62/001252	21-May-14		MALAGA CABLE-DISCONNECT POWER

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62/001477	21-May-14		MoCA OSP SUBNETWORKS
62/008917	06-Jun-14		METHOD AND APPARATUS FOR EFFECTIVELY FORMING A NEW NETWORK
62/013925	18-Jun-14		COMMUNICATION SYSTEM WITH EXPANDABLE CAPACITY
62/028317	24-Jul-14		METHOD AND APPARATUS FOR MOCA NETWORK WITH PROTECTED SET-UP
62/036489	12-Aug-14		METHOD AND APPARATUS FOR DETERMINING MOCA BEACON TRANSMIT POWER
62/036495	12-Aug-14		METHOD AND APPARATUS FOR ADMISSOIN TO A MOCA NETWORK
62/036496	12-Aug-14		METHOD AND APPARATUS FOR ADMISSION TO A MOCA NETWORK
62/036545	12-Aug-14		METHOD AND APPARATUS FOR PRE-ADMISSION MESSAGING TO NODES OF A MOCA NETWORK
62/043403	28-Aug-14		METHOD AND APPARATUS FOR PROVIDING A HIGH SECURITY MODE IN A MOCA 2.0 NETWORK
62/051532	17-Sep-14		METHOD AND APPARATUS FOR MOCA NETWORK WITH PROTECTED SET-UP
62/068621	24-Oct-14		METHOD AND APPARATUS FOR MOCA NETWORK WITH PROTECTED SET-UP
62/075039	04-Nov-14		QUADRICORRELATOR CARRIER FREQUENCY TRACKING
62/076351	06-Nov-14		MULTI-BAND TRANSCEIVER FRONT-END ARCHITECTURE WITH REDUCED SWITCH INSERTION LOSS
PCT/US2011/040844	17-Jun-14		NODE-BASED QUALITY-OF-SERVICE MANAGEMENT
PCT/US2012/025268	15-Feb-12		OPTICAL CONVERTER WITH ADC BASED CHANNELIZER FOR OPTICAL LNB SYSTEM
PCT/US2012/030858	28-Mar-14		METHOD AND APPARATUS FOR QUALITY-OF-SERVICE (QOS) MANAGEMENT
PCT/US2012/037023	09-May-12		MoCA-WiFi MULTIPLEXING
PCT/US2012/059234	08-Oct-12		DISTRIBUTED CONTINUOUS ANTENNA
PCT/US2013/020350	04-Jan-13		DISCOVERY IN MoCA NETWORKS
PCT/US2013/027451	22-Feb-13		SCANNING ALGORITHM FOR EMBEDDED NETWORK DEVICES

Application Number	Filing Date	Patent Number	Title
PCT/US2013/029923	08-Mar-13		METHOD AND APPARATUS FOR UNIFYING AN EPON ACCESS NETWORK AND A COAX-BASED ACCESS NETWORK
PCT/US2013/036725	16-Apr-13		DUAL FUNCTION SOLID STATE CONVERTER
PCT/US2013/051459	23-Jul-13		SWITCHABLE DIPLEXER TO PROVIDE IMPROVED ISOLATION
PCT/US2013/051610	23-Jul-13		CASCADED DIPLEXER CIRCUIT
PCT/US2013/059899	16-Sep-13		AN EFFICIENT PROGRESSIVE JPEG DECODE METHOD
PCT/US2013/060006	16-Sep-13		DIGITALLY CONTROLLED OSCILLATOR WITH THERMOMETER SIGMA DELTA ENCODED FREQUENCY CONTROL WORD
PCT/US2013/060523	19-Sep-13		PARALLEL DECODE OF A PROGRESSIVE JPEG BITSTREAM
PCT/US2013/060626	19-Sep-13		A PROGRESSIVE JPEG BITSTREAM TRANSCODER AND DECODER
PCT/US2013/065613	18-Oct-13		TIME DIVISION DUPLEXING FOR EPoC
PCT/US2014/018196	25-Feb-14		METHOD AND APPARATUS FOR DIGITIZATION OF BROADBAND ANALOG SIGNALS
PCT/US2014/023639	11-Mar-14		SYNCHRONIZED MULTI-CHANNEL ACCESS SYSTEM WITHOUT GUARD-BAND
PCT/US2014/029102	14-Mar-14		NETWORK BRIDGING WITH QoS
PCT/US2014/038260	15-May-14		MULTIPLE ANTENNA COMMUNICATION SYSTEM AND METHOD
PCT/US2014/038269	15-May-14		HYBRID WIRED AND WIRELESS NETWORK COMMUNICATION SYSTEM AND METHOD
PCT/US2014/047988	24-Jul-14		HIGH-EFFICIENCY SWITCHED CAPACITOR VOLTAGE REGULATOR
PCT/US2014/048591	29-Jul-14		METHOD AND APPARATUS FOR COMBINED ACCESS NETWORK AND HOME NETWORK USING A DUAL-ROLE DEVICE
PCT/US2014/058554	01-Oct-14		TRANSMIT ENERGY LEAKAGE CONTROL IN A RECEIVER
PCT/US2014/060035	10-Oct-14		TRANSMIT NOISE AND IMPEDANCE CHANGE MITIGATION IN WIRED COMMUNICATION SYSTEM
PCT/US2014/066654	20-Nov-14		CABLE SENSING SYSTEMS AND METHODS FOR POWER MANAGEMENT

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PCT/US2014/066661	20-Nov-14		NETWORK SENSING SYSTEMS AND METHODS FOR POWER MANAGEMENT
PCT/US2014/066666	20-Nov-14		AUTOMATIC NETWORK DETECTION AND FORMATION SYSTEMS AND METHODS
PCT/US2014/070416	15-Dec.2014		FLEXIBLE UPSTREAM AND DOWNSTREAM LOADING