# 504395978 06/02/2017

# **PATENT ASSIGNMENT COVER SHEET**

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4442675

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	

### **CONVEYING PARTY DATA**

Name	Execution Date
DUK JU JEONG	05/12/2017

## **RECEIVING PARTY DATA**

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# **PROPERTY NUMBERS Total: 1**

Property Type	Number	
Application Number:	15611645	

## **CORRESPONDENCE DATA**

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NAME OF SUBMITTER:	REGINA BAKER
SIGNATURE:	/REGINA BAKER/
DATE SIGNED:	06/02/2017

**Total Attachments: 2** 

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PATENT 504395978 REEL: 042578 FRAME: 0719

## COMBINED DECLARATION AND ASSIGNMENT UNDER 37 CFR 1.63(e)

### DECLARATION

As a below-named inventor, I hereby declare that this declaration is directed to United States Patent Application entitled:

# CIRCUIT FOR GENERATING BIAS CURRENT FOR READING OTP CELL AND CONTROL METHOD THEREOF

The above-identified patent application was made or was authorized to be made by me.

I believe I am the original inventor or an original joint inventor of a claimed invention in the above-identified application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

I have reviewed and understand the contents of the above-identified application, including the claims.

I am aware of the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information that became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

#### ASSIGNMENT

For valuable consideration, the receipt and sufficiency of which is hereby acknowledged, as a below-named assignor, I hereby assign to

MagnaChip Semiconductor, Ltd. 215, Daesin-ro, Heungdeok-gu, Cheongju-si, Chungcheongbuk-do, 361-725 Republic of Korea,

and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title, and interest throughout the world in the inventions and improvements that are the subject of the application identified in the above declaration, which is United States Patent Application Number 15/611,645 filed on June 1, 2017

I authorize and request the attorneys appointed in the above-identified application to hereafter complete this Assignment by inserting above the application number and the filing date of the above-identified application when known, and to correct any typographical errors that may be discovered in this Assignment.

This Assignment includes the above-identified application, any and all United States and foreign patents, utility models, and design registrations granted for any of the inventions and improvements that are the subject of the above-identified application, and the right to claim priority based on the filing date of the above-identified application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and I authorize the Assignee to apply in all countries in my name or in its own name for patents, utility models, design registrations, like rights of exclusion, and inventors' certificates for any of the inventions and improvements that are the subject of the above-identified application; and I agree for myself and my

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# COMBINED DECLARATION AND ASSIGNMENT UNDER 37 CFR 1.63(e)

heirs, legal representatives, and assigns without further compensation to perform such lawful acts and to sign such further applications, Assignments, preliminary statements, and other lawful documents as the Assignee may reasonably request to effectuate fully this Assignment.

In witness whereof, I, as a below-named inventor and assignor, intending to be legally bound, have hereunto affixed my signature on the date indicated below next to my signature.

Inventor's Legal Name	Duk Ju JEONG		
Inventor's Signature	2/R23	Date	2017, Ot. 12
Residence (City, Country)	Seoul, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor, Ltd. 215, Daesin-ro, Heungdeok-gu, Cheongju-si, Chungcheongbuk-do 361-725, Republic of Korea		

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RECORDED: 06/02/2017

**PATENT** REEL: 042578 FRAME: 0721