

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4433160

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	CORRECTION OF THE RECORDATION COVER SHEET OF THE ASSIGNMENT RECORDED AT REEL/FRAME: 018014/0309 ON: 6/29/2006; TO CORRECT THE ASSIGNEE'S NAME TO TROPIAN, INC.

CONVEYING PARTY DATA

Name	Execution Date
TROPIAN, INC.	04/03/2006

RECEIVING PARTY DATA

Name:	MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
Street Address:	20813 STEVENS CREEK BLVD.
City:	CUPERTINO
State/Country:	CALIFORNIA
Postal Code:	95014

PROPERTY NUMBERS Total: 51

Property Type	Number
Patent Number:	4889919
Patent Number:	6219394
Patent Number:	6269135
Patent Number:	6112071
Patent Number:	5952895
Patent Number:	6140882
Patent Number:	6377784
Patent Number:	6864668
Patent Number:	6094101
Patent Number:	6198347
Patent Number:	6215355
Patent Number:	6567491
Patent Number:	6366177
Patent Number:	6650711
Patent Number:	6636112
Patent Number:	6462627
Patent Number:	6751265
Patent Number:	6323731
Patent Number:	6734724

PATENT

Property Type	Number
Patent Number:	6522192
Patent Number:	6686806
Patent Number:	6724177
Patent Number:	6528975
Patent Number:	6969984
Patent Number:	6850736
Patent Number:	6690233
Patent Number:	6724830
Patent Number:	6983025
Patent Number:	7010276
Patent Number:	6356155
Patent Number:	6580329
Patent Number:	7027545
Patent Number:	7116728
Patent Number:	6587018
Application Number:	09874458
Application Number:	09919696
Patent Number:	7071792
Patent Number:	6690215
Patent Number:	6867574
Patent Number:	6653896
Patent Number:	6624695
Patent Number:	7099688
Patent Number:	7054385
Patent Number:	7020230
Patent Number:	7127226
Patent Number:	7099635
Patent Number:	7012984
Patent Number:	6760572
Application Number:	09362670
Application Number:	09624574
Application Number:	60719991

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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Address Line 4: WASHINGTON, D.C. 20001

ATTORNEY DOCKET NUMBER: 043890-0010

NAME OF SUBMITTER: TAKASHI SAITO

SIGNATURE: /Takashi Saito/

DATE SIGNED: 05/26/2017

Total Attachments: 9

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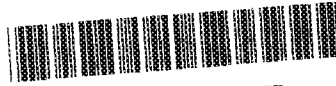
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07-03-2006



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U.S. Department of Commerce
Patent and Trademark Office
PATENT
RECORDATION SECTION

**RECORDATION FORM COVER SHEET
PATENTS ONLY**

TO: The Commissioner of Patents and Trademarks: Please record the attached original document(s) or copy(ies).

Submission Type

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Resubmission (Non-Recordation)
Document ID#

Correction of PTO Error
Reel # Frame #

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Reel # Frame #

Conveyance Type

Assignment Security Agreement

License Change of Name

Merger Other

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Departmental File Secret File

Conveying Party(ies)

Mark if additional names of conveying parties attached

Name (line 1) Execution Date
Month Day Year

Name (line 2)

Second Party

Name (line 1)

Name (line 2)

Execution Date
Month Day Year

Receiving Party

Mark if additional names of receiving parties attached

Name (line 1) If document to be recorded
is an assignment and the
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domiciled in the United
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Name (line 2)

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Domestic Representative Name and Address

Enter for the first Receiving Party only.

Name

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06/30/2006 DBYRNE 00000138 09362670
01 FC:8021 2040.00 DP

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Mail documents to be recorded with required cover sheet(s) information to:
Commissioner of Patents and Trademarks, Box Assignments, Washington, D.C. 20231

PATENT
REEL: 042588 FRAME: 05429

Correspondent Name and Address

Area Code and Telephone Number

Name

Address (line 1)

Address (line 2)

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Pages Enter the total number of pages of the attached conveyance document including any attachments.

#

Application Number(s) or Patent Number(s)

Mark if additional numbers attached

Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).

Patent Application Number(s)

Patent Number(s)

<input type="text" value="09/362,670"/>	<input type="text" value="09/919,696"/>	<input type="text" value="10/040,535"/>
<input type="text" value="09/624,574"/>	<input type="text" value="09/865,409"/>	<input type="text" value="10/094,104"/>
<input type="text" value="09/874,458"/>	<input type="text" value="10/013,209"/>	<input type="text" value="60/719,991"/>

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<input type="text"/>	<input type="text"/>	<input type="text"/>
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If this document is being filed together with a new Patent Application, enter the date the patent application was signed by the first named executing inventor.

Patent Cooperation Treaty (PCT)

Enter PCT application number only if a U.S. Application Number has not been assigned.

PCT <input type="text"/>	PCT <input type="text"/>	PCT <input type="text"/>
PCT <input type="text"/>	PCT <input type="text"/>	PCT <input type="text"/>

Number of Properties

Enter the total number of properties involved. #

Fee Amount

Fee Amount for Properties Listed (37 CFR 3.41): \$

Method of Payment: Enclosed Deposit Account

Deposit Account (Enter for payment by deposit account or if additional fees can be charged to the account.)

Deposit Account Number: #

Authorization to charge additional fees: Yes No

Statement and Signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.

J. Davis Gilmer

Name of Person Signing

Signature

06/23/06

Date

Attachment to Recordation Form Cover Sheet

Additional Patent Application Numbers:

Application No.	Application No.	Application No.
08/947,027	09/648,914	09/833,967
09/006,938	09/661,167	09/834,247
09/027,954	09/684,496	09/852,818
09/027,742	09/684,497	09/865,972
09/197,523	09/688,269	09/942,449
09/247,095	09/738,094	09/942,448
09/247,097	09/738,114	09/942,484
09/268,731	09/738,691	10/037,870
09/362,880	09/746,257	09/999,090
09/416,865	09/746,530	09/992,049
09/419,707	09/746,249	09/997,743
09/495,891	09/794,542	10/040,534
09/585,591	09/834,056	10/095,738
09/637,269	09/834,024	10/115,298

PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT (this "Patent Assignment") dated as of April 5, 2006 ("Effective Date"), is made by and between Tropicana, Inc., a California corporation ("Assignor"), and Matsushita Electric Industrial Co., Ltd., a corporation organized under the laws of Japan ("Assignee").

WHEREAS, Assignor and Assignee have entered into an Assignment of Intellectual Property, executed on even date herewith, pursuant to which Assignor has agreed to assign all of its patent rights to Assignee.

NOW, THEREFORE, for good and valuable consideration, including the promises and covenants set forth in the Assignment of Intellectual Property, the parties agree as follows:

1. Patents.

"Patents" shall mean the patents and patent applications listed on Attachment 1 attached hereto, as well as any reexaminations, extensions and reissues thereof and any divisionals, continuations and continuation-in-parts and any other applications or patents that claim priority therefrom, including, without limitation, any corresponding foreign patents and applications.

2. Assignment.

Assignor hereby assigns, transfers, sells and conveys to Assignee all of its rights, title and interest in and to the Patents, and all rights, claims and privileges pertaining to the Patents, including, without limitation, rights to the underlying inventions, the right to sue and recover damages for past, present and future infringement thereof, and the right to prosecute and maintain the Patents.

[THE REMAINDER OF THIS PAGE IS INTENTIONALLY LEFT BLANK]

IN WITNESS WHEREOF, the parties have caused this Patent Assignment to be executed as of the date set forth below.

ASSIGNOR:

TROPIAN, INC.

By: 

Name: TIM UNGAR
Title: PRESIDENT + CEO
Date: MARCH 3, 2006

ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL
CO., LTD.

By: _____

Name:
Title:
Date:

[SIGNATURE PAGE TO PATENT ASSIGNMENT]

IN WITNESS WHEREOF, the parties have caused this Patent Assignment to be executed as of the date set forth below.

ASSIGNOR:

TROPIAN, INC.

By: _____

Name:

Title:

Date:

ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL
CO., LTD.

By: *Susumu Koike*

Name: *Susumu Koike*

Title: *Vice President, Member of the Board*

Date: *April 4, 2006*

[SIGNATURE PAGE TO PATENT ASSIGNMENT]

Title	Patent No.	Issue Date	Serial No.	Filing Date	Inventors
PLL Noise Smoothing Using Dual-Modulus Interleaving	-	-	09/362,670	07/29/99	Sander, Brian; McCune, Earl W.
Direct Digital Frequency Synthesis Enabling Spur Elimination	-	-	09/624,574	07/24/00	Sander, Wendell B.
Circuit for Compensation Against Backgating	-	-	09/874,458	06/04/01	Judkins, James G.
Digital Phase Discrimination Based on Frequency Sampling	-	-	09/919,696	07/31/01	Sander, Wendell B.
Quadrature Alignment in Communications Receivers	-	-	09/865,409	05/25/01	McCune, Earl W.
Combined Low-IF/Direct Down Conversion Baseband Architecture for 3G GSM/WCDMA Receivers	-	-	10/013,209	12/07/01	Wilson, Duane
Twin-T Dual Notch Filter	-	-	10/040,535	12/28/01	Toison, Nigel J.
High-Efficiency Modulating RF Amplifier	-	-	10/094,104	03/07/02	McCune, Earl W.
APPARATUS AND METHOD FOR MULTI-PHASE DIGITAL SAMPLING	-	-	60/719,991	09/23/05	Sander, Wendell B.
Digital Frequency Sampling and Discrimination	6,219,394	04/17/01	08/947,027	10/08/97	Sander, Wendell B.
Digital Phase Discrimination Based on Frequency Sampling	6,269,135	07/31/01	09/006,938	01/14/98	Sander, Wendell B.
Direct Digital Synthesis of Precise, Stable Angle Modulated RF Signal	5,952,895	09/14/99	09/027,954	02/23/98	McCune, Earl W.; Sander, Wendell B.
Quadrature-Free RF Receiver for Directly Receiving Angle Modulated Signal	6,112,071	08/29/00	09/027,742	02/23/98	McCune, Earl W.
Phase Lock Loop Enabling Smooth Loop Bandwidth Switching	6,140,882	10/31/00	09/197,523	11/23/98	Sander, Brian
High-Efficiency Modulation RF Amplifier	6,377,784	04/23/02	09/247,095	02/09/99	McCune, Earl W.
High-Efficiency Amplifier Output Level and Burst Control	6,864,668	03/08/05	09/247,097	02/09/99	McCune, Earl W.; Sander, Wendell B.
Direct Digital Frequency Synthesis Enabling Spur Elimination	6,094,101	07/25/00	09/268,731	03/17/99	Sander, Wendell B.; Sander, Brian
Driving Circuits for Switch Mode RF Power Amplifiers	6,198,347	03/06/01	09/362,880	07/29/99	Sander, Wendell B.; McCune, Earl W.; Meck, Ronald A.
Constant Impedance for Switchable Amplifier with Power Control	6,215,355	04/10/01	09/416,865	10/13/99	Meck, Ronald A.; McCune, Earl W.; Burns

Title	Patent No.	Issue Date	Serial No.	Filing Date	Inventors
High Efficiently Line Driver for High Crest-Factor Signals Such as DMT/ADSL Signals	6,567,491	05/20/03	09/419,707	10/14/99	McCune, Earl W.; Sander, Wendell B.
High Efficiency Power Modulators	6,366,177	04/02/02	09/495,891	02/02/00	McCune, Earl W.; Sander, Wendell B.
Quadrature Modulation with Reduced Phase-Error Distortion	6,650,711	11/18/03	09/585,591	06/02/00	Booth, Richard W. D.
High-Efficiency Modulating RF Amplifier	6,636,112	10/21/03	09/637,269	08/10/00	McCune, Earl W.
Oscillator Circuit Having Reduced Phase Noise	6,462,627	10/08/02	09/648,914	08/25/00	Lee, Jerold
Method and System of Amplitude Modulation Using Dual/Split Channel Unequal Amplification	6,751,265	06/15/04	09/661,167	09/13/00	Schell, Stephen V.; Sander, Wendell B.; McCune, Earl W.
Variable Bias Control for Switch Mode RF Amplifier	6,323,731	11/27/01	09/684,496	10/06/00	McCune, Earl W.
Power Control and Modulation of Switched-Mode Power Amplifiers with One or More Stages	6,734,724	05/11/04	09/684,497	10/06/00	Schell, Stephen V.; Sander, Wendell B.; Meck, Ronald A.; Bayruns, Robert J.
Boost Doubler Circuit	6,522,192	02/18/03	09/688,269	10/11/00	Sander, Wendell B.
Ring VCO Based on RC Timing	6,686,806	02/03/04	09/738,094	12/14/00	Dufour, Yves
Method and Apparatus for Accurate Measurement of Communication Signals	6,724,177	04/20/04	09/738,114	12/14/00	Schell, Stephen V.
Saturation Prevention and Amplifier Distortion Reduction	6,528,975	03/04/03	09/738,691	12/15/00	Sander, Wendell B.
Method and Apparatus for Reception Quality Indication in Wireless Communication	6,850,736	02/01/05	09/746,257	12/21/00	McCune, Earl W.
Efficient, Precise RF Modulation Using Multiple Amplifier Stages	6,690,233	02/10/04	09/746,530	12/21/00	Sander, Wendell B.
Direct Phase and Frequency Modulation	6,969,984	11/29/05	09/746,249	12/21/00	McCune, Earl W.
High Efficiently Line Driver for High Crest-Factor Signals such as DMT/ADSL signals	6,724,830	04/20/04	09/794,542	02/26/01	Do, Gary L.; McCune, Earl W.; Sander, Wendell B.
Multi-Band Amplifier Having Multi-Tap RF Choke	6,356,155	03/12/02	09/834,056	04/11/01	Judkins, James G.
Communications Signal Amplifiers Having Independent Power Control and Amplitude Modulation	7,010,276	03/07/06	09/834,024	04/11/01	Sander, Wendell B.; Meck, Ronald A.; McCune, Earl W.
High Quality Power Ramping in a Communications Transmitter	6,983,025	01/03/06	09/833,967	04/11/01	Schell, Stephen V.
PLL Bandwidth Switching	6,560,329	06/17/03	09/834,247	04/11/01	Sander, Wendell B.

PATENT

REEL: 042588 FRAME: 0549

Title	Patent No.	Issue Date	Serial No.	Filing Date	Inventors
Data Sampler for Digital Frequency/Phase Determination	7,027,545	04/11/06	09/852,818	05/09/01	Sander, Brian
Notch Filter and Method	6,587,018	07/01/03	09/865,972	05/25/01	Meck, Ronald A.; McCune, Earl W.; Twitchell, Edwin R.
Sigma-Delta-Based Frequency Synthesis	6,690,215	02/10/04	09/942,449	08/29/01	McCune, Earl W.; Sander, Wendell B.
Method and Apparatus for Impedance Matching in an Amplifier Using Lumped and Distributed Inductance	09/942,448	07/04/06	09/942,448	08/29/01	Meck, Ronald A.
Power Supply Processing for Power Amplifiers	6,781,452	08/24/04	09/942,484	08/29/01	Cioffi, Kenneth R.; Tolson, Nigel J.; McCune, Earl W.
Reduction of Average-to-Minimum Power Ratio in Communications Signals	7,054,385	05/30/06	10/037,870	10/22/01	Booth, Richard W. D.; Schell, Stephen V.; Biedka, Thomas E.; Liang, Paul Cheng-Po
Waveform Preshaping for Efficiency Improvements in DC to RF Conversion	6,624,695	09/23/03	09/999,090	10/31/01	Sevic, John F.; Salam, Khan M.
Switch Mode Power Supply and Driving Method for Efficient RF Amplification	6,867,574	03/15/05	09/992,049	11/21/01	Silic, Bojan
Differential RF/Microwave Power Amplifier Using Independent Synchronized Polar Modulators	6,653,896	11/25/03	09/997,743	11/30/01	Sevic, John F.; Sander, Schell, Stephen V.
Frequency Synthesizer for Dual Mode Receiver	7,020,230	03/28/06	10/040,534	12/28/01	Tolson, Nigel J.
PLL Noise Smoothing Using Dual-Modulus Interleaving	7,012,984	03/14/06	10/095,738	03/11/02	Sander, Brian; McCune, Earl W.
Method and Apparatus for Combining Two AC Waveforms	6,760,572	07/06/04	10/115,298	04/02/02	Noori, Basim