

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4446190

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
SILICON VALLEY BANK	05/13/2016

RECEIVING PARTY DATA

Name:	TEMPO SEMICONDUCTOR, INC.
Street Address:	8627 N. MO-PAC EXPRESSWAY
Internal Address:	SUITE 130
City:	AUSTIN
State/Country:	TEXAS
Postal Code:	78759

PROPERTY NUMBERS Total: 22

Property Type	Number
Patent Number:	5563553
Patent Number:	5566101
Patent Number:	5592165
Patent Number:	6226663
Patent Number:	6608902
Patent Number:	6654900
Patent Number:	6885900
Patent Number:	6901127
Patent Number:	7302067
Patent Number:	7358814
Patent Number:	7579832
Patent Number:	7702058
Patent Number:	7760016
Patent Number:	7809144
Patent Number:	7813823
Patent Number:	7916875
Patent Number:	7966085
Patent Number:	8160272
Patent Number:	8233639

PATENT

Property Type	Number
Patent Number:	8238577
Patent Number:	8386758
Patent Number:	8970298

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: (512) 695-2866
Email: alan@agrlegal.com
Correspondent Name: ALAN D. GREEN
Address Line 1: 1607 W. 9TH ST.
Address Line 4: AUSTIN, TEXAS 78703

NAME OF SUBMITTER:	ALAN D GREEN
SIGNATURE:	/Alan D Green/
DATE SIGNED:	06/06/2017

Total Attachments: 5

source=Tempo Semiconductor Release of Security Interest Signed#page1.tif
source=Tempo Semiconductor Release of Security Interest Signed#page2.tif
source=Tempo Semiconductor Release of Security Interest Signed#page3.tif
source=Tempo Semiconductor Release of Security Interest Signed#page4.tif
source=Tempo Semiconductor Release of Security Interest Signed#page5.tif

RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY

THIS RELEASE OF SECURITY INTEREST IN INTELLECTUAL PROPERTY (this “**Release**”) is filed as of the 6th day of June, 2017, by TEMPO SEMICONDUCTOR, INC., a Delaware corporation (the “**Assignee**”), as authorized by the “Pay-Off Letter” dated effective May 13, 2016 pursuant to which SILICON VALLEY BANK (the “**Assignor**”) authorized the Assignee to prepare and file the Release and related documents.

WHEREAS, in connection with certain security agreements entered into on or about February 17, 2015 (the “**Agreement**”), the Assignee granted to the Assignor, for the benefit of the Assignor, a security interest in all of the intellectual property identified on Exhibit A attached hereto (collectively, the “Intellectual Property”), and pledged and mortgaged (but did not transfer title to) the Intellectual Property to the Assignor; and

WHEREAS, the aforementioned security interest in the Intellectual Property has terminated and has been released by Assignor, as evidenced in the “Pay-Off Letter.”

NOW, THEREFORE:

The Assignor’s security interest in the Intellectual Property has been terminated and released, and the Assignor has assigned and transferred to the Assignee, without any representation, warranty, or recourse, the Assignor's entire right, title, and interest in and to the Intellectual Property, effective as of May 13, 2016.

IN WITNESS WHEREOF, the Assignee has caused this Release to be duly executed as of the first date written above.

ASSIGNEE:

TEMPO SEMICONDUCTOR, INC.

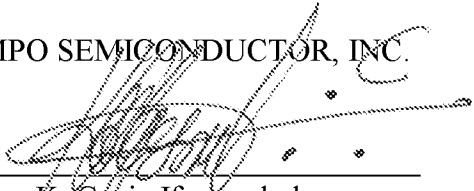
By: 
K. Gozie Ifesmachukwu,
Chief Executive Officer

EXHIBIT A**Patents**

Description	Application Number	Registration Number
Method and Apparatus for a Controlled Oscillation That May be Used in a Phase Locked Loop		5,563,553 (10/08/1995)
Method and Apparatus for a Finite Impulse Response Filter Processor		5,566,101 (10/15/1996)
Method and Apparatus for an Oversampled Digital to Analog Converter		5,592,165 (01/07/1997)
Method and Apparatus for Overflow Detection in a Decimation Filter		6,226,663 (05/01/2001)
Stereo Signal Separation Circuit and Application Thereof		6,608,902 (08/19/2003)
Method and Apparatus for Producing Multiple Clock Signals Having Controlled Duty Cycles by Controlling Clock Multiplier Delay Elements		6,654,900 (11/25/2003)
Method and Apparatus for Providing Multiple Channel Audio in a Computing System		6,885,900 (04/26/2005)
Method and Apparatus for Data Recovery		6,901,127 (05/31/2005)

EXHIBIT A (Continued)**Patents**

Description	Application Number	Registration Number
Audio System for a Computer		7,302,067 (11/27/2007)
Differential Amplifier and Methods for Use Therewith		7,358,814 (04/15/2008)
Cross-Drive Impedance Measurement Circuits for Sensing Audio Loads on Codec Channels		7,579,832 (08/25/2009)
Data Recovery Method and Module		7,702,058 (04/20/2010)
Anti-Pop Circuits and Methods for Audio Amplifiers Using Variable Resistors		7,760,016 (07/20/2010)
Audio Input-Output Module, Plug-In Detection Module and Methods for Use Therewith		7,809,144 (10/05/2010)
Computer Audio System and Method		7,813,823 (10/12/2010)
Audio Input-Output Module, Plug-In Device Detection Module and Methods for Use Therewith		7,916,875 (03/29/2011)

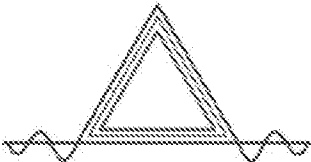
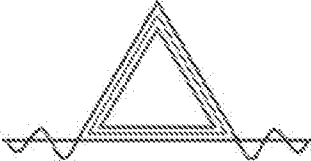
EXHIBIT A (Continued)

Patents

Description	Application Number	Registration Number
Audio Source System and Method		7,966,085 (06/21/2011)
Audio Output Circuits Having Ramped Attenuation Circuits that Inhibit Pop Disturbances When Audio Sources are Switched		8,160,272 (04/17/2012)
Audio Codec Producing a Tone Control Output		8,233,639 (07/31/2012)
Audio System with Tone Controller for Use in a Computer		8,238,577 (08/07/2012)
System and Method of Enabling Codec Device Features		8,386,758 (02/26/2013)
Digitally Controlled Power Supplies for an Audio Amplifier		8,970,298 (03/03/2015)

EXHIBIT A (Continued)

Trademarks

Description	Serial Number	Registration Number
DDX (& design)		4822052
		4817024
DDX (& design)	86/330,284 (July 7, 2014)	
	86/330,223 (July 7, 2014)	