

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT4521783

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
RUNZI CHANG	05/12/2017
WINSTON LEE	05/26/2017
PETER LEE	05/22/2017
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	MARVELL SEMICONDUCTOR, INC.
<b>Street Address:</b>	5488 MARVELL LANE
<b>City:</b>	SANTA CLARA
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95054
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	15611628
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(408)222-2755
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	408-222-2500
<b>Email:</b>	lgalang@marvell.com
<b>Correspondent Name:</b>	KELVIN VIVIAN
<b>Address Line 1:</b>	5488 MARVELL LANE
<b>Address Line 4:</b>	SANTA CLARA, CALIFORNIA 95054
<b>ATTORNEY DOCKET NUMBER:</b>	MP10062
<b>NAME OF SUBMITTER:</b>	KELVIN VIVIAN
<b>SIGNATURE:</b>	/Kelvin Vivian/
<b>DATE SIGNED:</b>	07/26/2017
<b>Total Attachments: 4</b>	
source=MP10062_(EXECUTED)_Assignment_INV_to_MSI#page1.tif	
source=MP10062_(EXECUTED)_Assignment_INV_to_MSI#page2.tif	
source=MP10062_(EXECUTED)_Assignment_INV_to_MSI#page3.tif	
source=MP10062_(EXECUTED)_Assignment_INV_to_MSI#page4.tif	

## ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, each of us, the undersigned,

Ranzi Chang  
Winston Lee  
Peter Lee

who have created a certain invention for which an application for United States Letters Patent was executed by each of us concurrently herewith and entitled:

## INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING THRESHOLD VOLTAGES OF TRANSISTORS

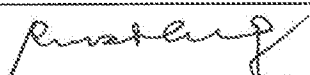
Does hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said application and to any and all inventions described in said application for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Does hereby further sell, assign and transfer to Marvell Semiconductor, Inc., its successors, assigns, and legal representatives, the full and exclusive right to United States Provisional Patent Applications(s) No(s). 62/344,596, filed June 2, 2016, entitled Method to Decouple the FinFET SRAM Transistors Driving Currents Without Changing Process Complexity, and to any and all inventions described in said provisional patent application(s) for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

Full Name of First Inventor:	
Runzi Chang	
Inventor's Signature 	Date: Month/Day/Year 5/12/17

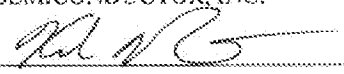
Full Name of Second Inventor:	
Winston Lee	
Inventor's Signature	Date: Month/Day/Year

Full Name of Third Inventor:	
Peter Lee	
Inventor's Signature	Date: Month/Day/Year

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By:

  
Kelvin Vivian

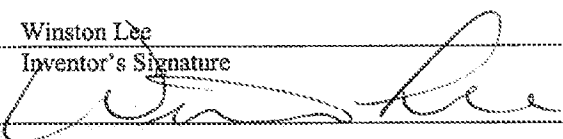
Director, Intellectual Property

Date:

05/30/17

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

Full Name of First Inventor:	
Runzi Chang	
Inventor's Signature	Date: Month/Day/Year

Full Name of Second Inventor:	
Winston Lee	
Inventor's Signature 	Date: Month/Day/Year 5/26/17

Full Name of Third Inventor:	
Peter Lee	
Inventor's Signature	Date: Month/Day/Year

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By:

Kelvin Vivian  
Director, Intellectual Property

Date: \_\_\_\_\_

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

Full Name of First Inventor:	
Runzi Chang	
Inventor's Signature	Date: Month/Day/Year

Full Name of Second Inventor:	
Winston Lee	
Inventor's Signature	Date: Month/Day/Year

Full Name of Third Inventor:	
Peter Lee	
Inventor's Signature <i>Peter Lee</i>	Date: Month/Day/Year 5/22/2017

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By:

Kelvin Vivian  
Director, Intellectual Property

Date: