## 504547321 09/14/2017

# **PATENT ASSIGNMENT COVER SHEET**

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4594029

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE

#### **CONVEYING PARTY DATA**

Name	Execution Date
ROBUST CHIP INC.	09/13/2017

### **RECEIVING PARTY DATA**

Name:	DEFENSE THREAT REDUCTION AGENCY, US DOD
Street Address:	8728 JOHN J KINGMAN ROAD
Internal Address:	STOP 6201
City:	FORT BELVOIR
State/Country:	VIRGINIA
Postal Code:	66020

## **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	13692800

#### **CORRESPONDENCE DATA**

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NAME OF SUBMITTER:	ELLEN KLANN
SIGNATURE:	/ellen klann/
DATE SIGNED:	09/14/2017

**Total Attachments: 1** 

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PATENT 504547321 REEL: 043587 FRAME: 0579

## Confirmatory License Agreement

This instrument confirms to the United States Government, as represented by the Defense Threat Reduction Agency ("DTRA"), a non-exclusive, nontransferable, irrevocable, paid-up license to practice or have practiced for or on behalf of the United States the below invention described in any patent application and in any and all divisions, continuations, and continuations in part, and in any and all patents and re-issues granted thereon, throughout the world.

This instrument also confirms all other rights acquired by the Government by reason of the identified agreement. This instrument grants the licensee the irrevocable power to inspect and make copies of the identified patent application.

Invention Title: Soft Error Hard Electronics Layout Arrangement and Logic Cells

Inventor(s) and Affiliation: Klas Lilja, Robust Chip Inc.

Patent Application Serial Number: 13/692,800

Patent Serial Number: US 9,083,341 B2

Patent Application Filing Date: April 19, 2009 (priority date)

(Signature)

Foreign Applications Filed/Intended: Yes.

Applicable Agreement Number and Agency: HDTRA1-10-C-0078, the Defense Threat Reduction Agency

Signed this 13 day of September, 2017

By Klas Lilja

(Printed Name of Licensor Official)

Title: CEO

For: Robust Chip Inc.

(Organization)

At: 7901 Stoneridge Drive, ST226, Pleasanton, CA, 94588 (Business Address)

PATENT REEL: 043587 FRAME: 0580

**RECORDED: 09/14/2017**