

<b>PATENT ASSIGNMENT COVER SHEET</b>
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EPAS ID: PAT4602903

<b>SUBMISSION TYPE:</b>	CORRECTIVE ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	Corrective Assignment to correct the ERRONEOUSLY FILED ASSIGNMENT DOCUMENT previously recorded on Reel 042356 Frame 0290. Assignor(s) hereby confirms the CORRECTIVE ASSIGNMENT IS A DUPLICATE OF REEL 042356 FRAME 0200 AND IS FILED FOR THE PURPOSE OF CORRECTION ONLY.

**CONVEYING PARTY DATA**

Name	Execution Date
YASUTOSHI AIBARA	02/14/2002
HIROKI NAKAJIMA	02/14/2002
EIKI IMAIZUMI	02/15/2002
TATSUJI MATSUURA	02/14/2002

**RECEIVING PARTY DATA**

<b>Name:</b>	HITACHI, LTD.
<b>Street Address:</b>	6, KANDA SURUGADAI 4-CHOME, CHIYODA-KU
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>Name:</b>	HITACHI ULSI SYSTEMS CO., LTD.
<b>Street Address:</b>	22-1, JOSUIHONCHO 5-CHOME, KODAIRA-SHI
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>Name:</b>	HITACHI TOHBU SEMICONDUCTOR, LTD.
<b>Street Address:</b>	1-1, NISHIYOKOTEMACHI, TAKASAKI-SHI
<b>City:</b>	GUNMA
<b>State/Country:</b>	JAPAN

**PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	11000415

**CORRESPONDENCE DATA**

**Fax Number:** (703)684-1157

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

**Phone:** 7036841120

**Email:** escotti@mmiplaw.com

**Correspondent Name:** MATTINGLY & MALUR, PC

**PATENT**

**Address Line 1:** 1800 DIAGONAL ROAD  
**Address Line 2:** SUITE 210  
**Address Line 4:** ALEXANDRIA, VIRGINIA 22314

**ATTORNEY DOCKET NUMBER:** T&A-GEN/Z-1704(USMA) CRR

**NAME OF SUBMITTER:** JOHN R. MATTINGLY

**SIGNATURE:** /John R. Mattingly/

**DATE SIGNED:** 09/19/2017

**Total Attachments: 3**

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<b>PATENT ASSIGNMENT COVER SHEET</b>
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Electronic Version v1.1  
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EPAS ID: PAT4411818

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
BUNSHI KURATOMI	08/07/2003
FUKUMI SHIMIZU	08/07/2003
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	RENESAS TECHNOLOGY CORP.
<b>Street Address:</b>	4-1, MARUNOUCHI 2-CHOME, CHIYODA-KU
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>Name:</b>	RENESAS EASTERN JAPAN SEMICONDUCTOR, INC.
<b>Street Address:</b>	3-2, FUJIHASHI 3-CHOME, OME-SHI
<b>City:</b>	TOKYO
<b>State/Country:</b>	JAPAN
<b>PROPERTY NUMBERS Total: 3</b>	
<b>Property Type</b>	<b>Number</b>
<b>Patent Number:</b>	7681308
<b>Patent Number:</b>	7351946
<b>Patent Number:</b>	8117742
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(703)684-1157
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	7036841120
<b>Email:</b>	escotti@mmlplaw.com
<b>Correspondent Name:</b>	MATTINGLY & MALUR, PC
<b>Address Line 1:</b>	1800 DIAGONAL ROAD
<b>Address Line 2:</b>	SUITE 210
<b>Address Line 4:</b>	ALEXANDRIA, VIRGINIA 22314
<b>ATTORNEY DOCKET NUMBER:</b>	T&A-GENERAL/Z-1704(USMA)R
<b>NAME OF SUBMITTER:</b>	JOHN R. MATTINGLY
<b>SIGNATURE:</b>	/John R. Mattingly/

<b>DATE SIGNED:</b>	05/11/2017
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<b>Total Attachments: 1</b> source=(r) Assignment - Inv to RenTech and RenEas#page 1.tif
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ASSIGNMENT  
( 譲渡証 )

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., corporations organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan ; 22-1, Josuihoncho 5-chome, Kodaira-shi, Tokyo, Japan and 1-1, Nishiyokotemachi, Takasaki-shi, Gunma, Japan, respectively, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., their successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd., their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD., Hitachi ULSI Systems Co., Ltd. and Hitachi Tohbu Semiconductor, Ltd.

Signed on the date(s) indicated aside signatures:

INVENTOR(S)

(発明者フルネームサイン)

Date Signed

(署名日)

- |   |                         |
|---|-------------------------|
| 1) <u>Yasutoshi Aibara</u> (Yasutoshi AIBARA) | <u>14/February/2002</u> |
| 2) <u>Hiroki Nakajima</u> (Hiroki NAKAJIMA)   | <u>14/February/2002</u> |
| 3) <u>Eiki Imaizumi</u> (Eiki IMAIZUMI)       | <u>15/February/2002</u> |
| 4) <u>Tatsuji Matsuura</u> (Tatsuji MATSUURA) | <u>14/February/2002</u> |
| 5) _____                                      | _____                   |
| 6) _____                                      | _____                   |
| 7) _____                                      | _____                   |
| 8) _____                                      | _____                   |
| 9) _____                                      | _____                   |
| 10) _____                                     | _____                   |