

<b>PATENT ASSIGNMENT COVER SHEET</b>
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 Stylesheet Version v1.2

EPAS ID: PAT4606537

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT

**CONVEYING PARTY DATA**

Name	Execution Date
KONINKLIJKE PHILIPS ELECTRONICS N.V.	09/28/2006

**RECEIVING PARTY DATA**

<b>Name:</b>	PHILIPS SEMICONDUCTORS INTERNATIONAL B.V.
<b>Street Address:</b>	HIGH TECH CAMPUS 60
<b>City:</b>	EINDHOVEN
<b>State/Country:</b>	NETHERLANDS
<b>Postal Code:</b>	5656 AG

**PROPERTY NUMBERS Total: 406**

Property Type	Number
Application Number:	08372423
Application Number:	08927479
Application Number:	08927831
Application Number:	08932310
Application Number:	08934831
Application Number:	08935592
Application Number:	08939550
Application Number:	08941898
Application Number:	08944463
Application Number:	08951880
Application Number:	08957671
Application Number:	08959346
Application Number:	08959218
Application Number:	08962593
Application Number:	08962154
Application Number:	08966224
Application Number:	08968374
Application Number:	08974203
Application Number:	08974043
Application Number:	08976190

PATENT

<b>Property Type</b>	<b>Number</b>
Application Number:	08984483
Application Number:	09167655
Application Number:	08988593
Application Number:	08988711
Application Number:	08993244
Application Number:	08995471
Application Number:	08995679
Application Number:	08995651
Application Number:	08995468
Application Number:	08995469
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Application Number:	09539911
Application Number:	09007117
Application Number:	10003130
Application Number:	09009747
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Application Number:	09009749
Application Number:	09010217
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Application Number:	09014832
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Application Number:	09016752
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Application Number:	09023638
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Application Number:	09042965
Application Number:	09044544
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<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	09054112
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Application Number:	09649678
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Application Number:	09690267
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Application Number:	09730656
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Application Number:	09737606
Application Number:	11412596
Application Number:	09738781
Application Number:	09739500
Application Number:	09741723
Application Number:	10390946
Application Number:	09747121
Application Number:	09759177
Application Number:	09780739
Application Number:	09781504
Application Number:	09781473
Application Number:	09782664
Application Number:	09784421
Application Number:	09788691
Application Number:	10923284
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Application Number:	09795002
Application Number:	09797087
Application Number:	09804004
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Application Number:	09810143
Application Number:	09811638
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Application Number:	09819280
Application Number:	09822729
Application Number:	09825280
Application Number:	09828106
Application Number:	09829860
Application Number:	09837947
Application Number:	09838999
Application Number:	09839321

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Application Number:	09847220
Application Number:	09847219
Application Number:	09851442
Application Number:	09851964
Application Number:	09852018
Application Number:	09854402
Application Number:	09862291
Application Number:	09864131
Application Number:	09864133
Application Number:	09864144
Application Number:	09864135
Application Number:	09864140
Application Number:	09864145
Application Number:	09867893
Application Number:	09877805
Application Number:	09882083
Application Number:	10635141
Application Number:	09888146
Application Number:	09888459
Application Number:	09891669
Application Number:	10069893
Application Number:	09894089
Application Number:	09897817
Application Number:	09902219
Application Number:	09922416
Application Number:	09923604
Application Number:	09925357
Application Number:	09925344
Application Number:	09925334
Application Number:	09929115
Application Number:	09932106
Application Number:	09932086
Application Number:	09939326
Application Number:	09943719
Application Number:	09946715
Application Number:	09954649
Application Number:	09957128
Application Number:	10462312

Property Type	Number
Application Number:	09968121
Application Number:	09973640
Application Number:	10788057
Application Number:	11445841
Application Number:	12257582
Application Number:	09981471

**CORRESPONDENCE DATA**

**Fax Number:** (512)895-6630

*Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.*

**Email:** Tracy.Buffett@nxp.com

**Correspondent Name:** NXP B.V.

**Address Line 1:** 6501 WILLIAM CANNON DRIVE WEST

**Address Line 4:** AUSTIN, TEXAS 78735

<b>ATTORNEY DOCKET NUMBER:</b>	ROYAL PHILIPS TO PSI
<b>NAME OF SUBMITTER:</b>	TRACY BUFFETT
<b>SIGNATURE:</b>	/Tracy Buffett/
<b>DATE SIGNED:</b>	09/22/2017

**Total Attachments: 110**

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**INTELLECTUAL PROPERTY TRANSFER AND LICENSE AGREEMENT**

*relating to*

**the transfer, assignment and licensing of intellectual property and know-how**

*between*

**Koninklijke Philips Electronics N.V.**

*and*

**Philips Semiconductors International B.V.**

*Dated 28 September 2006*

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**INTELLECTUAL PROPERTY TRANSFER AND LICENSE AGREEMENT**

**THIS AGREEMENT IS MADE BETWEEN:**

- (1) **Koninklijke Philips Electronics N.V.**, a limited liability company incorporated in the Netherlands, with corporate seat in Eindhoven, the Netherlands, and address at Groenewoudseweg 1, Eindhoven, the Netherlands ("Royal Philips"),

and

- (2) **Philips Semiconductors International B.V.**, a limited liability company incorporated in the Netherlands, with corporate seat in Eindhoven, the Netherlands, and address at High Tech Campus 60, Eindhoven, the Netherlands ("PSI"),

**WHEREAS:**

- (A) as part of its disentanglement plan, Royal Philips has decided to disentangle all assets and liabilities constituting the business operations of the Royal Philips semiconductors division to a separate group;
- (B) by this Intellectual Property Transfer and License Agreement the Parties wish to set out the terms and conditions applicable to the assignment and licensing of intellectual property relevant to aforementioned disentanglement.

**IT IS AGREED AS FOLLOWS:**

**1 INTERPRETATION AND DEFINITIONS**

**1.1 Definitions**

When used in this Agreement, the following capitalized terms shall have the meanings set forth below:

**"3D Applications"** shall mean any application that enables a three-dimensional viewer impression of an image when presented on a display (including but not limited to conversion from 2D to 3D, conversion from stereo to 3D, rendering for multiview/3D displays, depth estimation or 3DTV).

**"Agreement"** shall mean this Intellectual Property Transfer and License Agreement together with all annexes, exhibits and other documents referred to herein or therein.

**"Associated Company(ies)"** shall mean any one or more entities, which is (are) directly or indirectly owned or controlled by Royal Philips or PSI, respectively, but any such entity shall only be deemed an Associated Company for the period such ownership or control exists. For the purposes of this definition, (i) Royal Philips or PSI shall be deemed to own and/or to control an entity if more than 50% (fifty per cent) of the voting stock of such entity, ordinarily entitled to vote for the election of directors (or, if there is no such stock, more than 50% (fifty per cent) of the ownership of or control in such entity) is held by and consolidated in the annual accounts of Royal Philips or PSI, respectively, and (ii) PSI and its Associated Companies shall not be deemed Associated Companies of Royal Philips, irrespective of Royal Philips' share in PSI.

**"Business Know-How"** shall mean any Know-How owned by Philips Group-Old, which originated within any entities of Philips Group-Old and that is primarily used or primarily intended to be used within PSI Group as of the Closing Date.

**"Business Software"** shall mean any Software owned by Philips Group-Old, which originated within any entities of Philips Group-Old and that is primarily used or primarily intended to be used within PSI Group as of the Closing Date, but excluding any such Software covered by the IT Separation Agreement entered into by Royal Philips and PSI Group on 28 September 2006.

**"Closing Date"** shall mean 28 September 2006 at 24.00 hours CET.

**"Confidential Information"** shall have the meaning referred to in Clause 10.

**"CTO Council"** shall mean the committee referred to in Clause 4.8.

**"Divested Company"** shall mean any former Associated Company as from the moment it no longer qualifies as an Associated Company of Royal Philips or PSI, respectively, and any former unincorporated business of a party or Associated Company as from the moment it is divested by the relevant party or Associated Company (other than to another Associated Company of the relevant party).

**"Domain Names"** shall mean the registered domain names and applications therefor as listed in Annex 5 to this Agreement.

**"GMR Philips-Patents"** shall mean the Philips-Patents that are listed in Annex 3 to this Agreement.

**"Integral Roadmaps of PSI BUs"** shall mean the technology/product roadmaps for the four full calendar years after the Closing Date of the PSI BUs, as attached to this Agreement as Annex 9B.

**"Intellectual Property"** shall mean Patents, Know-How, Software, the copyrights meant in Clause 6.2, Trademarks and Domain Names.

**"Kits"** shall mean kits consisting of one or more integrated circuits and software.

**"Know-How"** shall mean all technical information, data and documents of whatever nature, including without limitation drawings, specifications, photographs, samples, models, processes, procedures, reports and correspondence, all of which relate to the Products, but excluding any intellectual property rights (including data base rights) therein.

**"Party"** shall mean Royal Philips or PSI and **"Parties"** shall mean Royal Philips and PSI together.

**"Patents"** shall mean any patents, petty patents, provisionals, utility models or applications therefor, including any divisions, continuations, continuations-in-part, re-examinations, renewals and re-issues thereof, in any country of the world.

**"PCM Transfer-Patents"** shall mean the Transfer-Patents that are listed in Annex 1A to this Agreement.

**"Philips Group"** shall mean Royal Philips together with its Associated Companies.

**"Philips Group-Old"** shall mean Philips Group and any entities of PSI Group immediately after the Closing Date.

**"Philips Know-How"** shall mean any Know-How owned by Philips Group-Old, other than Business Know-How, which is available to PSI Group immediately after the Closing Date.

**"Philips-Owned Software"** shall mean any Software owned by Philips Group-Old, other than Business Software, which is used by PSI Group as of the Closing Date, but excluding any such Software covered by the IT Separation Agreement entered into by Royal Philips and PSI Group on 28 September 2006.

**"Philips-Patents"** shall mean any Patents owned or controlled by Philips Group-Old, other than Transfer-Patents, to the extent that they are entitled to the benefit of a filing date prior to the Closing Date and for which Philips is free to grant licenses to third parties without the consent of or accounting to any third party, other than an Associated Company of Royal Philips or an Associated Company of PSI.

**"Products"** shall mean PSI BUs Products, PSI EBs Products and PSI SW Products.

**"PSI Group"** shall mean PSI together with its Associated Companies, and where it concerns the period before the Closing Date PSI together with the Associated Companies of Royal Philips which are (to be) an Associated Company of PSI as of the Closing Date.

**"PSI BUs"** shall mean the Philips Semiconductors' business units referred to as Home, Mobile & Personal, Automotive & Identification and Multi Market Semiconductors as belonging to PSI Group as of the Closing Date.

**"PSI BUs Product(s)"** shall mean any product(s) as listed in the product catalogue attached to this Agreement as Annex 6 as well as proprietary products of the PSI BUs that PSI can reasonably demonstrate were offered for sale to third parties prior to the Closing Date.

**"PSI EBs"** shall mean the Philips Semiconductors' business unit referred to as Emerging Businesses, excluding the Philips Semiconductors' business unit Philips Software, as belonging to PSI Group as of the Closing Date.

**"PSI EBs Product(s)"** shall mean any product(s) as listed in the product catalogue attached to this Agreement as Annex 7 as well as proprietary products of the PSI EBs that PSI can reasonably demonstrate were offered for sale to third parties prior to the Closing Date.

**"PSI SW"** shall mean the part of the Philips Semiconductors' business unit referred to as Philips Software as belonging to PSI Group as of the Closing Date.

**"PSI SW Product(s)"** shall mean any product(s) as listed in the product catalogue attached to this Agreement as Annex 8 as well as proprietary products of PSI SW that PSI can reasonably demonstrate were offered for sale to third parties prior to the Closing Date.

**"Rolling Financial Forecasts of PSI EBs"** or "RFFs of PSI EBs" shall mean the financial forecasts for the six annual quarters of Q2 of 2006 – Q3 of 2007 of the PSI EBs, as attached to this Agreement as Annex 10B.

**"Rolling Financial Forecasts of PSI SW"** or "RFFs of PSI SW" shall mean the financial forecasts for the six annual quarters of Q2 of 2006 – Q3 of 2007 of PSI SW, as attached to this Agreement as Annex 11B.

**"Software"** shall mean (any rights on) a code in any programming language contained in any format, including human and machine-readable format.

**"Third Party Software"** shall mean any Software owned by a third party that is licensed to Philips Group-Old and which is predominantly used by PSI Group as of the Closing Date, but excluding any such Software covered by the IT Separation Agreement entered into by Royal Philips and PSI Group on 28 September 2006.

**"Trademarks"** shall mean the registered trademarks and applications therefor as listed in Annex 4 to this Agreement.

**"Transfer-Patents"** shall mean the Patents that are listed in Annex 1 to this Agreement, any corresponding Patents to the extent that they cover the same inventions as the Patents listed in Annex 1 and any (Patents on the) invention disclosures that are listed in Annex 2 to this Agreement, subject to any changes due to withdrawals and rejections in the normal course of business occurring between the date of compilation of these Annexes and the Closing Date.

**"Transferred Intellectual Property"** shall mean all Intellectual Property (to be) assigned to PSI Group in accordance with the provisions of this Agreement.

## 1.2 References to persons and companies

References to:

- 1.2.1 a "person" include any individual, company, partnership or unincorporated association (whether or not having separate legal personality); and
- 1.2.2 a "company" include any company, corporation or any body corporate, wherever incorporated.

**1.3 Headings and references to Clauses, Annexes, Parts and Paragraphs**

- 1.3.1 Headings have been inserted for convenience of reference only and do not affect the interpretation of any of the provisions of this Agreement.
- 1.3.2 A reference in this Agreement to a "Clause" or "Annex" is to the relevant clause of or annex to this Agreement; to a "Part" is to the relevant part of the relevant Annex; and to a "Paragraph" is to the relevant paragraph of (the relevant Part of) the relevant Annex.

**1.4 References to liabilities and obligations of Royal Philips and PSI**

- 1.4.1 Any reference in this Agreement to a liability or obligation of Royal Philips shall be deemed to incorporate a reference to an obligation on the part of Royal Philips to procure that the relevant liability is discharged or obligation is performed by any of the relevant members of Philips Group, subject to the terms set out in this Agreement.
- 1.4.2 Any reference in this Agreement to a liability or obligation of PSI shall be deemed to incorporate a reference to an obligation on the part of PSI to procure that the relevant liability is discharged or obligation is performed by any of the relevant members of PSI Group, subject to the terms set out in this Agreement.

**1.5 Legal terms**

In respect of any jurisdiction other than the Netherlands, a reference to any Netherlands legal term shall be construed as a reference to the term or concept which most nearly corresponds to it in that jurisdiction.

**1.6 Other references**

- 1.6.1 Whenever used in this Agreement, the words "include", "includes" and "including" shall be deemed to be followed by the phrase "without limitation".
- 1.6.2 Any reference in this Agreement to any gender shall include all genders, and words importing the singular shall include the plural and vice versa.
- 1.6.3 Whenever used in this Agreement in the context of the grant of a license or rights under any Intellectual Property, the words "use" or "used" shall be deemed to include all acts and activities which would (if not for the license) infringe or amount to an actionable misuse of the relevant rights including the use, development, keeping, making, selling, offering for sale, leasing, importing, exporting or otherwise disposing of any product, process or method.

**1.7 Drafting Party**

No provision of this Agreement shall be interpreted against a Party solely as a result of the fact that such Party was responsible for the drafting of such provision, it being acknowledged that the Parties and representatives of the Parties have participated in the drafting and negotiating of this Agreement.

**2 ASSIGNMENT OF TRANSFER-PATENTS TO PSI**

**2.1 Transfer**

Subject to the provisions of this Clause 2, Royal Philips hereby assigns to PSI and

agrees to transfer to PSI or such PSI Group member as designated by PSI and PSI shall procure that such PSI Group member shall accept the transfer of the Transfer-Patents, subject to all prior commitments and prior undertakings vis-à-vis third parties entered into by Philips Group-Old prior to the Closing Date and all arrangements between entities belonging to PSI Group and entities remaining with Philips Group made prior to the Closing Date, and subject to the license retained by Philips Group pursuant to Clauses 3.1 and 3.2 under any and all Transfer-Patents.

## **2.2 Effecting transfer**

Royal Philips shall execute and deliver all files, assignments, and titles, evidence or authorizations as may be required to effectuate or to formalize the transfer of the Transfer-Patents on a jurisdiction by jurisdiction basis, and to cause the Transfer-Patents to be recorded at the relevant patent registers around the world in the name of PSI or its designated Associated Companies. For the purpose of the registration of the transfer of the legal title to the Transfer-Patents as per Clause 2.1 hereof, Royal Philips and PSI shall sign a deed of transfer containing the same conditions as the deed of transfer in Annex 12A as soon as possible after the Closing Date when the Parties have reached agreement on the format of the annex to the deed of transfer. Royal Philips hereby grants PSI and its Associated Companies (designated by PSI) an irrevocable power of attorney to perform any (legal) acts and to execute any documents in its or its Associated Companies' name as may be necessary in this respect, it being understood that this Agreement may not be submitted to the relevant patent registers. If, for whatever reason, the transfer of the Transfer-Patents has not become effective, in full or in part, by the signing by both Parties of this deed of transfer, both Parties will promptly do whatever is necessary to fully effectuate the transfer of the Transfer-Patents.

## **2.3 Rights prior to Closing Date**

PSI does not acquire any rights accruing from ownership of the Transfer-Patents prior to the Closing Date. PSI Group may, however, (i) sue and collect damages in respect of any act of infringement or alleged infringement upon the Transfer-Patents committed prior to the Closing Date, and (ii) give a release for any acts performed prior to the Closing Date and infringing or allegedly infringing upon the Transfer-Patents to any third party. Royal Philips shall not be obliged to take any action in relation to any third party for any act of infringement prior to the Closing Date.

## **2.4 License pending transfer**

Pending the transfers contemplated by Clauses 2.1 and 2.2, PSI Group shall have a license to undertake anything they would be entitled to, had the transfers been effected already, except to enforce at law.

## **2.5 Information on challenges pending transfer**

Royal Philips shall pass on to PSI any correspondence Philips Group receives with respect to any of the Transfer-Patents. This obligation of Royal Philips to inform PSI shall cease upon transfer and recordal of the change in ownership of the Transfer-Patents in the national registers of the designated countries (in respect of such Patents), but no later than 12 (twelve) months after the Closing Date. PSI shall decide at its sole discretion whether it wants to take any action on the basis of the information provided by Royal Philips, and if so, which action it wishes to take. If PSI decides to take action against a third party in order to uphold or enforce the Transfer-Patents, Royal Philips shall provide any reasonable assistance which PSI may request, unless such assistance would harm the reasonable business interests of Philips Group. PSI shall compensate Royal Philips



for all reasonable costs which Royal Philips shall make in providing this assistance. Notwithstanding anything in the foregoing, Royal Philips shall not have any obligation to become a party in any litigation to which PSI is a party.

**2.6 Prior consent**

Where Philips Group requires a third party's prior consent for a transfer contemplated in Clauses 2.1 and 2.2, Royal Philips shall use its reasonable endeavors to procure such consent as soon as reasonably possible and will effect the assignment and transfer to PSI Group of such Transfer-Patents immediately thereafter.

**2.7 Information on prior arrangements**

Within 30 days following receipt of PSI's or any of its successors' or assigns' written request specifying the third party against whom PSI or any authorised third party contemplates to enforce any of the Transfer-Patents, Royal Philips shall use its reasonable endeavors to confirm in writing whether or not that third party can claim rights under those Transfer-Patents (i) granted by Philips Group-Old under any prior agreement or prior commitment entered into prior to the Closing Date or (ii) granted by Philips Group pursuant to Clause 3 after the Closing Date.

**2.8 Transfer costs**

PSI shall bear any costs related to the assignment and transfer of the Transfer-Patents from Philips Group to PSI Group pursuant to Clauses 2.1 and 2.2 and the registration thereof, including all official and agency fees.

**2.9 Maintenance costs**

PSI shall bear any costs of prosecution and maintenance of the Transfer-Patents arising as from the Closing Date, including any remuneration payable to inventors with respect to the period after the Closing Date in accordance with applicable national laws with respect to any of the Transfer-Patents, except that amounts that become due and payable after the Closing Date with respect to prosecution or maintenance of the Transfer-Patents in the period before the Closing Date, shall be borne by Royal Philips.

**2.10 Return of Philips-Patents**

Subject to all prior commitments and prior undertakings of Philips Group-Old to third parties entered into prior to the Closing Date and subject to the licenses granted to PSI Group pursuant to Clause 4, PSI Group hereby assigns to Royal Philips any Philips-Patents that are registered in the name of any entities of Philips Group-Old belonging to PSI Group as of the Closing Date. The above paragraphs of this Clause 2 apply mutatis mutandis (e.g. with Royal Philips bearing the cost).

**3 RETAINED LICENSE TO PHILIPS GROUP**

**3.1 Retained license**

Philips Group retains a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license under the Business Know-How available within Philips Group as of the Closing Date and under any and all of the Transfer-Patents, including the have-made and sublicensing rights as stipulated in Clause 3.2, but not including any other have-made or sublicensing rights.

**3.2 Have-made and sublicensing rights Philips Group**

The license retained by Philips Group pursuant to Clause 3.1 includes a fully paid-up and royalty-free right:

3.2.1 to have products made by a third party manufacturer, solely for the account of,

and for the use or resale by, Philips Group;

- 3.2.2 to grant sublicenses (a) for integrated circuits and discrettes, miniature loudspeakers, Kits or RF front end solutions, (b) for features that are designed by or exclusively for Philips Group, (c) to a third party manufacturer, which has obtained a right as referenced sub 3.2.1, (d) for the duration of such manufacturer delivering such products to Philips Group, (e) to enable such manufacturer to supply such products to third parties for the same applications as used by Philips Group after expiration of the leadtimes as agreed between Philips Group and such supplier for such products, provided (A) Royal Philips first requested PSI in writing to supply such products on competitive terms and conditions and (B) within thirty days after receipt of such request PSI has not offered in writing to supply such products on such terms and conditions;
- 3.2.3 to grant sublicenses for (a) other products than referred to sub 3.2.2 (a), (b) that are designed by or exclusively for Philips Group (c) to a third party manufacturer, which has obtained a right as referenced sub 3.2.1, (d) for the duration of such manufacturer delivering such products to Philips Group, (e) to enable such manufacturer to supply such products to third parties for the same applications as used by Philips Group after expiration of the leadtimes as agreed between Philips Group and such supplier for such products;
- 3.2.4 to grant fully paid-up and royalty-free sublicenses to third parties in the context of and to the extent necessary for enabling primarily technology co-operations; and licensing of software to third parties other than customers.
- 3.2.5 to grant sublicenses to third parties, with whom Royal Philips or any of its Associated Companies has entered or will enter into cross license agreements and to which PSI or any of its Associated Companies will become a party, and
- 3.2.6 to grant sublicenses in the context of and to the extent necessary for the sale or licensing, directly or indirectly, of services, Software and/or IP blocks by Philips Group,

but does not include any other have-made or sublicensing right.

### 3.3 Undertaking not to enforce

PSI shall not enforce or authorize any third party to enforce any of the Transfer-Patents against Philips Group or any authorised third party for any act performed within the license retained by Philips Group pursuant to Clauses 3.1 and 3.2, or against any former Associated Company of Royal Philips within 180 days after it ceasing to be an Associated Company.

### 3.4 Termination in respect of Divested Companies

The license retained by Philips Group pursuant to Clauses 3.1 and 3.2 above shall terminate automatically in respect of a Divested Company as from the date it becomes a Divested Company of Royal Philips. Such termination shall be without prejudice to and have no effect on the licenses granted to PSI Group pursuant to this Agreement with respect to any Philips-Patents, Philips Know-How or Philips-Owned Software owned or controlled by such entity.

### 3.5 Option for Divested Companies

In the event that such Divested Company requests PSI in writing with the written support of Royal Philips within 180 days after its divestment to enter into a license agreement with PSI Group as specified in Clause 3.6, then PSI Group shall enter into such license

agreement with such Divested Company forthwith.

**3.6 License to Divested Companies**

Such license agreement shall be for the grant of a non-transferable, non-exclusive, irrevocable, world-wide, fully paid-up and royalty-free license of the same scope and subject to the same conditions as the license pursuant to Clauses 3.1 and 3.2, provided however that such license (i) shall only be under those Transfer-Patents that are relevant to the Divested Company's business and (ii) shall be limited to the business so divested and not extend to any business, operation or activity, with which it is or becomes combined, merged or that otherwise grows "unnaturally".

**3.7 Respecting license and prior commitments in case of pledge of Transfer-Patents**

In the event PSI agrees with a third party to pledge, or otherwise encumber, any of the Transfer-Patents, PSI is under the obligation to agree with such third party, (a) that such third party shall respect (i) the licenses retained by Philips Group with regard to the Transfer-Patents, and (ii) the prior commitments and prior undertakings with regard to the Transfer-Patents vis-à-vis third parties entered into by Philips Group-Old prior to the Closing Date and all arrangements with regard to the Transfer-Patents between entities belonging to PSI Group and entities remaining with Philips Group made prior to the Closing Date, and (b) that such third party shall impose the obligations described under (a) on any other beneficiary/transferee of any of the Transfer-Patents by way of a perpetual clause.

**3.8 Registration license before registration pledge**

In the event that (i) any of the Transfer-Patents is pledged or otherwise encumbered, (ii) the pledge or other encumbrance is registered at the relevant patent authorities or other authority, and (iii) the license retained by Philips Group cannot be secured in the registration of the pledge or other encumbrance, then PSI shall be under the obligation to register such license at the relevant patent authorities prior the registration of such pledge or other encumbrance.

**4 LICENSE TO PSI GROUP**

**4.1 For current and successor products and technologies/features**

Subject to Clauses 4.6 and 4.12, Royal Philips hereby grants to PSI Group a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, including the have-made and sublicensing rights as stipulated in Clause 4.7, under any and all Philips-Patents or Philips Know-How

- 4.1.1 used in any PSI BUs Product for the continued use by PSI Group in such Product and any commercial successor type thereof;
- 4.1.2 used in any PSI EBs Product approved by the CTO Council (as evidenced by Annex 7) for the continued use by PSI Group in such Product and any commercial successor type thereof;
- 4.1.3 used in any PSI SW Product approved by the CTO Council (as evidenced by Annex 8) for the continued use by PSI Group in such Product and any commercial successor type thereof;
- 4.1.4 used in any technology/feature used in any PSI EBs Product approved by the CTO Council (as evidenced by Annex 7) for the use by PSI Group of such technology/feature in products within the current scope of PSI EBs;
- 4.1.5 used in any technology/feature used in any PSI SW Product approved by the

CTO Council (as evidenced by Annex 8) for the use by PSI Group of such technology/feature in products within the current scope of PSI SW.

A current scope includes products offered for sale prior to the Closing Date, technologies/features approved as set out above, their commercial successor types and any natural extension thereof.

**4.2 For technologies/features currently developed by or for PSI BUs**

Subject to Clauses 4.6 and 4.12, Royal Philips hereby grants to PSI Group a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, including the have-made and sublicensing rights as stipulated in Clause 4.7, under any and all Philips-Patents and Philips Know-How that are or will be used in any technology/feature, for which PSI can demonstrate to the reasonable satisfaction of Royal Philips, that this was

- (i) developed by and for the account of one or more of the PSI BUs prior to the Closing Date,
- (ii) under a material development by and for the account of one or more of the PSI BUs on the basis of an approved budget and started within one or more of the PSI BUs prior to the Closing Date, or
- (iii) developed exclusively for one or more of the PSI BUs and transferred to the relevant PSI BUs within 3 months after the Closing Date,

such license being for the use of such technology/feature in products within the current scope of the PSI BUs.

**4.3 For technologies/features developed pursuant to roadmaps or forecasts**

Subject to Clauses 4.6 and 4.12, Royal Philips hereby grants to PSI Group a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, including the have-made and sublicensing rights as stipulated in Clause 4.7, under any Philips-Patents or Philips Know-How that originated from such part (in scope and time) of the contract research work executed by Philips Group-Old prior to the Closing Date that was co-funded by PSI Group, as can be demonstrated by PSI to the reasonable satisfaction of Royal Philips, and that

- 4.3.1 are used in any technology/feature approved by the CTO Council (as evidenced by Annex 9A) that has been or will be developed by or exclusively for one or more of the PSI BUs pursuant to one or more of the Integral Roadmaps of PSI BUs, such license being for the use of such technology/feature in products within the current scope of the PSI BUs;
- 4.3.2 are used in any technology/feature approved by the CTO Council (as evidenced by Annex 10A) that has been or will be developed by or exclusively for one or more of the PSI EBs pursuant to one or more of the RFFs of PSI EBs, such license being for the use of such technology/feature in products within the current scope of the PSI EBs;
- 4.3.3 are used in any technology/feature approved by the CTO Council (as evidenced by Annex 11A) that has been or will be developed by or exclusively for PSI SW pursuant to one or more of the RFFs of PSI SW, such license being for the use of such technology/feature in products within the current scope of PSI SW.

**4.4 GMR Philips-Patents**

Subject to Clauses 4.6 and 4.12, Royal Philips hereby grants to PSI Group a non-

exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, including the have-made and sublicensing rights as stipulated in Clause 4.7, under the GMR Philips-Patents for use in Giant Magneto-Resistive devices outside the field of healthcare and bio applications.

#### **4.5 RFID tags**

Subject to Clauses 4.6 and 4.12, Royal Philips hereby grants to PSI Group a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, including the have-made and sublicensing rights as stipulated in Clause 4.7, under the Philips-Patents relevant to polymer electronics and that originated from such part (in scope and time) of the contract research work executed by Philips Group-Old prior to the Closing Date as was co-funded by PSI Group, as can be demonstrated by PSI to the reasonable satisfaction of Royal Philips, such license being limited to use in the field of RFID tags and labels. If PSI Group does not make and sell RFID tags and labels using polymer electronics prior to January 1<sup>st</sup>, 2015, the license set out in this Clause 4.5 terminates on that date.

#### **4.6 Respecting prior commitments**

The licenses granted by Royal Philips to PSI Group pursuant to Clauses 4.1 - 4.5 above are subject to all prior commitments and prior undertakings vis-à-vis third parties entered into by Philips Group-Old prior to the Closing Date and all arrangements between entities belonging to PSI Group and entities remaining with Philips Group, made prior to the Closing Date and, where such licenses to PSI Group are granted after the Closing Date, also the prior commitments and prior undertakings vis-à-vis third parties entered into by Philips Group between the Closing Date and such later date.

#### **4.7 Sublicensing rights**

The licenses granted by Royal Philips to PSI Group pursuant to Clauses 4.1 - 4.5 include the fully paid-up and royalty-free right for PSI Group:

- 4.7.1 to have products made by a third party manufacturer, solely for the account of, and for the use or resale by, PSI Group; and
- 4.7.2 to grant sublicenses in the context of and to the extent necessary for the sale or licensing, directly or indirectly, of services, Software and/or IP blocks by PSI Group;

The licenses granted pursuant to Clauses 4.1 - 4.5 do not include any other have-made or sublicensing right than set out in this Clause 4.7.

#### **4.8 Establishment CTO Council**

Parties have established and have agreed to continue a committee ("CTO Council") that is chaired by the CTO of Philips and further has as its members the CTO of PSI, the CTOs of the Philips Product Divisions, as well as the CEOs of Philips Research, Philips Applied Technologies and Philips Intellectual Property & Standards. The CTO Council shall meet upon the written request of one of the Parties. At meetings of the CTO Council at least one member of each Party shall be present.

#### **4.9 Decisions CTO Council**

The CTO Council has decided, and to the extent necessary, will decide after the Closing Date, on the basis of unanimity whether to approve any technology/feature or Product pursuant to Clauses 4.1 (except 4.1.1) and 4.3 above, such approval only to be withheld if such approval would harm the business interests of Philips Group. The CTO Council may decide on the basis of majority voting, including the affirmative vote of PSI Group's

CTO, to make such approval subject to the conditions that the license granted by Royal Philips to PSI Group shall only enter into force after expiration of certain leadtime(s) for Philips Group and/or that the license granted by Royal Philips to PSI Group shall be restricted to certain field(s) of use, which conditions shall be adhered to and respected by PSI Group in respect of its products offered for sale to third parties. The technologies/features or Products for which the CTO Council has made its approval subject to leadtime(s) and/or field(s) of use are indicated in Annexes 7, 8, 9A, 10A, and 11A to this Agreement, which shall be updated by the Parties from time to time in accordance with the provisions of this Agreement.

#### **4.10 Absence of positive CTO Council decision**

As of the Closing Date, PSI Group shall not have any license rights, neither directly nor indirectly, in respect of the technologies/features and Products for which the CTO Council has not given its approval in accordance with Clause 4.9 prior to the Closing Date (as evidenced by Annexes 7, 8, 9A, 10A and 11A to this Agreement), except in case the CTO Council decides to give its approval after the Closing Date. Such withholding of an approval by the CTO Council shall be without prejudice to PSI Group's right to seek approval for such technology/feature or Product again in a later meeting of the CTO Council after the Closing Date. In the event that the CTO Council then decides to approve such technology/feature or Product as yet, PSI Group shall be licensed for such technology/feature or Product in accordance with the applicable license granted to PSI Group pursuant to Clauses 4.1 - 4.3, provided however that any other conditions to which the grant of such license is subject, have also been satisfied. The Parties shall update Annexes 7, 8, 9A, 10A and 11A accordingly from time to time in accordance with the provisions of this Agreement.

#### **4.11 Additional negotiated licenses**

Subject to Clause 4.12, upon PSI's request, Philips shall negotiate with PSI Group the grant of non-exclusive licenses under any Philips-Patents other than those licensed to PSI Group pursuant to Clauses 4.1 - 4.5 for use within the scope of PSI Group's business against terms and conditions to be agreed upon between the Parties, unless the grant of such license to PSI Group would cause harm to any of Philips Group's business or IP interests.

#### **4.12 Exceptions**

None of the provisions of this Agreement shall be interpreted to require Royal Philips to grant or have granted any licenses to PSI Group under any Philips-Patents that:

- (i) are necessarily infringed when implementing a standard adopted by a standard setting body, interest group or agreed between two or more companies; or
- (ii) are or will be part of an industry-wide licensing program of which Royal Philips has notified PSI Group in writing.

#### **4.13 Termination in respect of Divested Companies**

The licenses granted by Royal Philips to PSI Group pursuant to Clauses 4.1 - 4.5 and 4.7 above shall terminate automatically in respect of a Divested Company as from the date it becomes a Divested Company of PSI. Such termination shall be without prejudice to and have no effect on any licenses granted to or retained by Philips Group under this Agreement with respect to any Transfer-Patents, Business Know-How or Business Software owned or controlled by such entity.

#### **4.14 Option for Divested Companies**

In the event that such Divested Company requests Royal Philips in writing with the written support of PSI within 180 days after its divestment to enter into a license agreement with Royal Philips as specified in Clause 4.15, then Royal Philips shall enter into such license agreement with such Divested Company forthwith.

#### **4.15 License to Divested Companies**

Such license agreement shall be for the grant of non-transferable, non-exclusive, irrevocable, world-wide, fully paid-up and royalty-free licenses of the same scope and subject to the same conditions as the licenses pursuant to Clauses 4.1 - 4.5, provided however that such licenses (i) shall only be under those Philips-Patents that are licensed to PSI Group pursuant to Clauses 4.1 - 4.5 at the moment of divestment and which are relevant to the Divested Company's business, and (ii) shall be limited to the business so divested and not extend to any business, operation or activity, with which it is or becomes combined, merged or that otherwise grows "unnaturally".

### **5 EXCEPTIONS**

#### **5.1 PCM Transfer-Patents**

PSI Group shall re-assign to Royal Philips the PCM Transfer-Patents, in the event that PSI Group has not satisfied all of the following conditions at the latest 5 years after the Closing Date:

- (i) phase change memory is on the official technology roadmap of PSI Group, with one committed product to a customer;
- (ii) a first phase change memory of at least 4 MBit has been designed; and
- (iii) first phase change memory wafers have been processed in Crolles2 or in any other advanced wafer facility.

#### **5.2 PCM Transfer-Patents retained license**

Any re-assignment pursuant to Clause 5.1 shall be subject to PSI Group retaining a non-exclusive, non-transferable, irrevocable, world-wide, fully paid-up and royalty-free license, without the right to grant sublicenses (except to have products made by a third party manufacturer, solely for the account of, and for the use or resale by, PSI Group), under the PCM Transfer-Patents for its own use.

#### **5.3 Stand still PCM Transfer-Patents**

Until fulfilment of the conditions set out in Clause 5.1 or re-assignment as set out in Clause 5.1 (whichever happens first), PSI Group shall

- 5.3.1 not enter into any commitment or undertaking vis-à-vis any third party that would materially restrict Philips Group's use of these Patents if and when transferred to Royal Philips pursuant to Clause 5.1;
- 5.3.2 endeavor to maintain the PCM Transfer Patents in each country, except those for which PSI first offered the relevant Patents to Royal Philips and Royal Philips did not express its desire to have such Patents transferred to it;
- 5.3.3 prosecute each of the PCM Transfer Patents in a manner, which reasonably preserves the broadest possible scope of protection in US, JP, CN, DE, FR and GB, in order to take into account Philips Group's interests in these Patents.

#### **5.4 3D applications**

Clause 4 does not apply to Philips-Patents and Philips Know-How relevant to 3D

Applications. Upon PSI's request, Royal Philips shall grant PSI Group a non-exclusive license under Philips-Patents and Philips Know-How relevant to 3D Applications, such license shall be against terms and conditions similar to those Royal Philips offers to other semiconductor manufacturers and subject to the exceptions set forth in Clause 4.12 which shall apply mutatis mutandis. Royal Philips shall not be required to grant PSI broader licenses than actually granted to other semiconductor manufacturers.

#### **5.5 Solid state lighting applications**

For products that are or incorporate a solid-state lighting device for lighting applications, or any component specifically designed therefor other than integrated circuits for use in control drivers, the licenses granted to PSI Group pursuant to Clause 4 only extend to those Philips-Patents that (i) originated from such part (in scope and time) of the contract research work executed by Philips Group-Old prior to the Closing Date as was co-funded by PSI Group, as can be demonstrated by PSI to the reasonable satisfaction of Royal Philips, or (ii) list as inventor(s) one or more employees that at the time of the invention were employed by PSI Group.

### **6 KNOW-HOW, COPYRIGHTS**

#### **6.1 Business Know-How**

Royal Philips hereby assigns and transfers and shall procure the assignment and transfer by its Associated Companies of all Business Know-How to PSI Group as per the Closing Date as part of PSI Group, subject to:

6.1.1 all prior commitments and prior undertakings, either within Philips Group-Old or vis-à-vis third parties, entered into by Philips Group-Old prior to the Closing Date; and

6.1.2 Philips Group retaining the license pursuant to Clauses 3.1 and 3.2.

#### **6.2 Copyrights**

Royal Philips transfers and shall procure the transfer by its Associated Companies to PSI of all copyrights it and they may have in the integrated circuits and discretets, miniature loudspeakers, Kits or RF front end solutions, all as sold or developed by or exclusively for PSI Group until the Closing Date, and any copyrights in the drawings for or in documentation about such products (all except to the extent this constitutes Software). Nothing in this Agreement shall be read to imply a license to any copyright of PSI Group.

### **7 SOFTWARE**

#### **7.1 Business Software**

Royal Philips hereby assigns and transfers, and shall procure that its Associated Companies will assign and transfer, to PSI Group Philips Group's entire right, title and interest to the Business Software, including all copyrights and trade secrets in the Business Software, for all countries of the world, subject to (i) all prior commitments and prior undertakings, either within Philips Group-Old or vis-à-vis third parties, entered into by Philips Group-Old prior to the Closing Date, and (ii) Philips Group retaining a non-exclusive, irrevocable, world-wide, fully paid-up and royalty-free licence to use (in the broadest sense) such Software within the scope of business of Philips Group, including, without limitation, the right to modify, create derivative works and the right to grant sublicenses for the use of such Software, in the context of and to the extent necessary for marketing or supplying Philips products and provided such Software is available within



Philips Group on the Closing Date.

**7.2 License to use Philips-Owned Software**

Royal Philips hereby grants to PSI Group a non-transferable, non-exclusive, irrevocable, world-wide, fully paid up and royalty-free license under any of Philips Group's copyrights and trade secrets in the Philips-Owned Software to the extent such Software is available within PSI Group on the Closing Date to use (in the broadest sense) such Software within the scope of business of PSI Group, including, without limitation, the right to modify, create derivative works and the right to grant sublicenses, in the context of and to the extent necessary for marketing or supplying integrated circuits and discretes, miniature loudspeakers, Kits or RF front end solutions supplied by PSI Group per the Closing Date and any commercial successor types thereof, subject to all prior commitments and prior undertakings, either within Philips Group-Old or vis-à-vis third parties, entered into by Philips Group-Old prior to the Closing Date .

**7.3 Third Party Software**

Royal Philips shall assign to PSI Group the agreements relating to Third Party Software entered into by Philips Group-Old prior to the Closing Date as listed in Annex 14 to this Agreement, but only to the extent that such agreements are assignable without the consent of a third party. For any such agreement not so assignable, Philips shall use its reasonable endeavours to procure the (consent to) assignment of such agreement to PSI Group or provide PSI Group the benefit of the rights under such agreements (subject to the obligations there under) in another way, all subject to PSI reimbursing Royal Philips for the additional costs incurred by Philips Group in relation thereto.

**7.4 Philips-Owned Software**

Royal Philips and PSI Group shall negotiate in good faith mutually agreeable terms and conditions for the maintenance and support of Philips-Owned Software licensed hereunder by Royal Philips to PSI Group.

**8 TRADEMARKS AND DOMAIN NAMES**

**8.1 Assignment**

Royal Philips hereby transfers and assigns to PSI Group the Trademarks, and the goodwill attached to and represented by such Trademarks and Domain Names, subject to all prior commitments and prior undertakings of Philips Group-Old to third parties entered into prior to the Closing Date. Clauses 2.2 and 2.5 shall apply mutatis mutandis, except that Royal Philips and PSI shall sign the deed of transfer in Annex 13A immediately after the signing of this Agreement.

**8.2 Restricted Use**

After the Closing Date PSI Group shall not use (i) the word Philips as a trade name or as part of a corporate name, (ii) the trademark Philips and/or Philips shield emblem, or (iii) any derivate or combination mark containing the elements PHIL or PHIL, other than as agreed in the Trademark License Agreement attached hereto as Annex 16.

**9 ASSIGNMENT OF PATENT LICENSE AND PATENT OWNERSHIP AGREEMENTS**

**9.1 Assignment**

Royal Philips shall assign to PSI Group the patent license agreements and the patent ownership agreements entered into by Philips Group-Old prior to the Closing Date as listed in Annex 15 hereto, to the extent that such agreements are assignable.

## 9.2 Exception to assignment

If any such agreement is not assignable without the consent of a third party, Royal Philips shall use its reasonable endeavors to obtain the consent of the other party(ies) to such agreement for the assignment thereof to PSI Group. If Philips is unable to obtain such consent, Philips shall use its reasonable endeavors to provide PSI Group the benefit of the rights under such agreements (subject to the obligations there under) in another way or assist PSI Group in finding alternative solutions, all subject to PSI reimbursing Royal Philips for the additional costs (except internal costs) incurred by Philips Group in relation thereto.

## 9.3 Divested Companies

In relation to any license agreements or cross licenses listed in Annex 15, by which rights may be granted to a divested company subject to Royal Philips or any of its Associated Companies supporting an application for the grant of such rights, Royal Philips shall support such applications and shall use reasonable endeavours to ensure that the benefit of the divested company provision in that agreement is realised for the benefit of the PSI Group as Divested Company (of Royal Philips), all subject to PSI reimbursing Royal Philips for the additional costs (except internal costs) incurred by Philips Group in relation thereto.

## 10 CONFIDENTIAL INFORMATION

In connection with the disentanglement and assignments to be performed and licenses to be granted under this Agreement, Parties may disclose or provide access to certain Confidential Information to each other or retain Confidential Information relating to the other Party and its Associated Companies. The receiving or retaining Party shall maintain such Confidential Information relating to the other Party strictly confidential and shall not use, disclose, or otherwise exploit any Confidential Information for any purpose not expressly authorized under this Agreement. For the purpose of this Agreement, the term "Confidential Information" shall mean any data or information that is designated as confidential by Philips Group-Old. Notwithstanding the foregoing, Confidential Information shall not include information that: (i) is publicly available or in the public domain at the time it is disclosed; or (ii) is or becomes publicly available or enters the public domain through no fault of either Party or (iii) is obtained by the other Party from an authorised third-party source or developed independently by the other Party, or (iv) is required by applicable law, court order or legal process to be disclosed, provided that the obligated Party shall promptly inform the other Party, and cooperate in any attempts by the other Party to obtain a protective order or similar treatment.

## 11 REPRESENTATIONS AND WARRANTIES

### 11.1 As is

Parties acknowledge and agree that the assignments made and licenses granted by either Party hereunder are made on an "AS IS" basis and Parties do not make and hereby expressly disclaim any other express or implied representation or any warranty (including, without limitation, any express or implied warranties of merchantability, fitness for a particular purpose, title, enforceability, validity, subsistence, scope, non-infringement or non-violation of any rights of any third party, and the absence of any outstanding order, judgement, decision, decree or agreement adversely affecting the use thereof by the other Party and its Associated Companies).

## 11.2 No maintenance obligation

Except as specifically provided elsewhere, neither Party shall have any obligation to maintain or support any Intellectual Property licensed under this Agreement, or to provide upgrades or enhancements thereto.

## 12 NON-COMPETE

### 12.1 Restricted activities

Philips Group undertakes that for a period of three years commencing on the Closing Date, it shall not make, sell and/or otherwise dispose of integrated circuits and discretets, miniature loudspeakers, Kits or RF front end solutions competing with products sold by PSI Group at the Closing Date or being developed by and for the account of PSI Group on the basis of an approved budget at the Closing Date, or their commercial successors (any such competing products hereafter "PSI Products"), other than those referred to in Clause 12.2, in any country of the world (the "Restricted Activity").

### 12.2 Products not restricted

Solid state and other lighting devices (including control modules for drivers of such devices), giant- or tunnel magneto-resistive devices for other than automotive applications, imaging- and bio- and other sensor devices, lab-on-chips, drug delivery and drug testing devices, therapeutic devices, implantables, sensor and actuator modules other than for automotive or RFID applications, micro electro-mechanical and/or semiconductor switches for bio or healthcare applications, flexible- or organic semiconductors and semiconductors using substrates other than silicon fall outside the Restricted Activity.

### 12.3 Activities not restricted

Nothing in Clause 12.1 shall restrict Philips Group from:

12.3.1 Conducting research, developing products, prototyping or making small non-commercial series of products competing with PSI Products;

12.3.2 Making, having made, selling or otherwise disposing of products the Philips Group offers for sale or develops as of the Closing Date (including through the businesses of the Product Divisions Consumer Electronics (except for the IC Lab), Medical Systems, Lighting (incl. Lumileds) and Domestic Appliances and Personal Care, as well as the Philips entities Consumer Healthcare Solutions, Corporate Technologies (including Philips Research and Philips Applied Technologies (except for the parts that move to PSI), the Lifestyle, Healthcare and Technology Incubators and the Molecular Healthcare Business) and Corporate Investments (including Ommic)) and their commercial successors;

12.3.3 Making, having made, selling or otherwise disposing of end-user products directed to

- (i) the consumer market (including any kind of personal care and domestic appliances);
- (ii) the professional and consumer medical markets and the professional and personal healthcare-and wellness markets; or
- (iii) for the professional, consumer and special lighting markets, whether based on lamps or on solid state devices;

- 12.3.4 Making or having made any type of semiconductor products (whether in the form of a die, wafer or packaged chip) for incorporation into any Philips products, systems, applications or services;
- 12.3.5 Making, selling or otherwise disposing of such semiconductor products as standalone products to third parties, provided that standalone semiconductor products falling within the Restricted Activity may only be sold or otherwise disposed of with the written consent of PSI;
- 12.3.6 making, using, selling, licensing and/or otherwise disposing of any software product that does not contain Business Software originating from and marketed by PSI Group separately or in combination with integrated circuits, either as a standalone product or bundled with any other Philips product, device, system, application or service;
- 12.3.7 making, using, selling and/or otherwise disposing of any demonstration or evaluation tools or kits for any of Philips products, systems or applications;
- 12.3.8 Developing or conducting incubands or ventures, spinning-out or spinning-off any business activities of Philips Group, or creating business out of Philips Group's research and development activities, within the Restricted Activity as long as the total activities of any such business separately has revenues less than 50 million euros per year;
- 12.3.9 Providing research, development and engineering facilities with clean room, material analysis, prototyping, sampling, small series production, verification, testing, and/or offering related services to third parties;
- 12.3.10 Acquiring a controlling interest in another company or entity, provided that the commercial activities of such company or entity falling within the Restricted Activity did not generate more than hundred million euros of revenue in the fiscal year preceding the year in which the acquisition is made. In the event that an acquisition should occur of a company or entity with a revenue above this threshold, then Royal Philips will enter into good faith negotiations with PSI Group for the sale of such competing commercial activities to PSI Group;
- 12.3.11 Acquiring a non-controlling interest in another company or entity, provided that if such company or entity is primarily engaged in activities falling within the Restricted Activity, such interest shall not exceed 10 per cent of the voting stock ordinarily entitled to vote in the election of directors of such company or entity;
- 12.3.12 Holding any securities or having any other interest in another company or entity, the main activity of which is to make, select, hold and/or manage investments in start-up or other businesses, or having any interest in any investment made, held or managed by such company or entity, provided that:
- i. Royal Philips is not able to control or direct such other company or entity in any manner with respect to such investments; and
  - ii. Royal Philips is not able to control the management or direct the activities of any such company or entity in which such investment has been made.
- 12.3.12 Continue its interest in TSMC as may vary from time to time, provided that an increase of its interest may only result from financial transactions not initiated by Royal Philips.

For the avoidance of doubt, the provisions of this clause 12.3 shall not be construed as broadening the scope of the restrictions in clause 12.1, nor as affecting the scope of any licenses granted under this Agreement.

**12.4 If Royal Philips is acquired**

If Royal Philips is acquired (whether by merger, acquisition or otherwise) by any person who is not an Associated Company of Philips ("the Acquirer"), this non-compete clause shall not apply in respect of any activities of the Acquirer or any of its Associated Companies (excluding Royal Philips).

**13 CLAIMS AND LIABILITY**

Any claim for a breach of any representations, warranties, covenants or undertakings contained in this Agreement shall only be enforceable by PSI Group against Royal Philips in accordance with the provisions of the Stock Purchase Agreement, and the liability and the limitations on such liability in respect of any breach of such representations, warranties, covenants and undertakings shall be determined solely in accordance with the terms of the Stock Purchase Agreement.

**14 TERMINATION**

After the Closing Date this Agreement cannot be terminated or rescinded.

**15 NOTICES**

All notices or other communications hereunder shall be given in accordance with Clause 10.1 of the Stock Purchase Agreement to the addressees provided therein, as well as:

in respect of Royal Philips to:

Philips Intellectual Property & Standards  
Building WAH  
Prof. Holstlaan 6  
5656 AA Eindhoven  
P.O. Box 220  
5600 AE Eindhoven  
The Netherlands  
F.a.o. Counseling Executive  
Fax no.: +31 40 274 34 89

in respect of PSI to:

NXP B.V.  
High Tech Campus 60  
5656 AG Eindhoven  
The Netherlands  
F.a.o. General Counsel

**16 NO ASSIGNMENT**

This Agreement shall be binding upon and inure to the benefit of the Parties and their respective successors and assigns. Save as explicitly provided otherwise herein, neither this Agreement nor any right or obligation hereunder shall be assignable by either Party, in whole or in part, to any third party without the prior written consent of the other Party.

**17 MISCELLANEOUS**

**17.1 No implied license**

Except as expressly provided in this Agreement, nothing contained in this Agreement shall be construed as implicitly granting a license or any other right under Patents, Know-How, Software, Trademarks or Domain Names.

**17.2 No agency**

Nothing contained in this Agreement shall be deemed or construed to constitute or create an agency, association, joint venture or partnership between the Parties.

**18 SEVERABILITY**

If any one or more of the provisions of this Agreement is determined to be invalid or unenforceable by any court of competent jurisdiction, such finding shall not invalidate the remainder of this Agreement which shall remain in full force and effect as if the provision(s) determined to be invalid or unenforceable had not been a part of this Agreement. In the event of such finding of invalidity or unenforceability, the Parties will endeavour to substitute forthwith the invalid or unenforceable provision(s) by such effective provision(s) as will most closely correspond with the original intention of the provision(s) so voided.

**19 ENTIRE AGREEMENT**

**19.1** This Agreement sets forth the entire understanding and agreement between the Parties as to the subject matter of this Agreement and supersedes, cancels and merges all prior agreements, negotiations, commitments, communications and discussions between the Parties relating to the subject matter hereof.

**19.2** No modification or amendment of this Agreement shall be binding upon either Party unless made in writing and signed by a duly authorized representative of each of the Parties hereto.

**19.3** It is acknowledged and agreed that the performance by the Parties of their obligations pursuant to this Agreement shall by no means result in any obligation on the part of either Party to enter into any further agreement containing obligations for either Party beyond the obligations contained herein or to realize any transaction with the other Party with respect to the subject matter hereof or otherwise, including without limitation, any agreement or transaction concerning the supply of services by either Party to the other.

**20 APPLICABLE LAW AND JURISDICTION**

**20.1 Dutch law**

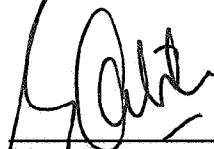
This Agreement shall be governed by and construed in accordance with the laws of The Netherlands, regardless of its conflict of law principles.

**20.2 Jurisdiction**

Any dispute between the Parties arising out of or in connection with this Agreement, including any question regarding its existence, validity or termination, shall be submitted to the competent courts of The Hague, The Netherlands, without prejudice to the right of either Party to seek injunctive relief in any place where an infringement of its rights occurs or threatens to occur.


**IN WITNESS WHEREOF**, the Parties have caused this Agreement to be signed by their duly authorized representatives on 28 September 2006.

**Koninklijke Philips Electronics N.V.**

  
\_\_\_\_\_  
(signature)

Name: *E. Continno*  
Title: *GENERAL SECRETARY*

**Philips Semiconductors International B.V.**

  
\_\_\_\_\_  
(signature)

Name: *Swako Dierich*  
Title: *General Counsel*

Annex 1 - Transfer-Patents



Patent No. | Date of Patent | Applicant | Title | Class

Table with columns: Patent No., Date of Patent, Applicant, Title, Class. Contains numerous patent entries such as 'Media playback software tracing', 'Horizontal etching combined with non-conformal deposition', 'Bluetooth microphone array', etc.

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Patent No.	Date	Inventor	Title	IPC Class.	Pub No.
006213	20-jun-06	JEROEN BIELEN@PHILIPS.COM	Optimal thermal design for RF power amplifier	EP	6115761.7
006376	20-jun-06	YANLI@PHILIPS.COM	Frequency Estimation Method Based on Training Sequence	CN	to be assessed
005211	04-jun-06	ANDREAS BURJ@PHILIPS.COM	Wideband digital shaping filter	EP	6115698
005372	20-jun-06	KIRAN NAGARA@PHILIPS.COM	Non Pseudo Random Number Generation Algorithm	EP	6115656.4
005319	19-jun-06	ROEL HOFFMANN@PHILIPS.COM	Self-aligned PIN diffusion barrier process for Copper-Low-k Interconnects	EP	EP06116669.1
005507	19-jun-06	PAUL HOFMANN@PHILIPS.COM	Global Motion Estimation in the presence of Foreground Objects	EP	6115685.9
008034	18-jun-06	FRED HURKX@PHILIPS.COM	Contrast layer for the detection of hidden objects during layer thinning	EP	6115650.1
006793	14-jun-06	ERWIN HUIZEN@PHILIPS.COM	GRINGO: a fully self-aligned high speed HBT	EP	6116474.6
005067	12-jun-06	FRANZ AMTMANN@PHILIPS.COM	Novel low voltage NVM cell concept based on floating gate wrapped aryl	EP	6115400.1
005735	12-jun-06	MANUEL COLLADOS@PHILIPS.COM	Method for Übertragung von sensitiven Daten	EP	EP06115373.0
005174	9-jun-06	CLAIRE RAVIT@PHILIPS.COM	Power-efficient envelope and phase signal generation for polar transmitters	EP	6115266.6
005713	8-jun-06	JOSEF LUTZ@PHILIPS.COM	Enhancing the ultra-fast programming of submicron polysilicon fuses with a	EP	6115191.6
005709	8-jun-06	CHRISTOPH TAPLER@PHILIPS.COM	Improvement of acoustic properties of a piezo-speaker by designing appr	EP	EP06115173.4
005842	7-jun-06	DENK REEFMANN@PHILIPS.COM	Reminis Defragmentation	EP	6300562.3
005608	7-jun-06	CHRISTIAN SCHERABON@PHILIPS.COM	DC/DC converter based on integratable batteries	EP	EP06115080.1
005071	30-mai-06	ROB VAN SCHALK@PHILIPS.COM	Montageverfahren eines UHF-RFID-ICs auf eine Antenne	EP	6114722.9
005636	30-mai-06	PETER NUIJTEN@PHILIPS.COM	Array configuration for double gate floating body FinFET DRAM cell with a	EP	6114702.1
005935	30-mai-06	JUERGEN NOWOTNICK@PHILIPS.COM	A digitally controlled DC-DC converter with low output ripple	EP	EP06114725.2
005235	30-mai-06	PETER SCHOLTEMS@PHILIPS.COM	Removal detector for pharmaceuticals	EP	6114665
005365	29-mai-06	RODOLFO VERZEENASSI@PHILIPS.COM	Verfahren zur Erzeugung eines Zählerstrahles in einem nicht-flüchtigen S	EP	6114673.4
006060	25-mai-06	MICHAEL GANZERA@PHILIPS.COM	A Bandwidth Matched Dynamic Reference Ladder	EP	6300527.6
005591	25-mai-06	CHRIS WYLAND@PHILIPS.COM	Display on a contactless smart card as an integral part of a secure transa	EP	EP06114658.5
005834	24-mai-06	LEON HESE@PHILIPS.COM	Method and System for Composite Bond Wires	US	6088858.7
005864	24-mai-06	RICHARD KLEHROST@PHILIPS.COM	Unified multi-port registerable with uniform read and selective writs ports	US	60889588
004894	23-mai-06	WINFRIED BIRTH@PHILIPS.COM	Highly linear continuous gain controlled low noise amplifier	EP	6114509
006072	22-mai-06	WIRFEL MURRAY@PHILIPS.COM	Parallel processor array for both parallel pixel processing and parallel blo	EP	6114368.1
005973	19-mai-06	MICHEL VAN DUUREN@PHILIPS.COM	Secure, Keyless, Provision of Payment Card Details During Inference	EP	6114333.6
004971	18-mai-06	DETLEF GOETTING@PHILIPS.COM	SOMOS for one-time-programmable (OTP) applications; addendum to ID	EP	6114257.6
005486	18-mai-06	PHILIPPE GENTRIC@PHILIPS.COM	Mask-Free method for inductor performance improvement	EP	6300487.3
006028	18-mai-06	HARTMUT HABEN@PHILIPS.COM	Adaptive rounding for graceful panic mode regulations	EP	6114136.8
005313	17-mai-06	CHRIS WYLAND@PHILIPS.COM	Mobile platform emulation for the "every PC becomes yours" use case	EP	6300468.1
004236	17-mai-06	MARCO BEKKO@PHILIPS.COM	Copy Function for Diagnostic Purposes in CAN Applications	EP	6114147.9
005372	17-mai-06	ARMET BASTOU@PHILIPS.COM	Stacked Die Stress Release Method for Low-k Dielectric Devices	US	60891598
005546	17-mai-06	JEROEN VAN DEN BOOM@PHILIPS.COM	Progress aware memory controller for real-time applications	EP	6114086.5
005727	17-mai-06	CHRISTIAN HAAS@PHILIPS.COM	Noise Variance, SINR, and QPSK Amplitude Estimation in CDMA System	EP	6300478.2
005940	17-mai-06	GERHARD KOPORS@PHILIPS.COM	FEEDBACK LOOP FOR FAST RECOVERY OF CAPACITIVE MEMS SE	EP	6114074.3
005871	16-mai-06	KEES VAN BERKEL@PHILIPS.COM	Prevention from rocking mode in loudspeaker applications	EP	EP06114068.2
006031	16-mai-06	KAREN BANEERJEE@PHILIPS.COM	Quantum Cascade Laser with perpendicular out-coupling	EP	6114102.8
005924	15-mai-06	DERK REEFMANN@PHILIPS.COM	Low-cost SIMD memory with limited scatter gather	EP	6113575.4
005829	15-mai-06	FRANZ AMTMANN@PHILIPS.COM	Wireless Medium Reservation scheme in the 802.11 networks for collica	US	601801086
005944	12-mai-06	STEPAN BITZMANN@PHILIPS.COM	Minisaturated DC/DC converter	EP	6113655.6
005920	11-mai-06	EDWIN V.D. HEIJEN@PHILIPS.COM	Data protection with random password	EP	6113609.3
005117	10-mai-06	FRANCOIS MARTIN@PHILIPS.COM	Implementation of Current Interface with a blocking capacitor	EP	6113638.4
005053	9-mai-06	SOENKE OSTERLUNG@PHILIPS.COM	Resonator with shored stub and MIM-capacitor	EP	6113621
005052	8-mai-06	ANTOINETTE DUCLOUX@PHILIPS.COM	Media Synchronization in 3GPP packet switched services	EP	6300458.2
005051	8-mai-06	SAUL DOOLEY@PHILIPS.COM	Schaltung zur Spannungsüberüberprüfung logischer Signale zur Er	EP	6113747.7
005050	5-mai-06	MARIA GAJDA@PHILIPS.COM	Adaptive Jitter Extraction	EP	6300441
005049	5-mai-06	HARSH DHAND@PHILIPS.COM	Variable Sample Rate for GPS	EP	6113636.8
004284	4-mai-06	D NOEL@PHILIPS.COM	Linear current ratio sensor for Trench+ applications	EP	6113646.1
004283	4-mai-06	MICKAEL BOUTYAU@PHILIPS.COM	NON C2 - Automatic PVT Distributed Compensation Schemes for High Pe	EP	6300442.8
004282	4-mai-06	KARIM BOUTYAU@PHILIPS.COM	Peer to peer authentication using master-slave communication framework	EP	6113559.4
004523	3-mai-06	GERALD SCHAFFLER@PHILIPS.COM	TrenchFET with Low Reson and Reduced Parasitic Characteristics	EP	6113585.1
004522	3-mai-06	ANDY NEGOCI@PHILIPS.COM	Antenna Setting for Optimal Link Budget in MI Data Link	EP	6300441
004521	2-mai-06	JAN VINK@PHILIPS.COM	Automatic Frequency Control for a 3G UE in call DCH Soft Handover	EP	6300439.4
004520	2-mai-06	SRDJAN KORDIC@PHILIPS.COM	Enhanced Emergency call feature	EP	6300440.2
004127	28-apr-06	LIONEL GUIRAUD@PHILIPS.COM	Inclusive RFID coupling and Capacitive Body Coupling in one device by a	EP	6113531.6
005849	28-apr-06	SANDEEP AGRAWAL@PHILIPS.COM	Very Low Power PVT (Process, Voltage and Temperature) Analog Comp	EP	6300436.1
005822	27-apr-06	LIANGLIANG HU@PHILIPS.COM	Latency optimized re-synchronization solution for DDR/DDR2 SDRAM re	US	6078747.2
		WILLEMA JONKER@PHILIPS.COM	Membrane for Water De-Chloring	EP	6300423.8
			TRIPOD PILLAR	EP	6300422
			Higher UHF Band Rates generation at lower XTAL oscillator frequency	EP	6113305.4
			Novel Method and Structure of Software Defined Radio Device Configura	CN	2.0061E+11
			A lucky dip as a secure data store	EP	6113193.8

Patent No.	IPC Class.	Inventor	Title	Applicant	Pub. No.
004981	26-apr-06	01-CMOS and embedded in	FinFET with corner-free and cylindrical shape Fin for non-volatile memory	EP	6113147
005382	26-apr-06	01-CMOS and embedded in	Chemical Mechanical Polishing of Abrinrium	EP	6300405.5
005278	26-apr-06	05-Mobile Communication	Very high power RF device arrangement with low junction temperature	EP	6113148.8
006311	26-apr-06	05-Mobile Communication	Programmable Power Mode Transition Controller	US	60795909
004943	26-apr-06	10-Other	Performance improvement of a wireless modem	EP	6113156.1
005042	26-apr-06	07-Identification	Analog BIST for bandgap voltage and current reference	EP	6113026.6
004237	24-apr-06	01-CMOS and embedded in	Novel SRAM cells using asymmetrical FinFETs	EP	6130044.3
005655	24-apr-06	01-CMOS and embedded in	IMPROVED MEMORY CELL SENSING TECHNIQUE	EP	6112968.8
000851	24-apr-06	05-Mobile Communication	Low-cost image mosaic creation system from video	EP	6300368.2
004933	21-apr-06	01-CMOS and embedded in	MON C2 - Improved Accuracy Resistor Divider Digital to Analog Converter	EP	6300363.3
004179	21-apr-06	05-Mobile Communication	A polar WLAN OFDM transmitter architecture	EP	6112906
005716	20-apr-06	01-CMOS and embedded in	On-Chip Circuit and Method for Water Temperature Measurement using	EP	6300363.4
004813	20-apr-06	04-Audio/Video	Real-time generation of movie-in-a-minute during video encoding	EP	6112812
004512	18-apr-06	04-Audio/Video	COMMON DAC SUPPLY OF PRESTAGE FILTERS	EP	612772.6
005852	18-apr-06	04-Audio/Video	Reducing halo with a Bidirectionally Estimated Refiner for vector fields	EP	6112756.9
005709	14-apr-06	03-RF Devices	ESD-protection with LC-circuit to prevent memory effects	EP	6112677.7
005738	14-apr-06	05-Mobile Communication	Integrated Dotarray Amplifier	EP	6112675.1
004469	13-apr-06	01-CMOS and embedded in	Chip identification using embedded volatile memories	EP	6112656.1
004248	12-apr-06	01-CMOS and embedded in	Memis package with the functional substrate as hermetic seal	EP	6112653.6
005100	12-apr-06	01-CMOS and embedded in	Ordnat Estimation of Time-varying Channel Parameters Associated With	EP	6300358.6
005869	10-apr-06	02-Packaging & Testing	3V3 signaling with 2V5 devices	EP	6112622.5
004877	10-apr-06	07-Identification	Low Power PLL Configuration Algorithm	US	60761631
004352	7-apr-06	01-CMOS and embedded in	Inter-layer connections for full MEMS technology	EP	6112417.8
005552	7-apr-06	01-CMOS and embedded in	RFID tag with moisture-dependent contact	EP	6112418.3
004375	7-apr-06	06-Connectivity	Secure Key Storage with Flip-Flop PUF	EP	6112408.5
005151	6-apr-06	08-Automotive	Hybrid exposure (EUV/optical) for an integration architecture with amor EP	EP	6300348.1
004990	5-apr-06	03-RF Devices	Method and System for Enabling Heterogeneous Networks without using	US	60791101
005847	5-apr-06	05-Mobile Communication	Efficient Startup Protection for Communication Networks	US	6112349.3
005161	5-apr-06	03-RF Devices	Secured modular exponentiation with protection against the Differential F	EP	6112279.2
004779	5-apr-06	01-CMOS and embedded in	Built-up test to verify the monotonic behavior of a capacitor array and	EP	6300329.7
005558	30-mnt-06	05-Mobile Communication	Four-click reduction in class D amplifiers	EP	651206
005022	30-mnt-06	05-Mobile Communication	Calibration loop by measuring differential variance	EP	6112124
004781	28-mnt-06	05-Mobile Communication	Method and System for DC Clock Generation	US	60798570
004189	27-mnt-06	05-Mobile Communication	Method and System for Signal Driver Using Capacitive Feedback	US	60787869
004135	27-mnt-06	08-Connectivity	Method and System for Signal Control	US	60788477
003744	24-mnt-06	01-CMOS and embedded in	A technological process to localize air gap formation without impacting th	EP	6111964
005283	24-mnt-06	04-Audio/Video	Bit line cross-coupling compensation	EP	6300306.5
005032	23-mnt-06	02-Packaging & Testing	IP2 self calibration system for wireless transceiver	EP	6111958.9
004781	23-mnt-06	05-Mobile Communication	Hybrid Polar Radio Transmitter Architecture	EP	6112024.2
004125	22-mnt-06	05-Mobile Communication	Compensation of Delay Mismatch	EP	6300282.7
003744	22-mnt-06	05-Mobile Communication	Demasking algorithm for uniformed and non uniformed distributed layer	EP	6111830.3
005283	22-mnt-06	04-Audio/Video	RF PWM & PWM with 4 multipliers	EP	6300289.3
005032	22-mnt-06	02-Packaging & Testing	Low-voltage, Low-noise, Accurate VDD/VOL differential swing up driver	US	60785681
004781	22-mnt-06	05-Mobile Communication	Rapid Creation and Configuration of Microcontroller Products with Config	EP	6111698.4
004125	22-mnt-06	05-Mobile Communication	Staged AGC in a Tuner TV	EP	6300282.8
003744	22-mnt-06	05-Mobile Communication	Electronically Enhanced Wirebond Package	US	60785698
005283	22-mnt-06	05-Mobile Communication	Full and Dynamic Compensation of IR drops Along Power Supply Lines	EP	6300280.2
004781	21-mnt-06	01-CMOS and embedded in	Improvement of Common Mode Stability of Differential Amplifiers	EP	6300271.1
004125	21-mnt-06	05-Mobile Communication	Pseudo-Synchronous Small Register Designs with Very Low Power Cons	US	60784641
005473	20-mnt-06	04-Audio/Video	Pulse Width Modulation Based LED Dimmer Control	US	60784643
005374	17-mnt-06	03-RF Devices	Streaming kernel	GB	606547.9
005247	17-mnt-06	04-Audio/Video	Reduction of initial-ion concentration on silicon wafers	EP	6111395.6
004643	17-mnt-06	05-Mobile Communication	Electronic device with antenna	EP	63500244.8
003809	16-mnt-06	01-CMOS and embedded in	Combining qualifier watermarking with colour compression	EP	6111329.6
004699	14-mnt-06	01-CMOS and embedded in	Ripple free DC converter	EP	6300245.5
005332	14-mnt-06	03-RF Devices	Method of Reverse coupling for high voltage designs using low voltage th	EP	6111241.3
004861	14-mnt-06	07-Identification	Source and drain junction formation for thin body Si devices	EP	6111109.2
004433	13-mnt-06	04-Audio/Video	Full MEMS folding method for packaging	EP	6111124.1
			Automatic Data Migration	EP	6111084.7
			DRR Interface Timing Issue	EP	6111040.9

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005329	01-CMOS and embedded p...	WIBO.VAN.DOORT@PHILIPS.COM	Partially insulated heterojunction Bipolar Transistor (PHIBT)	EP	6110950
005366	04-Audio/Video	PVD.CAMMEN@CATEENA.NL	Monostereo blind function by means CMM ratio drive for special audio	EP	6110953.4
005371	04-Audio/Video	HNAUTA@CATEENA.NL	ON-CHIP REFERENCE FREQUENCY OSCILLATOR LOCKED TO EXIT	EP	6110977.3
005373	04-Audio/Video	HNAUTA@CATEENA.NL	Accurate (analog) on-chip filters for low-IF filter applications in low-power	EP	6110981.5
004362	09-Discretes and Multimarke	KEVIN.MAHOOTI@PHILIPS.COM	Pulse-Shaping Circuit for Crystal Oscillator	US	60781236
004846	03-RF Devices	MAREK.SWORSKI@PHILIPS.COM	Embedded MEMS resonator device	EP	6110892.4
005334	04-Audio/Video	BERT.EGELMEERS@PHILIPS.COM	Wideband Antenna Automatic Alignment	EP	6110905.4
005351	04-Audio/Video	PVD.CAMMEN@CATEENA.NL	System for pilot suppression in FM stereo radio receivers using "switching	EP	6110816.1
005353	04-Audio/Video	HNAUTA@CATEENA.NL	Limiter and RSSI (Radio Signal Strength Indication) for low-IF FM radio r	EP	6110918.7
004625	06-Connectivity	ELIJAH.KHOURY@PHILIPS.COM	Low Power High Speed +Gbits Differential Interface and Related Intercon	US	60781124
004238	02-Packaging & Testing	VICTOR.VAN.ACHT@PHILIPS.COM	Quick but safe egg retrieval	EP	6110785
005175	04-Audio/Video	PAUL.SCHWAN@PHILIPS.COM	About the testing of WLAN-Systems for embedded devices running offair	EP	6110836.5
004149	05-Mobile Communication	TOMSON.GEORGE@PHILIPS.COM	Efficient method of addressing the on chip memory for block operations	EP	6110716.5
004349	05-Mobile Communication	XAVIER.XC.QIANG@PHILIPS.COM	Clock signals generation for quadrature subsampling	CN	2,036,51E+11
003808	04-Discretes and Multimarke	AJAY.KAPOOR@PHILIPS.COM	The Design and Realization of a CMOS Analog Switch with Injection-Curr	EP	6110655.7
003889	04-Audio/Video	BUG.THOMAS@PHILIPS.COM	Adaptive cache partitioning using task execution phases	US	60779271
005253	04-Audio/Video	TOMSON.GEORGE@PHILIPS.COM	Method of compilation during conditional branching	US	60778335
005254	06-Connectivity	PEN.LU@PHILIPS.COM	On Selecting the Guard Interval Length in OFDM Systems	US	60778515
001998	28-Feb-06	PEN.LU@PHILIPS.COM	Calibration for wireless Channel Reciprocity using blind parity	US	60778514
002203	01-CMOS and embedded p...	WIM.BESLING@PHILIPS.COM	Side wall reconstruction of degraded porous TALK side wall with CVD lines	EP	6280339.8
004612	01-CMOS and embedded p...	TON.VAN.DE.KERKHOF@PHILIPS.COM	Controlled pump down of an LPCVD furnace	EP	6118311
004170	05-Mobile Communication	REMCO.BRINKMAN@PHILIPS.COM	Compiled pump down of an LPCVD furnace	EP	6110913.6
003528	04-Audio/Video	VICTOR.VAN.ACHT@PHILIPS.COM	A Window Comparator With Accurate Levels For Use In DC-DC Converter	EP	6118504.5
003634	04-Audio/Video	MILIND.KULVARNI@PHILIPS.COM	Wear leveling with global freelist	EP	6118440.2
005188	07-Identification	JUERGEN.SCHROEDER@PHILIPS.COM	Method and systems for effective cache performance based on data rmpm	EP	6110435.2
003873	24-Feb-06	JANGS.FARIKAS@EMAIL.AVAILABLE	Protocol negotiation	EP	6118431.1
005182	07-Identification	ANTON.SALFELNER@PHILIPS.COM	Multi-Step Surface Functionalization for Metallization of Semiconductor D	WO	EP2006.002862
004113	02-Packaging & Testing	ARNE.KRAEMER@PHILIPS.COM	Schleifengesteuerte UHF-RFID-Antenne	EP	6110388.3
005212	05-Mobile Communication	ANDY.YULE@PHILIPS.COM	Integration of Bias Magnets into semiconductor packages	EP	6118312.3
003776	05-Mobile Communication	XAVIER.XC.QIANG@PHILIPS.COM	Further enhancements of GPS for Cameras	EP	6110353.6
005129	05-Mobile Communication	ANMET.BASTUG@PHILIPS.COM	Joint compensation method for amplitude and phase imbalances in recent CM	CN	200810006861
004391	09-Discretes and Multimarke	PAUL.MATEMAN@PHILIPS.COM	Method and Apparatus for Improving the Speed of Convergence and Trac	EP	6300126.4
004391	17-Feb-06	MATTHIAS.SPODE@PHILIPS.COM	Exponential (linear in dB's) DAC	EP	6110216.6
004787	09-Discretes and Multimarke	ISABELLE.DURPES@PHILIPS.COM	Integrated two paths ESD-protection	EP	6110085.5
004685	09-Discretes and Multimarke	BART.HUISING@PHILIPS.COM	Improved Die-Die Comparisons for OPC Error Detection	WO	2006002576
005032	18-Feb-06	GENE.FELTEN@PHILIPS.COM	A CMOS input cell with enhanced hysteresis effect at low voltages	EP	6116022.6
004787	13-Feb-06	MARK.VAN.DAL@PHILIPS.COM	Non-Conductive Planarization of Substrate Surface for Mold Cap	US	60774119
004685	01-CMOS and embedded p...	VIET.NGUYENHOANG@PHILIPS.COM	Method for obtaining dual work functions, independent gates for PMFET a	EP	6101603.6
004389	05-Mobile Communication	ANDREA.ANCORA@PHILIPS.COM	Horizontal etching of dielectric materials for advanced interconnect opto	EP	6101594.7
005016	13-Feb-06	OSKAR.VI7@STUDENT.LIU.SE	Methods and Apparatus for HSDPA Symbol Level Equalization	EP	6300128.3
004187	10-Feb-06	JOE.VANEN@PHILIPS.COM	'Common start' Pulse Width Modulation (PWM) encoding for buses	EP	6101602.8
005103	19-Feb-06	IGOR.BLEDNOV@PHILIPS.COM	FOCUS (Focus On Center Using Spheres)	EP	6101552.5
004148	9-Feb-06	JOACHIM.STACHE@PHILIPS.COM	F-class amplification with wide dynamic range	EP	6101513.7
004392	9-Feb-06	GIANCARLO.CUTRIGNELLI@PHILIPS.COM	Dielectric protection layer for precision resistors in integrated circuits	EP	6101583.6
005081	8-Feb-06	JOEN.WESTENDORP@PHILIPS.COM	INTEGRATED CIRCUIT HAVING A LOW-POWER RANDOM ACTIVE SIEP	EP	6101468.8
004813	3-Feb-06	OLAF.HIRSCH@PHILIPS.COM	Power-down detection circuit	EP	6101465.8
004988	3-Feb-06	STEVE.SHEARER@PHILIPS.COM	GSM spurious emission cancellation	US	60771105
003329	04-Audio/Video	HANG.TICHELAAAR@PHILIPS.COM	A Simple method for WLAN Transmit Power control	US	60765912
004008	09-Discretes and Multimarke	WOLFGANG.SCHNITT@PHILIPS.COM	HDTV main video flow tap-off secondary scaler device for recording or di	EP	6101375.3
003938	26-Jan-06	XAVIER.XC.QIANG@PHILIPS.COM	Method and Apparatus for Improving the Speed of Convergence and Trac	EP	6301015.1
004276	31-Jan-06	OLIVIER.DUBREUIL@ST.COM	Cost Saving P-Used Chemical Dispenses System	EP	6300388.8
004276	31-Jan-06	JOHAN.DONKERS@PHILIPS.COM	Raised Extrinsic - and Elevated Source-Drain Bipolar Transistors	EP	6101072.4
004276	31-Jan-06	PMEIJER@CATEENA.NL	High/Low side LO injection for dual tuner phase diversity system	EP	6101104.5
004276	31-Jan-06	SEBASTIEN.NUTTINC@PHILIPS.COM	Self-protecting current mirror	EP	6101103.7
004276	31-Jan-06	LIM.RUYIYONG@PHILIPS.COM	A method to integrate dual workfunction for the gate of 3D-Stacked-FinFE	EP	6101021.1
004276	27-Jan-06	XAVIER.XC.QIANG@PHILIPS.COM	+33V Generator For Dual Tuners Application	EP	6100971.8
004276	26-Jan-06	WOLFGANG.SCHNITT@PHILIPS.COM	Subsampling receiver architectures	CN	2,006,1E+11
004008	26-Jan-06	WOLFGANG.SCHNITT@PHILIPS.COM	Verfahren zur Herstellung integrierter Dispersiver Bauelemente	EP	6100869.4
004069	25-Jan-06	J.P.FERRING@PHILIPS.COM	Prediction of Integrated circuits comprising semiconductor incompatible I	EP	6100371
0043584	25-Jan-06	YANKLE.GULLOU@PHILIPS.COM	Reduction of interference between RF and BB sub-section of a mobile ph	EP	6300069.9
002761	25-Jan-06	FRED.HURKX@PHILIPS.COM	Digital self-calibration for continuous-time sigma delta ADC.	EP	6300070.7
004788	25-Jan-06	FRED.HURKX@PHILIPS.COM	A nanowire tunnelling transistor	EP	6100814
004849	24-Jan-06	PIETER.HOCHSTENBACH@PHILIPS.COM	Reappraisal in BiCMOS + BONA	EP	6100816.5
003812	23-Jan-06	GERBEN.W.DE.JONG@PHILIPS.COM	Polar & Cartesian Transmitter via up-mixing 3-state PWM where PA-swit	EP	6100713.4

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Patent No.	Date	IPC Class	Applicant	Inventor	Title	Pub No.
003888	20-Jan-06	04-Audio/Video	EWOUT BRANDSMA@PHILIPS.COM		A low-cost low-delay temporal frame-rater producing fully MPEG-compatible Connected Secured Car Locking System	6100668
004126	18-Jan-06	08-Adaptive	PRAKASH MOHAPATRA@PHILIPS.COM		Integration of self-aligned trenches in-between metal lines	6100609.4
000184	18-Jan-06	01-CMOS and embedded pr	LAURENT GOSSET@PHILIPS.COM		Compact two-stage amplifier with class-AB output stage	6290126.1
004602	16-Jan-06	04-Audio/Video	PAUL BRUNJ@PHILIPS.COM		Radio communication system - superimposed pilots for OFDM	6100521.1
005010	16-Jan-06	05-Mobile Communication	ANDREA ANCOR@PHILIPS.COM		Unified Memory Protection Architecture	6300098.2
004895	16-Jan-06	02-Packaging & Testing	BART VERMEULEN@PHILIPS.COM		Novel Slope Processing, realized by Passive Polyphase Filtering	6100454.5
003698	16-Jan-06	04-Audio/Video	AXEL KATNER@PHILIPS.COM		Cost-effective 88dB cooling scheme for serial data communication	6100400.8
004853	12-Jan-06	05-Mobile Communication	GERRIT DEN BESTEN@PHILIPS.COM		Frontside contact to SOI wafer	6100380.3
002714	12-Jan-06	08-Discretes and Multimarke	PIEBE ZULSTR@PHILIPS.COM		Slow wave transmission line structure suitable for standards RF (3x)CMO	6100148.1
002711	9-Jan-06	01-CMOS and embedded pr	LUUK TIEMEIJER@PHILIPS.COM		Method of and an arrangement for characterizing non-linear behaviour of IC inductor	6100158.5
003788	9-Jan-06	01-CMOS and embedded pr	LUUK TIEMEIJER@PHILIPS.COM		Test circuit and method for testing of power switches	6100257.4
004956	9-Jan-06	02-Packaging & Testing	SANDEPRKUMAR GOEL@PHILIPS.COM		Feedback configuration for single-ended class-D amplifiers with asymmet	6100146.3
004797	9-Jan-06	04-Audio/Video	MARCO BERTHOUD@PHILIPS.COM		Switch for speed inter-clock domain testing of ICs	EP06100198.2
004156	6-Jan-06	02-Packaging & Testing	TOM WAAYERS@PHILIPS.COM		True Local-Silicon-On-Insulator Technology for RF CMOS Transistors	6100189.5
003930	5-Jan-06	01-CMOS and embedded pr	SEBASTIEN NUTTRICK@PHILIPS.COM		Method and system for effective interrupt distribution in a symmetric multi	6075684.24
003816	4-Jan-06	08-Discretes and Multimarke	MILIND KULKARNI@PHILIPS.COM		Ultra Wideband Notch Antenna	6300004.6
003910	3-Jan-06	07-Identification	KLEMENS BREITFUSS@PHILIPS.COM		Kombination zweier phasensynchronisieriger Take in einem Signal	6100035.2
003550	2-Jan-06	03-RF Devices	PETER MASSEY@PHILIPS.COM		Diodeic spacers on top metal lines for robust process integration and re	6300004.6
003249	28-Dec-05	01-CMOS and embedded pr	MOHAMED AIMADEDDINE@ST.COM		Low-Complexity Low-Power IEEE 802.11a WLAN Receiver Architecture	EP 5626549.3
004182	28-Dec-05	06-Connectivity	JENS DAVID@PHILIPS.COM		Using TX Buffer to reduce IMS spurious	EP 530111.1
003870	28-Dec-05	05-Mobile Communication	DOMINIQUE DELBECQ@PHILIPS.COM		Method for reducing temperature drift of a MEMS resonator	EP 5112038.5
004178	25-Dec-05	03-RF Devices	J.T.M.VAN BEEK@PHILIPS.COM		Method for reducing transition gap of MEMS resonator	EP 5112043.5
004204	25-Dec-05	03-RF Devices	J.T.M.VAN BEEK@PHILIPS.COM		Performance Analysis based System level Power Management	US 60755398.3
002878	23-Dec-05	04-Audio/Video	NAGARAJI BUSSA@PHILIPS.COM		An AV renderer peripheral with dual interrupt lines for staggered interrupt	US 60755398.0
002868	23-Dec-05	04-Audio/Video	PURANJOY B@PHILIPS.COM		Method and system for dynamic cache management	US 60755398.6
003817	23-Dec-05	04-Audio/Video	MILIND KULKARNI@PHILIPS.COM		Flow control mechanisms on synchronous serial TDMA bus	EP 5112936.9
004676	23-Dec-05	05-Mobile Communication	XAVIER LAMBERT@PHILIPS.COM		Formation of on chip multi-level central circulation for interconnect cooling	EP 5301163.7
003270	23-Dec-05	10-Other	LAURENT GOSSET@PHILIPS.COM		SONOS memory with gas arc	EP 5112822.1
003654	22-Dec-05	01-CMOS and embedded pr	ROB VAN SCHALK@PHILIPS.COM		RF MEMS capacitors with improved power handling	EP 5112831.2
003661	22-Dec-05	03-RF Devices	PETER STEENEKEN@PHILIPS.COM		Nevel Configuration of Tunable Devices with Variable Tuning Range and	EP 5112842.9
002709	22-Dec-05	04-Audio/Video	ACHIM HILGERS@PHILIPS.COM		LCD Display Driver Circuit with Internal Down-conversion of Low-Voltage	EP 5112730.6
003208	22-Dec-05	04-Audio/Video	ANDY NEGRO@PHILIPS.COM		Printer structure with low wear	EP 5112732.2
003687	22-Dec-05	08-Discretes and Multimarke	JAN SONSRY@PHILIPS.COM		High voltage devices with an active stripe as field plate in SOI CMOS te	EP 5112801.5
004430	22-Dec-05	10-Other	JAA SONSRY@PHILIPS.COM		SI-membrane processing and novel substrate transfer	EP 5112835.3
003749	21-Dec-05	02-Packaging & Testing	MUNESH NAIR@PHILIPS.COM		Delay Measurement of IC cells	EP 5112855.5
003048	21-Dec-05	04-Audio/Video	SCOTT GLUD@PHILIPS.COM		Method and apparatus for mobile display interfaces	US 60752856
002907	21-Dec-05	04-Audio/Video	VICTOR VAN ACHT@PHILIPS.COM		Power-failure-robust updating	EP 5112840.7
002245	21-Dec-05	04-Audio/Video	VICTOR VAN ACHT@PHILIPS.COM		Optimal way of soldering reworking tabs	EP 5112840.7
003874	21-Dec-05	04-Audio/Video	SAINATH KARAPALEM@PHILIPS.COM		Method and apparatus for power aware dynamic paritioning of cache	US 60752857
002880	21-Dec-05	05-Mobile Communication	SAINATH KARAPALEM@PHILIPS.COM		Schedule based Cache/Memory Power Minimization Technique	US 60752858
003755	21-Dec-05	05-Mobile Communication	QIMAR KERGERT@PHILIPS.COM		Increased Medium Efficiency of DECT base stations with Blind Slot R	EP 5112828.2
003656	20-Dec-05	01-CMOS and embedded pr	ZORAN MILICAVLJEVIC@PHILIPS.COM		Smart SMS	EP 5112560.7
002657	20-Dec-05	02-Packaging & Testing	LUDOVIC GLOUX@IMEC.BE		A vertical PCM cell	US 60752739
001900	20-Dec-05	04-Audio/Video	ROLAND BRANDL@PHILIPS.COM		DC powering triggers backscatter when chip is fully functional	EP 5112528.4
002752	20-Dec-05	06-Connectivity	MARCO BEKOOL@PHILIPS.COM		Memory defragmentation unit for real-time systems	EP 5112468.3
003759	19-Dec-05	01-CMOS and embedded pr	JAN PETER FRAMBACH@PHILIPS.COM		3f Noise Cancellation Coupled Sawtooth Oscillator	EP 5112450.4
003744	19-Dec-05	01-CMOS and embedded pr	RADU SURDEANU@PHILIPS.COM		Method of contact fabrication for thin body Si devices	EP 5112432.9
002913	19-Dec-05	05-Mobile Communication	TEO LETAVIC@PHILIPS.COM		A Substrate Isolated Integrated High Voltage Diode with Silicon Trench	US 60751532
002912	19-Dec-05	05-Mobile Communication	BALA JIMANIGANDAN@PHILIPS.COM		Informative Messages for switched off mobile phone	EP 5301079.9
002910	19-Dec-05	05-Mobile Communication	AHMED BASTU@PHILIPS.COM		Methods and Apparatus for HSDPA Chip Level Equalization	EP 5301071.6
002909	19-Dec-05	06-Connectivity	AHMED BASTU@PHILIPS.COM		Decision Directed Equalization for HSDPA	EP 5300391.7
002911	19-Dec-05	07-Identification	WOLFRAM DRESCHER@PHILIPS.COM		Method and apparatus for an exact timing compatible MB-04 UWB trans	EP 5112468.9
003383	18-Dec-05	09-Discretes and Multimarke	TOMY KERSELAERS@PHILIPS.COM		Near Field Communication Method	EP 5112372.7
002882	18-Dec-05	04-Audio/Video	TED LETAVIC@PHILIPS.COM		An asymmetric semiconductor device structure with improved reliability	US 60751501
002662	16-Dec-05	05-Mobile Communication	ARIE KOPPELAAR@PHILIPS.COM		Colour shift compensation in LTPS displays through smart source slice	EP 5112275.2
002814	16-Dec-05	05-Mobile Communication	FRANK VAN DER VELDEN@PHILIPS.COM		Power saving by early switch-off of Receiver Front-end	EP 5112276.6
002815	16-Dec-05	05-Mobile Communication	JAN VROMANS@PHILIPS.COM		Battery Full detection for Charge and Play circuits	EP 5112344.7
002665	16-Dec-05	06-Connectivity	VOLKER AUE@PHILIPS.COM		PLL Multi-Mode Modulator	EP 5112311.5
004408	15-Dec-05	01-CMOS and embedded pr	ROB VAN SCHALK@PHILIPS.COM		Symbol time error estimator for OFDM with applications to DVB-T/H	EP 5112274.5
004046	15-Dec-05	02-Packaging & Testing	CHRIS WYLAND@PHILIPS.COM		Memory devices with 'quasi' vacuum tunnel dielectrc	EP 5112188.7
002613	15-Dec-05	05-Mobile Communication	FRANK VAN DER VELDEN@PHILIPS.COM		Enhanced Substrate Using Metamaterials	US 60751095
002816	15-Dec-05	05-Mobile Communication	GERBEN W.DE JONG@PHILIPS.COM		Battery recharge prevention principle for short battery voltage dips	EP 5112211.7
002816	15-Dec-05	05-Mobile Communication	GERBEN W.DE JONG@PHILIPS.COM		Polar & Cartesian Transmitter via up-mixing 3-state PWM where PA-switch	EP 5112208.1

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Patent No.	Date	Inventor	Title	IPC Class.	Pub No.
004592	15-dec-05	OLAF HIRSCH@PHILIPS.COM	WLAN, Bluetooth, GSM coexistence synchronization	US	60751173
002708	15-dec-05	ROLAND BRANDL@PHILIPS.COM	Combine ESD Structure and Limiter Transistor	EP	51122433
004436	14-dec-05	STEVEN THEEDWEN@PHILIPS.COM	Starcase shield for CMOS	EP	51131685
002764	14-dec-05	OZGUN PAKER@PHILIPS.COM	A low-cost near optimum non-linear synaptic detector for MIMO receivers	EP	51121416
002154	14-dec-05	GEERT JAN SCHRIJVER@PHILIPS.COM	On-chip estimation of key-extraction parameters for coding PLUFs	EP	51121716
002522	14-dec-05	MICHAEL RAUBER@PHILIPS.COM	Methode zur Anpassung u. Optimierung u. Datenübertragung durch unterschiedl. EP	05	112 093.9
001135	13-dec-05	MARKUS MULLER@PHILIPS.COM	Circuits with dual work functions for better short channel effect control and	EP	5292650.8
003913	13-dec-05	GILBERTO CURATOLO@PHILIPS.COM	Diels-Ioffe Junction FET	EP	5112076.4
004070	13-dec-05	KRISTOF VAN RECK@PHILIPS.COM	Harmonics creation with transient removal	EP	5112028.9
002668	13-dec-05	WINFRIED BIRTH@PHILIPS.COM	Improvement for GMSK TX spectra	EP	5112008.7
001895	13-dec-05	LUCIEN BREEMS@PHILIPS.COM	RF Sigma-delta A/D Conversion	EP	5112001.2
001695	12-dec-05	WOLFRAM DRESCHER@PHILIPS.COM	Architecture for a MEMS-UWB transmitter with support of spectrum frame EP	5111990.7	
003606	12-dec-05	WOLFRAM DRESCHER@PHILIPS.COM	MBOA UWB Digital Base Band burst control mechanism	EP	5111992.3
003954	12-dec-05	ROBERT SPINDLER@PHILIPS.COM	Power saving by synchronizing and switching multiphased clocks	EP	5111954.3
003955	12-dec-05	ROBERT SPINDLER@PHILIPS.COM	Power saving by synchronizing and switching multiphased clocks	EP	5111956.8
001887	12-dec-05	ROBERT SPINDLER@PHILIPS.COM	Mismatch mitigation system for full-flash ADCs	EP	5111990.8
003698	8-dec-05	MARCEL PELGRAIM@PHILIPS.COM	Build High-Voltage Power Switches Using Standard Low-Voltage CMOS	EP	5301026
002176	7-dec-05	YANN LE GUILLOU@PHILIPS.COM	Selective deposition of ALD barriers	EP	5301018.5
003123	7-dec-05	NICK LAMBERT@PHILIPS.COM	High density memory cell topology using vertical access transistor	EP	5111771.6
002326	8-dec-05	KARL-RAGMAR RIEMSCHEIDER@PHILIPS.COM	Control method and circuit of matching of transponder antennas	EP	5111795.2
003727	8-dec-05	ARMET BASTIUC@PHILIPS.COM	Determination of active spreading codes and their power through Fast W/EP	5301016.4	
002367	8-dec-05	YANN LE GUILLOU@PHILIPS.COM	Enhanced Sigma-delta ADC performance by introducing a dead zone in EP	5301014.6	
002368	5-dec-05	YANN LE GUILLOU@PHILIPS.COM	A Dynamic Element Matching Technique For Complex Multi-Bit Sigma-Delta EP	5301005.9	
004423	1-dec-05	JEAN-FRANCOIS LEFERRE@PHILIPS.COM	Highly digitized wireless receiver	EP	5301002.1
004360	2-dec-05	Olivier Grand@PHILIPS.COM	NCM C2 - Boundary scan insertion flow	EP	5300996.5
003898	2-dec-05	Pen Li@PHILIPS.COM	LNA with transistormer	EP	5300996.1
004437	2-dec-05	CHARLES RAZZELL@PHILIPS.COM	MIMO OFDM transmit beamforming with explicit feedback modelled by an US	60741825	
004438	2-dec-05	CHARLES RAZZELL@PHILIPS.COM	Sweep Notch UWB Transmitter	US	60741858
004439	1-dec-05	CHARLES RAZZELL@PHILIPS.COM	OFDM Cognitive Radio with Zero Overhead Signalling of Deleted Subcar US	60741813	
004348	1-dec-05	DIETMAR GASSMANN@PHILIPS.COM	Efficient matrix access to general purpose memory	EP	5111548.7
004360	1-dec-05	GIAN HOOZAAD@PHILIPS.COM	Soft Commutation	EP	5111572.3
001167	30-nov-05	MOHAMED BOUHAMME@PHILIPS.COM	A new discharge system for a high voltage DCDC converter in low voltage EP	5300978.2	
002060	30-nov-05	JUDITH RICHARDS@PHILIPS.COM	Automated Timing Constraint Merging in Hierarchical SoC Designs	US	60744003
003869	29-nov-05	WAYNE NUNN@PHILIPS.COM	Bumpless Flip Chip Assembly with a Compliant Interposer Connector	US	60746872
004351	29-nov-05	BERNARD VAN VLIJMEREN@PHILIPS.COM	CC-A zero overhead mechanism for detecting bus synchronization	EP	5111413
002531	28-nov-05	LEO POLL@PHILIPS.COM	GPS secured memory device	EP	5111420
002532	28-nov-05	WIM BRESLING@PHILIPS.COM	Self aligned Cu-alloy line capping to improve electromigration and stress EP	5300968.2	
003820	28-nov-05	REINHARD ROGY@PHILIPS.COM	Fabrication of self-aligned Schottky junctions by metal deposition through EP	5300871.8	
004309	25-nov-05	BOB MILSON@PHILIPS.COM	Robust & high speed assembly process	EP	05 111 387.6
003876	25-nov-05	NICOLAS REGENT@PHILIPS.COM	Low spurious SAW filter by frequency-offset reflector	EP	5111314
003786	24-nov-05	ACHIM HILGERS@PHILIPS.COM	Low cost changing system with application current consumption compare EP	5300565	
002123	23-nov-05	JAVI SINDRENA@PHILIPS.COM	RFID-Transponder with Sensing Functionality	EP	5111251.4
004029	23-nov-05	EWA HEKSTRA-NOWACKA@PHILIPS.COM	Concept for high linear and low noise continuous AGC	EP	5300861.9
004275	23-nov-05	GERBEN W. DE JONG@PHILIPS.COM	Error recovery for asynchronous data in the chip-to-chip networks adhering EP	5111125.8	
003602	21-nov-05	GERRIT DEN BESTEN@PHILIPS.COM	Efficient Polar Modulation via bit-passing PPM where PA-switching-freq EP	5111151.6	
002762	18-nov-05	JOOST MELAI@PHILIPS.COM	RX clocking by NOP signalling via a self-clocking link without sending data EP	5111110.2	
003117	18-nov-05	PRABHAT AGARWAL@PHILIPS.COM	The oversharing bipolar A robust and simple integration scheme for self-EP	5110897.3	
004085	18-nov-05	MHAJ SANKUDEVANU@PHILIPS.COM	A metal-base narrowband transistor	EP	5110934.6
004085	18-nov-05	MILEN PENEV@PHILIPS.COM	Polar modulator with static DC-DC converter and power efficiency lineart EP	5110828.8	
004085	18-nov-05	GIAN HOOZAAD@PHILIPS.COM	TFT LCD DRIVING TECHNIQUE WITH REDUCED POWER CONSUMPTION EP	5110846	
004085	18-nov-05	ROMAN HOORAN@PHILIPS.COM	Single wire digital interface power driver	EP	5110979.1
004085	17-nov-05	PETER SCHOTTE@PHILIPS.COM	A novel moisture sensor using Interline leakage in porous low-k dielectrics EP	5110874.4	
004085	17-nov-05	PETER SCHOTTE@PHILIPS.COM	Efficient folding using current controlled tail sources	EP	5110830.6
004085	16-nov-05	V. JAYARAGHAVAN MR@PHILIPS.COM	MIS mediated semiconductor nanoparticles growth: an alternate and easy EP	5110768.6	
002758	15-nov-05	V. JAYARAGHAVAN MR@PHILIPS.COM	Top contacts to vertically standing nanowires: An integration scheme EP	5110790.2	
002110	15-nov-05	PETER KEVITS@PHILIPS.COM	Power reduction method for instructions fetched in two or more parts EP	5110764.7	
002117	15-nov-05	JEAN-PAUL SWEETS@PHILIPS.COM	Power reduction in register based instruction sets	EP	5110767
002118	15-nov-05	DAVID LEANE@PHILIPS.COM	Power reduction method when shuffling vectors on a per element basis EP	5110769.6	
002998	14-nov-05	GILBERTO CURATOLO@PHILIPS.COM	Novel vertical FinFET	EP	5110721.7
002985	14-nov-05	ERENT BUCHANAN@PHILIPS.COM	Method for Analyzing Voltage Drop in Power Distribution Across an Integ. US	607536266	
004117	14-nov-05	HAROLD VRAHNEAN@PHILIPS.COM	Space Compaction Logic for Compaction of Test Responses with Accurate EP	5110725.8	
003876	14-nov-05	SUSANNE WINDISCHBERGER@PHILIPS.COM	Mode engineering of correlated oscillations and wave propagation	EP	05 110 716.7
001808	11-nov-05	KATHLEEN PHILIPS@PHILIPS.COM	Calibration of a continuous-time sigma-delta ADC in low-IF receivers	EP	5110661.5
001895	11-nov-05	FRIEDEL GERFERS@PHILIPS.COM	DLL BASED HIGH-SPEED ANALOGS-TO-DIGITAL CONVERTER	EP	5110666.4

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002757	10-nov-05	07-Identification	ACHIM.HILGERS@PHILIPS.COM	Dual Band RFID-Transponder	EP	5110918,5
002289	10-nov-05	05-Antennas	BRUNO.ZINKE@PHILIPS.COM	Bus Guardian with improved Channel Monitoring	US	60735950
002523	8-nov-05	05-Mobile Communication	GHOLAMALI.HADDAD@PHILIPS.COM	Frequency-response optimization of vibrating plate by damping	EP	5110528,8
002694	8-nov-05	02-Packaging & Testing	VICTOR.KAN@PHILIPS.COM	Leadframe-Based IC-Package with Supply-Reference Comp	US	60735104
001693	8-nov-05	03-RF Devices	FRED.HOOZEBOOM@PHILIPS.COM	Ultrahigh-Frequency and Flaring Capacitors in Silicon for Decoupling and Ch	EP	5110488,3
001924	8-nov-05	03-RF Devices	JOHAN.KUDDOOLIK@PHILIPS.COM	Viola filling using a temporary cap layer over (blind) vias	EP	5110486,9
001925	8-nov-05	03-RF Devices	MARION.MATTERS@PHILIPS.COM	High frequency trench capacitor	EP	5110471,9
004077	8-nov-05	05-Mobile Communication	FRANCOIS.CHANCEL@PHILIPS.COM	Flag registers as an optimized solution for dual core synchronization	EP	EP08300900,7
004033	4-nov-05	02-Packaging & Testing	HARALD.VRANKEN@PHILIPS.COM	Space Compression Logic for Comparison of Test Responses with Accuracy	EP	5110397,7
001364	4-nov-05	08-Connectivity	TIM.FONTJUS@PHILIPS.COM	Alignment and deskew for multiple lanes of serial interconnect	US	NO_SIN_YET
002883	3-nov-05	02-Packaging & Testing	PAUL.DUKSTRA@PHILIPS.COM	Source Treatments for Contact Pads Used in Semiconductor Chip Packag	US	607356651
002426	2-nov-05	04-Audio/Video	DAVID.WITTEBS@PHILIPS.COM	A method for synchronizing 2 sides of an interface with aid of a virtual sm	EP	5110292,9
003368	2-nov-05	05-Connectivity	TOM.WAAYERS@PHILIPS.COM	Testable configurable scan chain architecture	EP	5110277
002312	2-nov-05	05-Discretas and Multimar	DIETMAR.GASSMANN@PHILIPS.COM	Sharing general-purpose system memory with hardware accelerators	EP	5110258,1
001989	1-nov-05	01-CMOS and embedded p	LUDIK@NATLAB.RESEARCH.PHILIPS.CO	Multi-path LDMOS using P-type epitaxy	EP	5110255,0
002881	1-nov-05	02-Packaging & Testing	VICTOR.VAN.ACHT@PHILIPS.COM	High density memory cell topology using lateral access transistor	EP	5110221,8
002882	1-nov-05	02-Packaging & Testing	ROELF.GROENHUIS@PHILIPS.COM	An Air Cavity Package for a Semiconductor Die and Methods of Forming US	US	60732680
002125	1-nov-05	04-Audio/Video	PAUL.DUKSTRA@PHILIPS.COM	Methods of Packaging a Semiconductor Die and Die Package Formed by US	US	60732683
002638	31-ott-05	04-Audio/Video	BERIC@NATLAB.RESEARCH.PHILIPS.CO	Efficient L1 memory organization for video applications	EP	5110219,2
002129	28-ott-05	05-Mobile Communication	ARNAUD.BADEY_Z@PHILIPS.COM	Efficient decoding of Blu-ray Disc	EP	EP05110185,5
003366	28-ott-05	05-Connectivity	LAYACHILDANECH@PHILIPS.COM	Data packing processing	EP	5300878,5
003872	28-ott-05	05-Discretas and Multimar	WOLFRAM.DRESCHER@PHILIPS.COM	Method and architecture for a save system sleep mode of MBDA LUMB a	EP	5110137,8
FR040192	27-ott-05	05-Mobile Communication	ARJAN.STRIJKER@PHILIPS.COM	Follow boost Power Factor Correction circuit with increased accuracy at EP	EP	5110158,2
003956	27-ott-05	05-Discretas and Multimar	JEAN-ROBERT.TOURNET@PHILIPS.COM	RF AUTOMATIC CALIBRATION	WO	023005058516
003960	26-ott-05	03-RF Devices	AMIR.Z.IAJO@PHILIPS.COM	Smart Tracking circuit for Synchronous rectification in a switched mode p	EP	5110099,8
001522	26-ott-05	03-RF Devices	EFTIMIOS.TSILIOUKAS@PHILIPS.COM	Design for Testability of Analog/RF Circuits	EP	5110048,5
002528	25-ott-05	05-Mobile Communication	XAVIER.XC.QIAN@PHILIPS.COM	Combined active VHF/UHF Antenna for DVB-T reception in hand-held res EP	EP	5300865,2
002526	25-ott-05	01-CMOS and embedded p	GILBERTO.CURATOLA@PHILIPS.COM	Front-end similar architectures of the wireless communication receiver CN	2.8051E+11	
003917	25-ott-05	05-Mobile Communication	LAURENT.PILOT@PHILIPS.COM	Voltage controlled reconfigurable gate for FinFET technology	EP	5109835,4
003901	24-ott-05	07-Identification	FRANZ.AMTMANN@PHILIPS.COM	Advanced Reset Mechanism	EP	5300857,8
003903	24-ott-05	05-Packaging & Testing	TOM.WAAYERS@PHILIPS.COM	Datensträger wahlweise sich Antikollisionsparameter selbst	EP	5109817,2
003902	24-ott-05	05-Packaging & Testing	TOM.WAAYERS@PHILIPS.COM	Instruction register with improved test access (IEEE Std. 1500)	EP	5109832,8
002081	24-ott-05	02-Packaging & Testing	TOM.WAAYERS@PHILIPS.COM	At speed robust scan test transport (IEEE Std. 1500)	EP	5109633,7
001876	24-ott-05	05-Mobile Communication	JACCO.VAN.GURP@PHILIPS.COM	Testable instruction register (IEEE Std. 1500)	EP	5109694,5
001818	21-ott-05	05-Mobile Communication	JOCHEN.GARBE@PHILIPS.COM	Low-loss Motion vector field refiner	US	60730162
003728	19-ott-05	02-Packaging & Testing	PAUL.MATTHEUSSEN@PHILIPS.COM	Elektrische Schaltung zum Verhindern von Angriffen auf sicherheitsreleva EP	EP	5109698,4
001989	19-ott-05	04-Audio/Video	MAX.MARTIN@PHILIPS.COM	Genetic Analogue Polar Modulator Architecture An FM modulator EP	EP	5109622,6
001484	19-ott-05	05-Mobile Communication	MIKE.LOO@PHILIPS.COM	A simple circuit for improving the ESD performance of medium size MOS EP	EP	5109827,5
001875	18-ott-05	01-CMOS and embedded p	JAN.WILLEM.VAN.DE.WAERT@PHILIP	Redistribution Layer for Water Level Chip Scale Package and Method Th US	US	60724653
002128	18-ott-05	04-Audio/Video	LEX.HARM@PHILIPS.COM	Cache with high access store bandwidth, at low cost	US	60724576
003569	17-ott-05	05-Mobile Communication	HEIKE.NEUMANN@PHILIPS.COM	Loopback mesh wires	EP	5109748,3
004160	17-ott-05	07-Identification	MARKUS.MULLER@PHILIPS.COM	Constantly re-initialization of pseudo random number generator	EP	5109726,5
003917	17-ott-05	04-Audio/Video	ROBERTO.MANCUSO@PHILIPS.COM	Selective removal of silicon oxide hard masks without any extra photolith EP	EP	5109656,4
003917	17-ott-05	05-Mobile Communication	COLIN.KING@PHILIPS.COM	YUV pixel based algorithm	EP	5109694,9
003917	17-ott-05	05-Mobile Communication	ANDREA.ANCORA@PHILIPS.COM	Encryption of executable images for fast decryption and relocation	EP	5109642,8
003917	17-ott-05	05-Mobile Communication	BERNHARD.SPIESS@PHILIPS.COM	Low complexity un-biased SINR estimator for low number of pilots	EP	5300827,2
002810	17-ott-05	07-Identification	JEROME.LEGROS@PHILIPS.COM	Automatic antenna trimming for RFID base stations	EP	5109621,2
002385	14-ott-05	01-CMOS and embedded p	ROB.VAN.SCHAUJK@PHILIPS.COM	Unified management of air interfaces NFC applications (card behaviour) FR	FR	510575
003917	14-ott-05	03-RF Devices	THEO.RUIKS@PHILIPS.COM	Depletion type SONOS devices with a different bottom dielectric layer the EP	EP	5109588,3
003917	13-ott-05	05-Mobile Communication	WY.ZHU@PHILIPS.COM	Fast driving of a MEMS tunable capacitor	EP	5109574,3
003917	13-ott-05	05-Mobile Communication	MARK.T.JOHNSON@PHILIPS.COM	Method and Apparatus for RRC Filter Implementation in Multiple Antennas CN	00510113463,x	
003917	11-ott-05	05-Connectivity	GEERT.IAN.JORDENS@PHILIPS.COM	"Cut-fold-order" electronics on flexible substrate	EP	5109504
003917	7-ott-05	05-Discretas and Multimar	VICTOR.VAN.ACHT@PHILIPS.COM	Low skew clock distribution in multiple-lane serial interfaces using a case US	US	60725806
003917	6-ott-05	01-CMOS and embedded p	FABRICE.BLANC@PHILIPS.COM	Single threshold NIMOS-only pulse amplifier	EP	5109354
002810	6-ott-05	01-CMOS and embedded p	WOLFGANG.EJEN@PHILIPS.COM	GNMOS improvement in a Lateral Buried NPN for ESD purposes	EP	5109282,3
002385	5-ott-05	01-CMOS and embedded p	ALMA.ANDERSON@PHILIPS.COM	Process sequence to produce SOI wafers with low crystal defect density EP	EP	5109284,9
001529	5-ott-05	04-Audio/Video	JOOP.VAN.LAMMEREN@PHILIPS.COM	Multi-Layer Inductive Element for Integrated Circuit	US	60724246
001055	30-sep-05	01-CMOS and embedded p	OLAF.WUNNICKE@PHILIPS.COM	Storing teletext pages in a FLASH memory	EP	5109211,2
003660	30-sep-05	01-CMOS and embedded p	PHILIPPE.MURIER@PHILIPS.COM	Narrowband Magnetic-Random-Access-Memory	EP	5109047
002618	30-sep-05	02-Packaging & Testing	PETER.DIRKS@PHILIPS.COM	Implemented SDR mono-emitter formation	EP	5109046,2
003917	30-sep-05	03-RF Devices	J.T.M.VAN.BEEK@PHILIPS.COM	Fin-Pitch Reducing in a Lead Frame Based System-in-Package (SIP) Dev US	US	60723181
002103	30-sep-05	03-RF Devices	BOB.MILSON@PHILIPS.COM	Rezo-resistive MEMS oscillator	EP	5109061,1
001512	30-sep-05	04-Audio/Video	JERGEN.DEN.BREEJEN@PHILIPS.COM	Low-loss BAW filter by frequency-shifted radiation bars	EP	5109109,8
002019	30-sep-05	04-Audio/Video	PHILIPPE.TEUWEN@PHILIPS.COM	Parallel Scattering: A Way of Dynamic Radiation Scattering with the use of a SW EP	EP	5109090
				Unalterable watermark	EP	5109056,7

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0020614	05-Mobile Communication	JEROME DURAND@PHILIPS.COM	Time reduction of initial cell search in WCDMA (3G)	EP	5300754.5
0020637	09-Discrettes and Multimark	ADAM BROWN@PHILIPS.COM	Commutated gate for use in powerMOSFETs	EP	5106031.4
0020646	01-CMOS and embedded	GERBEN DOORNBOOS@PHILIPS.COM	Asymmetric Dual Gate Flash Cell	EP	5108972
0020647	01-CMOS and embedded	PIERRE GOARING@PHILIPS.COM	Fin Flash Cell with double channel	EP	5108975.3
0020653	01-CMOS and embedded	BRENT BUCHANAN@PHILIPS.COM	Circuit Arrangement for Signal Transmission between Voltage Domains	US	60721386
0021119	27-sep-05	HERVE MARIE@PHILIPS.COM	Electronic selection of tested die	EP	5300776.7
002121	27-sep-05	HERVE MARIE@PHILIPS.COM	Test structures in scribe lane	EP	5300779.5
0022638	27-sep-05	PAUL DIJKSTRA@PHILIPS.COM	Thin Profile Packaging with Exposed Die Attach Adhesive	US	60721387
001160	27-sep-05	PIERRE DEMAJ@PHILIPS.COM	Joint speed and frequency offset estimator for UMTS	EP	5300776.1
001873	27-sep-05	SCENKE OSTERTUNG@PHILIPS.COM	Konfigurations Fehlerkorrekturschaltung für sichere Codes-Ausführung	EP	5108915.9
001620	26-sep-05	PAUL BRUJING@PHILIPS.COM	Over-current detection to avoid excessive self-heating in integrated SOI x EP	EP	5108959.1
001418	01-Audio/Video	WIM BESLING@PHILIPS.COM	ALD barrier process for CVD Cu superfill	EP	5108959.8
002046	01-CMOS and embedded	ROB VAN SCHAIJK@PHILIPS.COM	Improved performance SONOS memories by using PMOSFET devices w EP	EP	5108904.5
0020282	03-RF Devices	WALLACE LIU@PHILIPS.COM	Master clock hand-over mechanism in an inter-IC communication bus	EP	5108725.2
002427	05-Mobile Communication	CHRIS MARSHALL@PHILIPS.COM	Preliminary GPS signal processing	EP	5108696.0
001505	04-Audio/Video	ANDREW STEVENS@PHILIPS.COM	Methods for synchronising, frame-rate preserving reformatting of video	EP	5108635.3
002282	08-Discrettes and Multimark	VICTOR VAN ACHT@PHILIPS.COM	Single-threshold NMOS-only logic	EP	5108635.6
002574	05-Mobile Communication	SCOTT GUD@PHILIPS.COM	Method for adaptive error correction and apparatus provided with such an US	US	60719893
002575	05-Mobile Communication	SCOTT GUD@PHILIPS.COM	Multi protocol encapsulation forward error correction with minimized error US	US	60719890
002576	05-Mobile Communication	SCOTT GUD@PHILIPS.COM	Method for extracting residues for forward error correction in TV on mobile US	US	60719892
002948	05-Mobile Communication	PHILIPPE GENTRIC@PHILIPS.COM	local media sync	EP	5300756.3
002949	05-Mobile Communication	YIFENG ZHANG@PHILIPS.COM	Power efficient wireless system by clock synchronization	US	60719892
002953	06-Connectivity	FRIEDEL GERFFERS@PHILIPS.COM	Adaptive Equalizer Tuning Algorithm and Phase Selection Procedure	EP	5108923.9
001006	01-CMOS and embedded	WIM BESLING@PHILIPS.COM	ULK pore sealing strategy with a transformable, dielectric ALD Ti6Si6 ba EP	EP	5300749.8
002289	01-CMOS and embedded	ROBERT LANDER@PHILIPS.COM	Method for combining deposited metal and fully-silicided gate electrodes EP	EP	5108495.2
002381	01-CMOS and embedded	MARK VAN DAL@PHILIPS.COM	CMP-less dual metal FUSIMOS CMOS integration flow	EP	5108496.6
001031	08-Automotive	PETER FUHRMANN@PHILIPS.COM	Film uniformity enhancement by wet cleaning process on Single Wafer at EP	EP	5300746.4
003062	01-CMOS and embedded	TIM PONTIUS@PHILIPS.COM	Transmit queuing for identifier-based MAC schemes	EP	5108372.3
002373	02-Packaging & Testing	TIM LANGE@PHILIPS.COM	Power Management for Buses in CMOS Circuits	US	60716730
002479	06-Mobile Communication	PHILIPPE GENTRIC@PHILIPS.COM	Development of Assertions for Integrated Circuit Design Simulation	US	60716391
005750	05-Mobile Communication	SEGIT LANGERIS@PHILIPS.COM	Face recognition for delay mode switch in video conferencing and push to EP	EP	5300741.5
001807	07-Identification	DANIEL ARNITZ@PH-JOANNEUM.AT	Miniaturised intramechanical components fabricated using foil technology EP	EP	6112803.7
001546	08-Discrettes and Multimark	JACCO VAN DE VELDEN@PHILIPS.COM	Method zur exaktien Start-Of-Fremp Erkennung mit minimierten Rechen EP	EP	5108322.4
001584	02-Packaging & Testing	LAURENT SOUFER@PHILIPS.COM	Initial phase selection	EP	5108291.5
001843	01-CMOS and embedded	LAURENT SOUFER@PHILIPS.COM	Li-TRA SHIFT SCAN TEST MODE	EP	5300728.2
001844	01-CMOS and embedded	PETER ZANDBERGEN@PHILIPS.COM	Resist performance enhancement by dark area cross-linking	EP	5108163.6
002028	01-CMOS and embedded	PETER ZANDBERGEN@PHILIPS.COM	Lithographic frequency doubling by modifications in resist chemistry	EP	5108172.7
002029	01-CMOS and embedded	PETER ZANDBERGEN@PHILIPS.COM	Method to integrate critical SiGe isolation technology to CMOS	EP	5108168.4
001562	04-Audio/Video	ROHINI KRISHNAN.270833@PHILIPS.COM	Method to integrate critical SiGe isolation technology to CMOS	EP	5108124.6
002175	04-Audio/Video	PAUL WIELAGE@PHILIPS.COM	Compact hardware unit for signed multiplication on FPGAs	EP	5108122.2
002465	04-Audio/Video	HANS BREKELMANS@PHILIPS.COM	Area-efficient multiplier based logic blocks for FPGAs	EP	5108127.1
001774	04-Audio/Video	ROLAND BRANDI@PHILIPS.COM	A fast and robust asynchronous triple pipeline	EP	5108102.4
002528	07-Identification	EWALD BERGLER@PHILIPS.COM	Finite impulse response digital to analog converter with improved accuracy EP	EP	5108093.5
002802	01-CMOS and embedded	JANDRARKAS@NO.EMAIL.AVAILABLE	Variable gain amplifier coupled to a tunable LC resonant circuit	EP	5108037.2
002903	01-CMOS and embedded	LAURENT GOSSET@PHILIPS.COM	Fast capacitor of charge pump connected to ground	EP	5108037.2
002904	07-Identification	FRANCESCO GALLI@PHILIPS.COM	DC-Envelope Operation for Loop Antenna HF Chips	EP	5108037.2
002907	07-Identification	DANIEL ARNITZ@PH-JOANNEUM.AT	Pore sealing method for porous dielectric material	WO	EP20051010688
002547	05-Mobile Communication	ZHENHUA WANG@PHILIPS.COM	Pre-clean step for selective electroless deposition	WO	EP20051011128
002548	05-Mobile Communication	ZHENHUA WANG@PHILIPS.COM	Packaging solutions for above-IC and FEOL components in Integrated Ch EP	EP	5281738.5
002549	05-Mobile Communication	FABRIEN LEFEBVRE@PHILIPS.COM	GAP_TIMEOUT Estimation	EP	5107606.3
002550	05-Mobile Communication	ZHENHUA WANG@PHILIPS.COM	Methods zur adaptiven Nachführung von Schwellwerten in der Signalrate EP	EP	5107606.1
002551	05-Mobile Communication	ZHENHUA WANG@PHILIPS.COM	DC-DC Switching Converter with an One-Cycle Controller	EP	5300693.2
002552	05-Mobile Communication	FABRIEN LEFEBVRE@PHILIPS.COM	Switched-Mode Power Supply with Stable Power Efficiency and Confined EP	EP	5300693.8
002553	05-Mobile Communication	JAN VAN SINDEREN@PHILIPS.COM	Low cost solution to prevent software use with non-authorized chips	EP	5107757.6
002554	05-Mobile Communication	JAN VAN SINDEREN@PHILIPS.COM	Splitter configuration for multi-tuner applications	EP	5300684.7
002555	05-Mobile Communication	BERTRAND VAN DE WIELE@PHILIPS.COM	Microcontroller Waveform Generation	US	60710652
002556	05-Mobile Communication	BERTRAND VAN DE WIELE@PHILIPS.COM	Embedded Memory Protection	US	60710648
002557	05-Mobile Communication	SANDER VAN RUNSWOU@PHILIPS.COM	Controlling Embedded Memory Access	US	60710647
002558	05-Mobile Communication	SANDER VAN RUNSWOU@PHILIPS.COM	Test sequences for optimal RF-MS tests	US	5107630.5
002559	05-Mobile Communication	WOLFGANG BURH@PHILIPS.COM	Method for accelerating key generation	EP	5107614.9
002560	05-Mobile Communication	GIEL VAN DOREN@PHILIPS.COM	Light-attack-detection mechanism for NV-memories with randomized acc EP	EP	5107615.6
002561	05-Mobile Communication	RAINER MINTZLAFF@PHILIPS.COM	Method and apparatus for video encoding/decoding	EP	5107557
002562	05-Mobile Communication	GUILLAUME DE CREMOLUX@PHILIPS.COM	Adaptive noise reduction filter for digital data processing and digital pictu EP	EP	5107576
002563	05-Mobile Communication	GUILLAUME DE CREMOLUX@PHILIPS.COM	A multi purpose charge and play architecture	EP	5107544.4



Patent No.	Date	Classification	Inventor	Description	Status	Company
001112	17-aug-05	05-Mobile Communication	GUILLAUME DE CREMOUX@PHILIPS.COM	A very high speed unidirectional current slope limiter	EP	
001157	17-aug-05	05-Mobile Communication	MARCO FLAGNE@PHILIPS.COM	Depth Extractor from a 2D Encoded Video Bitstream	EP	
001964	16-aug-05	04-Audio/Video	MARCO BEKOO@PHILIPS.COM	FIFO write-back buffer to reduce processor stall cycles	EP	
000065	12-aug-05	05-Mobile Communication	STEPHANE AUBERGER@PHILIPS.COM	Low-cost rotational jitter correction system for video	EP	
002316	12-aug-05	05-Mobile Communication	CYRILLE NGALLE@PHILIPS.COM	Preventing a MIDI to MIDI to mislead the end user when dealing with protected	EP	
000260	10-aug-05	02-Packaging & Testing	ERIK JAN MARINISSEN@PHILIPS.COM	Secure Fuses for Testable Secure Chips	EP	
000263	10-aug-05	02-Packaging & Testing	ANDRE NIEUWLAND@PHILIPS.COM	Self Modifying Hardware for secure chips	EP	
000048	10-aug-05	05-Mobile Communication	STEPHANE AUBERGER@PHILIPS.COM	In loop system for video Stabilization	EP	
002364	10-aug-05	05-Mobile Communication	STEVEN THEEUWEN@PHILIPS.COM	A "microbrnm-type" Drain construction for High efficient W-CDMA LDMCS	EP	
001113	10-aug-05	08-Connectivity	BERND ELEND@PHILIPS.COM	Dynamic connection re-arrangement for fault tolerant links	EP	
001217	9-aug-05	08-Automotive	MATTHIAS PIEK@PHILIPS.COM	Motion Judder cancellation of video images sequences containing both vid EP	EP	
002321	8-aug-05	04-Audio/Video	DRIES VAN WAGENHAGEN@PHILIPS.COM	Half-iter-Bandwidth with Fundamentalturburem zum Schutz vor mchtment EP	EP	
001063	9-aug-05	08-Automotive	SDENKE HABENICHT@PHILIPS.COM	Sensorless Motordriver Architecture	EP	
001018	9-aug-05	09-Discrete and Multimar	ROLAND RICHTER@PHILIPS.COM	Method and System for Adaptive Contrast Enhancement in Digital Image	EP	
002242	9-aug-05	09-Discrete and Multimar	JOHAN DONKERS@PHILIPS.COM	Selective S&Ks attaching of the external base in a bipolar process	EP	
002346	8-aug-05	04-Audio/Video	PAVEL PETKOV@PHILIPS.COM	Infinite Skew Tolerant Delay Locked Loop	EP	
001741	3-aug-05	01-CMOS and embedded pr	J.P. PERRIN@PHILIPS.COM	Secure storage in a write once ROM	EP	
002236	3-aug-05	04-Audio/Video	ACHIM HILGERS@PHILIPS.COM	Broadband RFID-Transponder	EP	
000053	2-aug-05	07-Identification	TOH YEW TENG@PHILIPS.COM	A high performance TV and high sensitivity FM radio concept	EP	
000749	1-aug-05	04-Audio/Video	SANDER DERKSEN@PHILIPS.COM	Switching Estimator for current estimation in DC DC converter	EP	
002113	28-jul-05	05-Mobile Communication	ROBERT FIEDL@PHILIPS.COM	DC offset compensation in direct conversion TV front-end architecture by	EP	
000992	28-jul-05	04-Audio/Video	GUO YANG BIN@PHILIPS.COM	Method for USB ISO streams synchronization	US	
001110	29-jul-05	05-Mobile Communication	ANDRIES HEKSTRA@PHILIPS.COM	Alternative FIR structure	US	
002271	28-jul-05	02-Packaging & Testing	DAVID DUPELLAY@PHILIPS.COM	A new wireless transceiver configuration with self-calibration for better int	US	
001834	27-jul-05	02-Packaging & Testing	DANDY N. JADUCANA@PHILIPS.COM	SOLDER RESIST (SR) DAM DESIGN FOR CHIP-ON-BOARD APPLICA EP		
001468	27-jul-05	05-Mobile Communication	BRIAN MINNIS@PHILIPS.COM	Sensing and Correction of Differential Path Delay in Transmitters using E	EP	
001423	26-jul-05	04-Audio/Video	ELIE KHOURY@PHILIPS.COM	Booststrapping Error Correcting Decoders for Solid State Memory	US	
002211	26-jul-05	06-Connectivity	ROB FORTUIN@PHILIPS.COM	High Speed Driver Equalization	US	
002091	25-jul-05	02-Packaging & Testing	GEERT STEENBURGEN@PHILIPS.COM	Air cavity Package for Flip Chip	US	
000860	25-jul-05	04-Audio/Video	FRANS DE JONG@PHILIPS.COM	DC offset compensation in direct conversion TV front-end architecture by	EP	
002147	22-jul-05	02-Packaging & Testing	ZELJKO MRCARICA@PHILIPS.COM	TRSTr pin with bi-directional functionality	EP	
000801	22-jul-05	04-Audio/Video	VOLKER AUE@PHILIPS.COM	Push-sharing high-voltage ESD concept using distributed low-voltage dar	EP	
002598	22-jul-05	06-Connectivity	ROBERT GRUIJL@PHILIPS.COM	Quasi concurrent operation of DVB-H and WLAN in a combi DVB-H/WL EP	EP	
000374	21-jul-05	01-CMOS and embedded pr	JAAP RUGROK@PHILIPS.COM	Asynchronous Data Buffer	US	
000812	20-jul-05	05-Mobile Communication	FREDERIC PIROT@PHILIPS.COM	4-Layer Logic Decoder	US	
002582	20-jul-05	06-Connectivity	VOLKER AUE@PHILIPS.COM	Magnetic-RDM medium based on wet embossing using aligning field	EP	
002069	19-jul-05	02-Packaging & Testing	PHILIPPE CAUVET@PHILIPS.COM	Shocks erasing in mobile synchronization algorithms of OFDM systems	EP	
000751	19-jul-05	05-Mobile Communication	ERICH KLEIN@PHILIPS.COM	Reduced acquisition time for DVB-H burst reception	EP	
001561	19-jul-05	02-Packaging & Testing	ROGER SCHUTTER@PHILIPS.COM	Boosting techniques for a bidirectional switch	EP	
000863	15-jul-05	09-Discrete and Multimar	EDWIN SCHAPENDONK@PHILIPS.COM	Arrangement and method for an intermediate test control of a System-on-E	EP	
002023	14-jul-05	05-Mobile Communication	MAHJOU KOU@PHILIPS.COM	Production test method for serial communication transceivers	EP	
001766	13-jul-05	05-Mobile Communication	FRED VAN RIJS@PHILIPS.COM	Filtering after folding	EP	
002185	12-jul-05	01-CMOS and embedded pr	JEAN-LOUIS TORRES@ST.COM	Historical load profile statistical analysis for reduced flow power consum	US	
002018	12-jul-05	05-Mobile Communication	JEAN GOBERT@PHILIPS.COM	Local thick thermal-oxide to reduce hot carrier degradation in GEN5 LDM EP		
000795	12-jul-05	06-Connectivity	GUNNAR NITSCH@PHILIPS.COM	Integration control and reliability enhancement of air cavities	EP	
000849	12-jul-05	01-CMOS and embedded pr	LAURENT GOSSET@PHILIPS.COM	Median blur removal on digital pictures using motion extracted from greyn	EP	
002201	8-jul-05	02-Packaging & Testing	JOEL DIMASACAT@PHILIPS.COM	Protection of incumbent wireless services	EP	
000871	8-jul-05	10-Other	IGOR SIMONOVIC@PHILIPS.COM	Process to Control the Lateral Distribution of Air Gaps in Interconnects	EP	
001801	30-jun-05	06-Connectivity	WENYI SONG@PHILIPS.COM	Increased Down-Band Reliability By Using Groove Design on Exposed DIE	EP	
001882	29-jun-05	01-CMOS and embedded pr	TOM VAN DE KERKHOFF@PHILIPS.COM	An innovative rail-based ESD protection design method for multi-power-t EP		
001596	26-jun-05	05-Mobile Communication	MARKUS NEUMANN@PHILIPS.COM	Linear CMOS-IF amplifier and automatic gain control circuit	EP	
001735	25-jun-05	05-Mobile Communication	BEJAMIN REINECKE@PHILIPS.COM	Simplifying the de-rotation process in (digital) FM demodulator architectu	EP	
001755	25-jun-05	07-Identification	FRANK GRAEBER@PHILIPS.COM	high speed memory protection architecture	EP	
001634	25-jun-05	09-Discrete and Multimar	STEPHAN BUTZMANN@PHILIPS.COM	Software Layer for Communication Between RS-232 to I2C Translation If US		
001571	24-jun-05	01-CMOS and embedded pr	WIEBE DE BOER@PHILIPS.COM	Differential Multiphase Frequency Divider	US	
				Autotrophic pressure management in an RTP-chamber	US	
				Rectangular membrane with spaced edges	EP	
				Integrity protection of a multiple device system against unauthorized man EP		
				Sensor system with current interface receiver unit	EP	
				Doping profile improvement of in-situ doped n-type emitters.	US	

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0008436	28-jun-05	04-Audio/Video	RENE VAN DEN BERG@PHILIPS.COM	Device security based on a HW/SW combination supported by a Secure EP	5105734.7
0008810	28-jun-05	04-Mobile Communication	OLIVIER GARD@PHILIPS.COM	Mixed analog and digital control voltage of AGC for broadcast receivers	5300523.7
0009351	27-jun-05	01-CMOS and embedded	ERWIN JILZERN@PHILIPS.COM	Metal-Silicon Extrinsic-Base HBT	5105716.4
0007339	27-jun-05	01-CMOS and embedded	JAN SONSKY@PHILIPS.COM	Patterned buried (multi-) SiGe layer(s) and its applications in IC technology	5105718.8
0009888	27-jun-05	01-CMOS and embedded	WIBO VAN KOORT@PHILIPS.COM	Buried Metal layers for Silicon BiCMOS and CMOS devices	5106387.3
0016905	24-jun-05	01-CMOS and embedded	JENS ROEVER@PHILIPS.COM	Self-Synchronizing Data Streaming Between Address-Based Producer and Consumer	606844113
0013771	24-jun-05	02-Packaging & Testing	R. W. J. VAN DEN BOMMEN@PHILIPS.COM	Wafermap-Preventing Substrate and Method of Making Same	606893848
0011688	24-jun-05	05-Mobile Communication	JEAN-ROBERT TOURET@PHILIPS.COM	Wide band pseudonoise sine wave generator	5300508.8
0018178	24-jun-05	05-Mobile Communication	PETER FUHRMANN@PHILIPS.COM	Network Redundancy Management	5105682.8
0012773	22-jun-05	05-Discretes and Multichip	JAN SONSKY@PHILIPS.COM	Lateral dielectric resist and doping dilution in IC's	5105584.8
0009550	21-jun-05	05-Mobile Communication	PATRICE DEROUET@PHILIPS.COM	Modification of the gain repartition in the transconductance in a RF IC, mo	53003486.2
0006753	21-jun-05	05-Mobile Communication	TOM@SWAPDESIGN.BIZ	Sound Panel Concepts	5105485.5
0015447	21-jun-05	06-Connectivity	CASEY WOOD@PHILIPS.COM	Parallel Data Integrity Checking in PCI Express Devices	606922800
0016175	21-jun-05	04-Discretes and Multichip	BENEDYKT MIKA@PHILIPS.COM	Phase-Locked Loop Systems Using Static Phase Offset Calibration	606927722
0018175	21-jun-05	04-Discretes and Multichip	BENEDYKT MIKA@PHILIPS.COM	Phase-Locked Loop Systems Using Adaptive Low-Pass Filters in Switches	606927769
0004335	17-jun-05	05-Discretes and Multichip	GUNNAR SCHULZ@PHILIPS.COM	MEASURING OF POSITION AND ROTATIONAL SPEED WITH FREQUENCY	51053911.6
0012338	16-jun-05	01-CMOS and embedded	GEER VAN BELUNINGEN@PHILIPS.COM	Screwspreader	5105328.2
0012888	16-jun-05	01-CMOS and embedded	BARTEK PAWLAK@PHILIPS.COM	Solution for amorphized poly gates	5105323.9
0002559	18-jun-05	05-Mobile Communication	STAN BAGGEN@PHILIPS.COM	Simple Doppler Mitigation for DVB-T by using subcarrier-dependent (C) p	182005061940
0009350	15-jun-05	05-Discretes and Multichip	THOMAS LANGE@PHILIPS.COM	Verwendung von AIRTials UBM (under bump metallisation)	5105327.1
0002864	14-jun-05	07-Identification	PIM TUIJLS@PHILIPS.COM	Triggerable delay in RFID tags	5105236.4
0008694	14-jun-05	05-Discretes and Multichip	BORIS TRAA@PHILIPS.COM	Interference compensation by a repetitive generation of a string of stored	5105205.8
0004817	13-jun-05	04-Audio/Video	ARTUR BURCHARD@PHILIPS.COM	Pre-fetch and Write-back buffers dimensioning technique for zero delay	5105145.6
0022330	13-jun-05	05-Mobile Communication	ANDREI RADULESCU@PHILIPS.COM	Distributed frame synchronization for networks with different link speeds	5300744.9
0003009	13-jun-05	06-Connectivity	MIHAI SANGULEANU@PHILIPS.COM	Highly Linear, Variable-loss, 90 Oscillator	5105153
0014577	13-jun-05	04-Discretes and Multichip	TORSTEN PHILIP@PHILIPS.COM	Planar arrangement of magnetic field sensors for determination of 3D-mat	5105147.2
0000552	10-jun-05	05-Mobile Communication	STEPHANE VALENTE@PHILIPS.COM	Varying Half-Pi Rounding of Motion Vectors for MPEG-4 ASP Decoding	5300473.5
0004774	10-jun-05	04-Audio/Video	JURGEN WILLERT@PHILIPS.COM	A novel way to synchronize multiple full-bridge inverters	5105128.6
0004914	9-jun-05	04-Audio/Video	ARTUR BURCHARD@PHILIPS.COM	Intelligent Write-Back and Pre-Fetch Streaming Buffer Management for L	5105082.1
0004268	9-jun-05	04-Audio/Video	ARTUR BURCHARD@PHILIPS.COM	Use of PCH-Express Flow Control buffers for buffering of SDRAM burst re	51050394.8
0002434	8-jun-05	04-Mobile Communication	KEES MOERMAN@PHILIPS.COM	Using way-prediction to eliminate the use of multi-port memories in a m	5105035.9
0010988	9-jun-05	05-Mobile Communication	FRANK VOERMUNT@PHILIPS.COM	Flexible, extendable, and re-configurable FlexRay data buffering	5105064.9
0011000	8-jun-05	04-Automotive	MARTIN WAGNER_2@PHILIPS.COM	Transceiver mit adaptiver Flankensteuerung	5105070.6
0014002	8-jun-05	05-Mobile Communication	DAVID PENNA@PHILIPS.COM	Sample timing recovery in GPS input subsystems	5105003.7
0013588	7-jun-05	06-Connectivity	MONISHA GHOSH@PHILIPS.COM	Tone interleaves and Individual Stream Interleaved OFDM System	606889519
0001307	6-jun-05	01-Identification	PHILIPPE TEUWEN@PHILIPS.COM	Cheap NFC-RFID protection against eavesdropping by OTP	6064859.1
0000551	6-jun-05	02-Packaging & Testing	CHRIS WYLAND@PHILIPS.COM	Simplified oscillator circuit manufacturing	5104801.3
00020511	3-jun-05	04-Audio/Video	HENK DIJKSTRA@PHILIPS.COM	HIGH DENSITY PACKAGE INTERCONNECT POWER AND GROUND	5104960.3
0012653	1-jun-05	01-CMOS and embedded	POH CHENG TAN@PHILIPS.COM	Hybrid Texture for Interleaved MPEG-4 ASP Decoding with MPEG-4 SP	5300455.7
0009352	1-jun-05	05-Mobile Communication	STEPHANE VALENTE@PHILIPS.COM	Method to improve Base Access Resistance for NPN bipolar transistor	60686502
00101684	31-mai-05	03-RF Devices	JURGEN SCHROEDER@PHILIPS.COM	Multiple Pass Decoding for Interleaved MPEG-4 ASP Decoding with MPEG	5300440.4
0008245	30-mai-05	08-Automotive	RICARDO CASTANHA@PHILIPS.COM	Method to improve accuracy, linearity and temperature drift of Current Se	5104755.3
0002589	27-mai-05	01-CMOS and embedded	BOB MILSON@PHILIPS.COM	MEMS switched antenna with 50 ohm matching and reduced user interfac	5300434.7
0003597	27-mai-05	03-RF Devices	BOB MILSON@PHILIPS.COM	Erfindung Sicherheit durch einen zweiten zusätzlichen Zufallszahlengener	5104664.7
0007079	26-mai-05	05-Mobile Communication	EDWIN RIJCKEMA@PHILIPS.COM	Diversity interleaved for small device compared to the wavelength	5300431.3
0001371	25-mai-05	05-Mobile Communication	STEPHANE VALENTE@PHILIPS.COM	DATA PIPELINE MANAGEMENT SYSTEM AND METHOD FOR USING	5104626.6
000410	20-mai-05	05-Mobile Communication	ARNAUD ROSAY@PHILIPS.COM	Generic hardware for back end front side failure analysis	5104658.5
0002881	24-mai-05	01-CMOS and embedded	PAUL VAN DER SLUIS@PHILIPS.COM	Interdigital BAW filter employing lateral acoustic coupling of thin-film BA	5104574.6
0003316	24-mai-05	04-Audio/Video	GEERT VANDERHEIJDEN@PHILIPS.COM	Nested Slot Tooles	5104512.8
0003352	24-mai-05	05-Discretes and Multichip	HANS BOEVE@PHILIPS.COM	bias circuit with reduced parameter spread	5104506.6
0007359	24-mai-05	05-Discretes and Multichip	ROB VAN DALEN@PHILIPS.COM	Multiple Instances Decoding for Interleaved MPEG-4 ASP Decoding with M	5300411.5
0004442	23-mai-05	04-Audio/Video	EDWIN RIJCKEMA@PHILIPS.COM	Long Onkey disambiguation time management	5300411.5
0010665	23-mai-05	04-Audio/Video	JURGEN MOUWOTTECK@PHILIPS.COM	Method to improve autonomy of handheld devices	5300412.3
001675	23-mai-05	08-Automotive	NICOLAS VANHAELLEWYN@PHILIPS.COM	High speed cell search function	5300413.1
0006023	20-mai-05	05-Mobile Communication	EDWIN RIJCKEMA@PHILIPS.COM	Variable crease parameters to achieve continuous compliance	5104476.6
				chemically assisted anti-fuse	5104378.4
				video delay compensation	5104377.8
				Compass based three-dimensional angular sensor system	5104383.4
				Edge termination with increased stability against design variations and su	5104415.4
				Changing paths of existing connections and channels in networks on chip	5104347.8
				Error Correction for directional Da-interfacing	60684247
				Verfahren zur einfrachten Regelung der Resonanzfrequenz von LC-Schwi	5104341.2
				Improved refresh strategy in digital video encoders	5380382.7

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IPC Class	Inventor/Assignee	Description	Pub. No.	App. No.	App. Date	IPC Class	Inventor/Assignee	Description	Pub. No.	App. No.	App. Date	IPC Class	Inventor/Assignee	Description	Pub. No.	App. No.	App. Date
001035	IGOR BLEDNOCY@PHILIPS.COM	New input circuitry for FET/LDMOST integrated	5104913.1		14-01-05												
001746	FRANK GRABER@PHILIPS.COM	MULTI-TAG detection for location dependent	5104296.8		19-09-05												
000314	DIRK WOUTERS@IMEC.BE	METHOD TO CONTROL THE FIRST-TO-MELT	6068303.0		18-09-05												
000015	DIRK WOUTERS@IMEC.BE	METHOD FOR CONTROLLING THE FIRST-TO-MELT	6068303.0		19-09-05												
001195	DIRK DE VRIES@PHILIPS.COM	Test structure for combined electrical and	5300357.7		19-09-05												
000402	WERNER ZETTLER@PHILIPS.COM	Dynamische Anpassung des Spannungsabgrenz	5104260.4		17-09-05												
000876	JOSEF LUTZ@PHILIPS.COM	Compliance definition and stress release	5104083.9		13-09-05												
000987	ERIK JANLUUS@PHILIPS.COM	Two panel maskless lithography	5104068.7		13-09-05												
000812	BERT DE KONING@PHILIPS.COM	Stable system startup with L-I in battery	5104020.2		13-09-05												
000903	JUERGEN NOWOTNICK@PHILIPS.COM	Vorkalibrierung der Zufallszahl zur Realisier	5103930.4		11-09-05												
000478	CARLOS ALBA PHITO@PHILIPS.COM	Optimized sub-block access established	5103871.9		10-09-05												
000735	EWA HEKSTRA-NOWACKA@PHILIPS.COM	Intelligent flow control for best effort	5103855.2		10-09-05												
000842	CLEMENS DE HAAS@PHILIPS.COM	SLEEP WATCHDOG CIRCUIT FOR ASYNCHRONO	5103802.2		10-09-05												
000807	KEES SCHEFFERS@PHILIPS.COM	Advanced Feedback Communication Techniq	5103802.2		10-09-05												
000270	MARTIN GOOSSSENS@PHILIPS.COM	Photonic-Crystal Fresnel lens for through-	5103781		5-09-05												
000839	AKESSON@MATLAB RESEARCH PHILIPS	Introduction of prescheduler	5103760.4		4-09-05												
000312	RENCO BRINKMAN@PHILIPS.COM	A Peak Or Zero Current Comparator With	5103757		4-09-05												
000848	MONISHA GHOSH@PHILIPS.COM	Individualy Interleaved Alamoud Coding	60678556		4-09-05												
000316	HANS BOEYE@PHILIPS.COM	Planar System-In-Package Integrated Comp	5103724		4-09-05												
000378	TON IJINK@PHILIPS.COM	Measurement System for Characterizing t	5103712		4-09-05												
000313	TON IJINK@PHILIPS.COM	Vector-field Reconstruction System	5103711.3		4-09-05												
001101	PHILIPPE MEUNIER-BELLARD@PHILIPS	SOCl islands formation	5103679.6		3-09-05												
001782	PETER STEENEKEN@PHILIPS.COM	Capacitive RF MEMS switch with integrated	5103544		2-09-05												
000698	PETER FUHRMANN@PHILIPS.COM	Receiver with adaptive strobe offset adju	5103520.1		2-09-05												
000735	PHILIPPE MEUNIER-BELLARD@PHILIPS	Base and collector formation using two	5103520.1		2-09-05												
000767	PAUL HUBER@PHILIPS.COM	Smart EAS function	5103510.7		2-09-05												
000769	AMRITA DESHPANDE@PHILIPS.COM	I2C Slave Device with Programmable Write	60676360		2-09-05												
000778	AMRITA DESHPANDE@PHILIPS.COM	Generation of interrupt in I2C Slaves	60676367		2-09-05												
000771	AMRITA DESHPANDE@PHILIPS.COM	Simultaneous Control of Multiple I/O	60676381		2-09-05												
000772	AMRITA DESHPANDE@PHILIPS.COM	Device Identification Coding of Inter-	60676104		2-09-05												
000773	AMRITA DESHPANDE@PHILIPS.COM	Programming Parallel I2C Slaves from a	60676168		2-09-05												
000484	AMRITA DESHPANDE@PHILIPS.COM	I2C Slave/Master Interface Enhancement	50765035		2-09-05												
000526	AMRITA DESHPANDE@PHILIPS.COM	Dynamic I2C Slave Address Decoder	60676165		2-09-05												
000547	JOHAN DONKERS@PHILIPS.COM	New bipolar transistor integration	5103521		2-09-05												
000740	HANS TURMHLUT@PHILIPS.COM	A low complexity integration scheme	5103515.2		2-09-05												
000740	PATRICK GAMANDE@PHILIPS.COM	MOSFET layout method to improve param	5300335.8		2-09-05												
000717	DAVE EVANS@PHILIPS.COM	IO and Decoupling optimisation in	5103277.9		28-08-05												
000711	DAVE EVANS@PHILIPS.COM	Cellular plus DVB diversity in a mobile	5103551.9		28-08-05												
000715	KLEMENS BREITFUSS@PHILIPS.COM	Sichere, nicht abstrahierbare Kommu	5103521.3		28-08-05												
000396	STEPHANE ORAIN@PHILIPS.COM	3rd order multistage CESL	5290922.3		27-08-05												
000114	KEVIN BOYLE@PHILIPS.COM	BI-BAND ANTENNA	5300320.1		27-08-05												
000233	TOMY@SNAPDESIGN.BIZ	Sound Panel Concepts	5103451		27-08-05												
000123	MARCEL PELGROM@PHILIPS.COM	Voltage monitor for SiST	5103396.3		26-08-05												
000567	TOMAS HENRIKSSON@PHILIPS.COM	Implementation of multi-tasking on a	5103295.9		22-08-05												
000516	HARRY VEENDRICK@PHILIPS.COM	Charge recycling between idle and	5103277.9		22-08-05												
000107	GERAULD FLAGNE@PHILIPS.COM	MPEG-4 Spatial Prediction Using Size-	5300310.8		22-08-05												
000173	WALLACE LIU@PHILIPS.COM	A broadband V-groove guide mixer	2.0061E+11		20-08-05												
000072	MARKUS MULLER@PHILIPS.COM	Strain engineering by BOX removal	5300296		20-08-05												
000228	SREJAN KORDIC@PHILIPS.COM	Method to improve Cleaning Efficiency	5300298.4		20-08-05												
000122	PETER G BLANKEN@PHILIPS.COM	A test and power-efficient supply	5103146.8		20-08-05												
000126	PETER G BLANKEN@PHILIPS.COM	Ripple suppression filter in a	5103135.9		20-08-05												
000243	MAURICE ZELAN@PHILIPS.COM	Arrangement for demodulation of a	5103069		19-08-05												
000043	JACHIM BRILKA@PHILIPS.COM	Phase locked loop for phase	5102988.2		18-08-05												
000189	ULRICH MOEHLMANN@PHILIPS.COM	Umrichterstrom zwischen RTF und	5102935.3		18-08-05												
000204	HELMUT HAAR@PHILIPS.COM	Subharmonic mixer for mm-wave	5102965		18-08-05												
000258	MIHAL SANDULEANU@PHILIPS.COM	Positioning on a surface using	5102977.5		14-08-05												
000236	WILHEM FORTKUN@PHILIPS.COM	Suppression of vertical scan in	5102908		14-08-05												
000122	JAN SONKUY@PHILIPS.COM	Base formation inside CMOS	5300276.2		13-08-05												
000126	PHILIPPE MEUNIER-BELLARD@PHILIPS	CMOS speciar for parasitic bipolar	5102910.8		13-08-05												
000243	PHILIPPE MEUNIER-BELLARD@PHILIPS	'H' shape SiGe emitter formation	5300276.2		13-08-05												
000043	FABRIZIO CAMPANALE@PHILIPS.COM	Self-adaptive, fully digital	5102800.7		13-08-05												
000189	MIHAL SANDULEANU@PHILIPS.COM	Heat management	5102857.9		12-08-05												
000204	KLAUS BARTH@PHILIPS.COM	Mount-Tag Design of a RF Tur	5102876.9		12-08-05												
000204	PU-TIAN YAN_2@PHILIPS.COM	High Speed FFT/IFFT Architectures	5103268.8		12-08-05												

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AL0500286	8-Apr-05	03-RF Devices	JEAN-CLAUDE SX@PHILIPS.COM	LOW-VOLTAGE MEMS OSCILLATOR	EP	5300286.2
0000026	8-Apr-05	04-Audio/Video	KRISTOF VAN RECHT@PHILIPS.COM	Auto-Equalizer with midrange boost compression	EP	5102801.7
900121	8-Apr-05	07-Identification	MICHAEL RAUBER@PHILIPS.COM	Geregelte symmetrische Anpeisung einer Loop Antenne	EP	5102797.7
0000036	8-Apr-05	09-Discretes and Multimate	NIMALE PHAN@PHILIPS.COM	3D magnetoresistive acceleration sensor	EP	5102756.9
0000032	7-Apr-05	05-Mobile Communication	JEAN GOBERT@PHILIPS.COM	Simple depth generation of head-and-shoulders images	EP	5300256
NL050264	7-Apr-05	05-Mobile Communication	GUILAUME DE CREMOLUX@PHILIPS.COM	Method and apparatus for multibeam equalization	CN	2,0051E+11
CN050032	6-Apr-05	05-Mobile Communication	FRAN WU@PHILIPS.COM	ROUTER WITH FLEXIBLE INPUT QUEUEING	EP	5102680.5
NL050247	6-Apr-05	01-CMOS and embedded pr	DEE DENITENEER@PHILIPS.COM	RECEIVED SIGNAL STRENGTH INDICATOR IN AT THE DIGITAL OUT EP control of near capacitive mode in resonant converter by primary current / EP control of a resonant converter by primary peakcurrent and primary voltage	EP	5300243.2
FR050040	1-Apr-05	09-Discretes and Multimate	ROBERT VAN VELDHOVEN@PHILIPS.COM	Complementary Asymmetric High Voltage Devices and method of fabrication	EP	5102601.1
000234	1-Apr-05	10-Other	HANS HALBERSTADT@PHILIPS.COM	ANTI-PARALLEL VARICAP	EP	5102497.4
000059	31-Mar-05	01-CMOS and embedded pr	TED LETAVIC@PHILIPS.COM	EXTENDED FUNCTIONALITY OF RFID-TAGS	EP	5102495.8
NL050233	31-Mar-05	01-CMOS and embedded pr	BANG PHAN@PHILIPS.COM	Carbon Nanotube Bond Pad Structure and Method Thereof	US	602660338
FR050038	31-Mar-05	05-Mobile Communication	STEPHAN M KOCH@PHILIPS.COM	STATIC RANDOM ACCESS MEMORY CELLS WITH SHARED CONTACT EP SEMICONDUCTOR PACKAGE EMBEDDED WITH CONNECTORS	EP	5300226.3
NL050278	31-Mar-05	06-Connectivity	PU TIAN YAN_2@PHILIPS.COM	BURIED LAYERS IN A SILICON PROCESS	EP	5102404
GB050046	30-Mar-05	02-Packaging & Testing	CICERO VAUCHER@PHILIPS.COM	TWO STAGE FLASH ADC WITH REDUCED NUMBER OF SWITCHES	EP	5300223.4
NL050230	30-Mar-05	06-Connectivity	MARCO COLADROS@PHILIPS.COM	VIOLAN / CELLULAR LOCATION DEPENDENT SCANNING	EP	5102400
NL050231	28-Mar-05	06-Connectivity	DOMINEE LEENAERTS@PHILIPS.COM	SIDE WALL PORE SEALING FOR LOW-K DIELECTRICS	EP	5102397.4
AT050027	28-Mar-05	07-Identification	ACHIM HILGERS@PHILIPS.COM	FILTER DEVICE	EP	5102214.3
001456	28-Mar-05	02-Packaging & Testing	LIM KUI YONG@PHILIPS.COM	A LOW COST RF ID TAG BASED UPON LTPS TECHNOLOGY	EP	5102215
FR060036	24-Mar-05	01-CMOS and embedded pr	CHRIS WYLAND@PHILIPS.COM	HIGH POWER TRANSISTOR PRE-POST-MATCH STRUCTURE	EP	5102130.1
GB050041	24-Mar-05	01-CMOS and embedded pr	GILLES FERRU@PHILIPS.COM	SWITCHED MODE POWER CONVERTER AND METHOD OF OPERATION	EP	5102068.3
NL050265	24-Mar-05	09-Discretes and Multimate	BEN GELSEN@PHILIPS.COM	3 TERMINAL MOSFET WITH TEMPERATURE SENSE FACILITY	EP	5102062.9
US050113	23-Mar-05	06-Connectivity	OLAF HIRSCH@PHILIPS.COM	USB ON THE GO BRIDGE SOLUTION	EP	5101892.7
FR050034	22-Mar-05	01-CMOS and embedded pr	WIM BESLING@PHILIPS.COM	SWITCHED MODE POWER CONVERTER & METHOD OF OPERATION	EP	5101831.3
DE060058	21-Mar-05	04-Audio/Video	AXEL KATTNER@PHILIPS.COM	RECEIVER HAVING A GAIN-CONTROLLABLE INPUT AMPLIFIER	EP	5300176.4
AT050025	21-Mar-05	07-Identification	MARK T. JOHNSON@PHILIPS.COM	LOUDSPEAKER WITH COMPOUND MEMBRANE FOR USE IN SMALL EP RECEPTIVE MEMORY; STAGE SEQUENCE	EP	5101773.9
NL050228	18-Mar-05	05-Mobile Communication	IGOR BLEDNOV@PHILIPS.COM	RANGING BASED ASSOCIATION FOR UWB WIRELESS PAN DEVICE US ESQ PROTECTOR INTEGRATED CAPACITOR WITH LARGE CAPACITANCE	EP	5101587.2
GB050037	16-Mar-05	09-Discretes and Multimate	PETER DEGEN@PHILIPS.COM	GENERATOR FOR GENERATING A MESSAGE AUTHENTICATION CODE EP ROUTER; DIVERSE SCHEDULING OF MULTIPLE INPUT QUEUES	EP	5101524.6
GB050036	15-Mar-05	09-Discretes and Multimate	KEITH HEPPENSTALL@PHILIPS.COM	METHOD FOR GENERATING CACHE COHERENCE TESTING TRAFFIC	EP	5101496.1
NL050230	11-Mar-05	06-Connectivity	NAVSHOL SHARMA@PHILIPS.COM	LOWER POWER, LOW COST IMAGE COMPRESSION	EP	5101512.1
GB050034	11-Mar-05	09-Discretes and Multimate	PETER DEGEN@PHILIPS.COM	3 TERMINAL REVERSE MOSFET DEVICE PROVIDING CURRENT SENSE EP AN INTEGRATED CIRCUIT DEVICE PACKAGE WITH ADD CONTACT	EP	5101409
FR050031	7-Mar-05	01-CMOS and embedded pr	TON IKKINK@PHILIPS.COM	AN IC DEVICE PACKAGE WITH IMPROVED BOND PAD CONNECTION EP IMPROVED NOZZLE FOR SAWING APPARATUS	EP	5101384.5
FR050030	4-Mar-05	06-Connectivity	EWAL HEKSTRA-NONACK@PHILIPS.COM	EMBEDDED DRAM WITH INCREASED CAPACITANCE AND METHOD EP RFID ANTENNA FOR COMMUNICATION ON MULTIPLE SIDES OF AT EP ARCHITECTURE FOR WRITING DATA TO EMBEDDED DEVICES	EP	5300111.1
US050088	2-Mar-05	06-Connectivity	OLVIERE CHARLON@PHILIPS.COM	SMART CARD PROTECTION AGAINST LIGHT ATTACKS	EP	5101041.1
NL050170	2-Mar-05	09-Discretes and Multimate	MAREIKE KLEE@PHILIPS.COM	RESISTIVE MEMORY; LOW WRITE THRESHOLD, HIGH READ THRES EP COURSE SYNCHRONIZATION	EP	5100916.2
DE060043	2-Mar-05	06-Connectivity	MARC VAUCLAIR@PHILIPS.COM	METHODS AND APPARATUS FOR TUNING FILTER	US	60639257
DE060044	2-Mar-05	06-Connectivity	MARC VAUCLAIR@PHILIPS.COM	RANGING BASED ASSOCIATION FOR UWB WIRELESS PAN DEVICE US ESQ PROTECTOR INTEGRATED CAPACITOR WITH LARGE CAPACITANCE	EP	5101537.8
NL050199	20-Feb-05	01-CMOS and embedded pr	DEE DENITENEER@PHILIPS.COM	GENERATOR FOR GENERATING A MESSAGE AUTHENTICATION CODE EP ROUTER; DIVERSE SCHEDULING OF MULTIPLE INPUT QUEUES	EP	5101524.6
NL050178	20-Feb-05	02-Packaging & Testing	JAYRAM NAGESWARAN@PHILIPS.COM	METHOD FOR GENERATING CACHE COHERENCE TESTING TRAFFIC	EP	5101496.1
NL050187	20-Feb-05	04-Audio/Video	CHRIS SFEIRS@PHILIPS.COM	LOWER POWER, LOW COST IMAGE COMPRESSION	EP	5101512.1
US050026	24-Feb-05	09-Discretes and Multimate	KEITH HEPPENSTALL@PHILIPS.COM	3 TERMINAL REVERSE MOSFET DEVICE PROVIDING CURRENT SENSE EP AN INTEGRATED CIRCUIT DEVICE PACKAGE WITH ADD CONTACT	EP	5101409
NL050172	23-Feb-05	02-Packaging & Testing	PETER DIRKS@PHILIPS.COM	IMPROVED NOZZLE FOR SAWING APPARATUS	WO	193905050662 28590005951
NL050173	23-Feb-05	02-Packaging & Testing	F.H.N. TVELDK@PHILIPS.COM	EMBEDDED DRAM WITH INCREASED CAPACITANCE AND METHOD EP RFID ANTENNA FOR COMMUNICATION ON MULTIPLE SIDES OF AT EP ARCHITECTURE FOR WRITING DATA TO EMBEDDED DEVICES	EP	510187.7
NL050182	23-Feb-05	02-Packaging & Testing	VERONIQUE DE JONGHE@PHILIPS.COM	COMMUNICATION SYSTEM FOR ENABLING AT LEAST TWO HANDS EP METHOD FOR FORMING STI REGIONS IN ELECTRONIC DEVICES	EP	5101041.1
FR050024	18-Feb-05	01-CMOS and embedded pr	GERALD D. SCHAFFLER@PHILIPS.COM	CLOCK DOMAIN CROSSING TESTPOINT	EP	5100916.2
NL050114	14-Feb-05	04-Audio/Video	ARMIN GERREITSSEN@PHILIPS.COM	Estimation of error rates on unknown data bits	EP	5100908.6
FR050023	14-Feb-05	05-Mobile Communication	LAURENT BARNIER@PHILIPS.COM	RECEIVER	EP	51009106.1
NL050117	11-Feb-05	01-CMOS and embedded pr	JEROME DUBOIS@PHILIPS.COM	BATTERY CHARGING VIA NFC	EP	53001106.1
GB050022	11-Feb-05	02-Packaging & Testing	TOM WAAYERS@PHILIPS.COM	SEMICONDUCTOR DEVICE STRUCTURE WITH A TAPERED FIELD US SCALABLE USB ARCHITECTURE	US	606671429
001038	10-Feb-05	05-Mobile Communication	TIM MOULSLEY@PHILIPS.COM	A LOW-POWER REGISTER ARRAY WITH A RAM-LIKE CONFIGURAT US	US	601661434
NL050106	9-Feb-05	05-Mobile Communication	EDWIN V.D. HEIJDEN@PHILIPS.COM			
NL050107	9-Feb-05	05-Mobile Communication	EDWIN V.D. HEIJDEN@PHILIPS.COM			
AT050037	9-Feb-05	07-Identification	PHILIPPE MAUGES@PHILIPS.COM			
US050062	8-Feb-05	01-CMOS and embedded pr	TED LETAVIC@PHILIPS.COM			
US050065	8-Feb-05	06-Connectivity	LEE CHEE EEE@PHILIPS.COM			
			BILLE@PHILIPS.COM			

No.	Date	IPC Class	Applicant	Inventor	Title	Pub No.
NL0501123	7-jan-05	05-Mobile Communication	BLO THOMAS@PHILIPS.COM		DATAPROCESSING SYSTEM AND METHOD OF CACHE REPLACEMENT	5100821.7
AT0500116	7-feb-05	07-Identification	CHRISTIAN SCHERABONG@PHILIPS.COM		ANTENNA FOR DENSE READER ENVIRONMENT	5100833.2
GB0500016	7-feb-05	09-Discrete and Multimed	JAN SONSKY@PHILIPS.COM		MANUFACTURE OF LATERAL SEMICONDUCTOR DEVICES	5100846.4
NL0500069	2-feb-05	04-Audio/Video	TOM KOSTELIK@PHILIPS.COM		CPU GETS HIGHER PRIORITY FOR INTERRUPT PROCESSING	5100708.4
GB0500016	2-feb-05	05-Mobile Communication	PAUL MOORE@PHILIPS.COM		HYBRD CARTESIANPOLAR RADIO TRANSMITTER ARCHITECTURE	5100711.8
GB0500014	1-feb-05	02-Packaging & Testing	HERVE FLEURY@PHILIPS.COM		STRAIGHT-LOCK: AN ALTERNATIVE LOW-COST APPROACH TO CL EP	5300077.4
NL0500078	31-jan-05	04-Audio/Video	EFTHIMIOS TELLIOUKAS@PHILIPS.COM		CABLE COMPLAINT GAIN CONTROLLED LNA	5100633.6
NL0500084	31-jan-05	04-Audio/Video	MARC.O.BEKOOL@PHILIPS.COM		MEMORY DEFRAGMENTATION: RUNTIME RELOCATION OF FIFO	5100802.1
CR0500017	31-jan-05	05-Mobile Communication	IVY ZHU@PHILIPS.COM		A METHOD AND APPARATUS FOR DESPREAD DATA IN THE TD-SC	2.0051E+11
CR0500016	31-jan-05	05-Mobile Communication	IVY ZHU@PHILIPS.COM		A METHOD AND APPARATUS FOR IMPLEMENTING MATCHED FILTER	2.0051E+11
NL0500068	29-jan-05	01-CMOS and embedded pr	WIBO VAN MOORT@PHILIPS.COM		IMPROVED ANTICOLLISION ALGORITHM	5100681.4
DE0500078	28-jan-05	02-Packaging & Testing	TAGARAJU BUSSA@PHILIPS.COM		MEANS AND METHOD FOR DEBUGGING	5100551.8
NL0500063	26-jan-05	04-Audio/Video	THOMAS HAFEMEISTER@PHILIPS.COM		IF-STAGE FOR ANALOGUE AND DIGITAL VIDEO SIGNALS	5100530.1
NL0500035	27-jan-05	01-CMOS and embedded pr	PETER NUNTEK@PHILIPS.COM		DIGITAL CLASS D AMPLIFIER WITH NOISE SUPPRESSION IN FEEDBACK	5100590.0
NL0500081	25-jan-05	01-CMOS and embedded pr	YUKIYO FURUKAWA@PHILIPS.COM		METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE	5016726.1
NL0500078	25-jan-05	02-Packaging & Testing	MICHAEL T. ZANDTIG@PHILIPS.COM		PHASE-CHANGE RESISTOR IN BACKEND	5100458.8
NL0500072	25-jan-05	04-Audio/Video	NARENDRANATH LUDUPA@PHILIPS.COM		DATA PROCESSING SYSTEM AND METHOD OF DEBUGGING	5100452.1
NL0500074	25-jan-05	05-Mobile Communication	JAN HOOGBERG@PHILIPS.COM		MULTI-THREADED PROCESSOR	5100455.4
AT0500013	25-jan-05	07-Identification	MIKE SPLITHOFF@PHILIPS.COM		WIRELESS SENSOR SYSTEM	5100443
NL0500066	24-jan-05	05-Mobile Communication	ACHIM HILGERS@PHILIPS.COM		HARD MAGNETIC READ OUT SCHEME	5100424
NL0500081	24-jan-05	01-CMOS and embedded pr	JAAP RUIGROK@PHILIPS.COM		HMMROM DATA READ OUT SCHEME	5100427.3
GB0500011	22-jan-05	05-Mobile Communication	FRISO JEDEMA@PHILIPS.COM		METHOD OF HYSTERESIS PERIODS FOR GRANTED TRANSMISSION	501403
US0500032	21-jan-05	05-Mobile Communication	MATTHEW BAKER@PHILIPS.COM		HIGH DYNAMIC RANGE LOW-POWER DIFFERENTIAL INPUT STAGE	5016490.1
US0500033	21-jan-05	06-Connectivity	STEPHAN DRUDE@PHILIPS.COM		LOW-NOISE MIXER	5016460.9
AT0500011	19-jan-05	06-Connectivity	OSWALD MIDONEN@PHILIPS.COM		DYNAMIC ACTION INITIATOR SYSTEM EMPLOYING NFC ENABLED RF	5100336.3
000765	18-jan-05	07-Identification	PETER BALTUS@PHILIPS.COM		Using a "W"-snake collector to fabricate a high performance bipolar in CN EP	5100336.7.3
US0500019	18-jan-05	02-Packaging & Testing	JOHAN DONKERS@PHILIPS.COM		ERROR CORRECTING CODE FOR INTEGER PROCESSING UNIT	5016445.4
NL0500029	18-jan-05	04-Audio/Video	ARIE KOPPELAAR@PHILIPS.COM		PROGRAMMABLE GRAY LEVEL GENERATOR FOR COLOR TFT MOE	5100266.5
NL0500036	18-jan-05	06-Connectivity	SEBASTIAN LEGNER@PHILIPS.COM		TAMPER-RESISTANT TIME REFERENCE AND APPARATUS	5016445.4
DE0500017	18-jan-05	05-Mobile Communication	PIER LUIGI CAVALINI@PHILIPS.COM		AMR-SENDORELEMENT WITH LOW OFFSET FOR ANGULAR MEASUR	5100261.6
NL0500019	17-jan-05	05-Mobile Communication	STEFAN DRUDE@PHILIPS.COM		CHANNEL ENCODING	5100238.2
FR0500006	14-jan-05	05-Mobile Communication	INTRANIS@NATLAB.RESEARCH.PHILIPS.COM		DIADRATURE SUB-SAMPLING FOR LOW-LF RECEIVER	EP 5300095.2
CR0500008	13-jan-05	05-Mobile Communication	MARTIAL GANDER@PHILIPS.COM		USING TV CABLEING TO IMPROVE RELIABILITY OF WIFI NETWORKS	500801
GB0500006	13-jan-05	06-Connectivity	XAVIER XC. DIAGO@PHILIPS.COM		A NOVEL APPROACH FOR ISSUE LOGIC	2.0051E+11
GB0500006	13-jan-05	06-Connectivity	WOLFGANG MERGLER@PHILIPS.COM		MEMORY REF LEVEL CALC. USE AVERAGE, IGNORE EXTREME VA	5100195
US0500012	13-jan-05	04-Discrete and Multimed	IVY ZHU@PHILIPS.COM		A METHOD OF SLIDING WINDOW CHANNEL ESTIMATION IN TD-SC	2.0051E+11
NL0500035	12-jan-05	04-Audio/Video	VICTOR VAN ACHT@PHILIPS.COM		INDUCTOR	EP 5300077.9
GB0500006	12-jan-05	05-Mobile Communication	IVY ZHU@PHILIPS.COM		POWER FACTOR CORRECTOR WITH BURST MODE STANDBY TO M	500183.5
FR0400223	30-dec-04	04-Audio/Video	FRANCO.FESSOLANO@PHILIPS.COM		CIRCUIT FOR GENERATION OF TEST PATTERNS	410785A.2
NL0500021	30-dec-04	05-Mobile Communication	ANDREAS.GLOWATZ@PHILIPS.COM		DATA PROCESSING ARRANGEMENT	EP 4300956.4
FR0402225	30-dec-04	05-Mobile Communication	LAURENT CAPELLA@PHILIPS.COM		GENERIC MONITOR FOR BOOT MANAGEMENT	EP 4300954.7
FR0500006	4-jan-05	02-Packaging & Testing	ANDREAS.GLOWATZ@PHILIPS.COM		FINFET FOR LOW-POWER CMOS APPLICATIONS	EP 4107920.2
NL0411990	30-dec-04	04-Audio/Video	FRIEDBERT RIEDEL@PHILIPS.COM		SIMPLIFIED FULLY SELF-ALIGNED CMOS FRONT END TECHNOLOG	4107021
FR0400225	30-dec-04	05-Mobile Communication	JEAN-PAUL COUPIGNY@PHILIPS.COM		INSTANT MESSAGE COMMUNICATION APPLICATION IN PEER-TO-P	2.0041E+11
FR0401500	28-dec-04	01-CMOS and embedded pr	YOURI PONOMAREV@PHILIPS.COM		FINFET FOR LOW-POWER CMOS APPLICATIONS	EP 4107008.7
FR0401501	28-dec-04	01-CMOS and embedded pr	YOURI PONOMAREV@PHILIPS.COM		PNP-TRANSISTOR IN THIN-LAYER SOI WITH BASE-BOOST UNDER	EP 4107809.5
FR0401004	26-dec-04	05-Mobile Communication	JOCKI LICHENG@PHILIPS.COM		HIGH DENSITY PROGRAMMABLE ANTI-FUSE FOR ADVANCED TECH	EP 4107805.3
NL041091	26-dec-04	05-Mobile Communication	PIER LUIGI DALESSANDRO@PHILIPS.COM		TRANSMITTER APPARATUS	EP 4106997.2
FR0401088	27-dec-04	05-Discrete and Multimed	LUKIJAN@NATLAB.RESEARCH.PHILIPS.COM		ON SILICON CAPACITANCE MEASUREMENT	EP 4103917
FR0401082	27-dec-04	01-CMOS and embedded pr	SEBASTIAN FARE@PHILIPS.COM		INTERFACE CIRCUIT METHOD FOR RECEIVING/DECODING DATA	EP 4106924.6
NL0411466	23-dec-04	04-Audio/Video	ALEXANDER LAMPE@PHILIPS.COM		LOW COMPLEXITY BLIND TRANSPORT FORMAT DETECTION	EP 4106920.4
NL0411065	23-dec-04	05-Mobile Communication	FRANZ EDER@PHILIPS.COM		DEVICE AND METHOD FOR HOLDING OR TRANSPORTING A SUBST	5016336.9
NL041361	22-dec-04	04-RE Devices	MARTIN KNOTTNER@PHILIPS.COM		CHARGE BIASED MEM RESONATOR	EP 4106835.4
GB040224	21-dec-04	04-Audio/Video	PETER STEENEKEN@PHILIPS.COM		QUALITY OF SERVICE (BANDWIDTH) BY PREPARING AND MODIF	EP 4106836.7
FR0402226	21-dec-04	05-Mobile Communication	FRANZ EDER@PHILIPS.COM		GATE DIELECTRIC USING SIZE EFFECT OF PEROVSKITE	EP 427980.6
NL041448	21-dec-04	05-Mobile Communication	IGOR BLEDKOV@PHILIPS.COM		INTEGRATED CIRCUIT WITH IMPROVED DEVICE SECURITY	EP 4300929.19
NL041443	20-dec-04	02-Packaging & Testing	M.VAN SPLUNTER@PHILIPS.COM		POWER SENSOR OF PA AS A BOND WIRE STRUCTURE	EP 4106793.5
					TESTABLE MULTIPROCESSOR SYSTEM WITH SCANCHAIN BYPASS	EP 4106751.3



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Patent No.	Pub No.	IPC Class.	Title	Inventor	Assignor	Applicant	Pub Date
DE040321	20-10-04	06-Connectivity	CELL SIZE CONTROL IN WIRELESS CELLULAR NETWORKS	MATTHIAS WAGNER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106726.5
DE040320	20-10-04	07-Identification	DATAP PROCESSING DEVICE AND OPERATING METHOD FOR ...	BERNO ELENDD@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106722.4
DE040325	20-10-04	08-Automotive	BUS GUARDIAN	LAURENT BARRIER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106748.3
FR040217	17-10-04	04-Audio/Video	COMMUNICATION SYSTEM COMPRISING A MASTER AND A SLAVE	BRUNO BALLARIN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000915.8
FR040216	17-10-04	05-Mobile Communication	MESH BIN TREE SUM	RAY FENG@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000918.2
CN040072	17-10-04	07-Identification	SYSTEMS AND METHODS FOR MOBILE DEVICE ANTI-THEFT BY US CM	TIM RIDGERS@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	2.0041E+11
FR040215	15-10-04	05-Mobile Communication	COMPENSATION CIRCUIT AROUND TRANSMIT DIRECT CONVERTERS	ANDREAS BURY@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000908.3
DE040318	15-10-04	06-Connectivity	METHOD AND APPARATUS FOR CALIBRATION OF THE ANALOG IQ	HANS-PETER LOEBL@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106643.2
DE040315	15-10-04	03-RF Devices	ACOUSTIC REFLECTOR STACK FOR SAW RESONATORS/FILTERS	OLAF BOSMA@MASTER-ADUJO.NL	PHILIPS.COM	PHILIPS.COM	4106650.5
W04544	14-08-04	01-CMOS and embedded	RF amplifier with APCA architecture	FRAN YU@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000695.2
FR040140	13-10-04	05-Mobile Communication	BORDERLESS LAYER STACK FOR HIGH EFFICIENT LOOPS COMPILING IN VU CM	MARC SOLER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000682
CN040054	13-10-04	05-Mobile Communication	A NOVEL METHOD FOR HIGH EFFICIENT LOOPS COMPILING IN VU CM	HANS BOEVE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	2.4041E+11
FR040211	13-10-04	05-Mobile Communication	EDGE MIPS LIMITER	FRANCIS GAGNON	PHILIPS.COM	PHILIPS.COM	43000673
GB040247	10-10-04	01-CMOS and embedded	IMPROVEMENT OF THE ESD PROTECTION BY USING THE LEAKAGE	FREDERIC BARBIER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000879.6
US040252	10-10-04	01-CMOS and embedded	ONE-TIME PROGRAMMABLE PHASE-CHANGE READ ONLY MEMORY	HANS BOEVE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60353129
GB040246	10-10-04	03-RF Devices	INTEGRATED WAFER-LEVEL PACKAGING (IWLP)	FABRICE VERJUS@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000878.8
NL041457	10-10-04	04-Audio/Video	TASK SCHEDULER INDICATES CACHE VICTIMS	SAINATH KARLALEM@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106445.8
DE040301	10-10-04	07-Identification	MACHINE READABLE DOCUMENT	MANFRED PAESCHKE@BOR DE	PHILIPS.COM	PHILIPS.COM	4106463.5
FR040208	7-10-04	05-Mobile Communication	REDUCTION HETEROGENE DES IMAGES PAR LA METHODE D'ACCIEP	CHRISTOPHE KEFFEDER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000853.1
FR040216	7-10-04	05-Mobile Communication	REDUCTION SUPERIEUR REFERENCE CIRCUIT SUITABLE FOR PAST EP	ZHENHUA WANG@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000853.1
DE040307	7-10-04	07-Identification	MULTI-APPLICATION MANAGEMENT ON SMART CARDS	GEERT JAN SCHRIJVEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106353.8
NL041349	6-10-04	01-CMOS and embedded	FABRICATION OF PDS FULLY SILICIDED GATE FOR CMOS	MARK VAN DAL@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106350.4
NL041350	6-10-04	01-CMOS and embedded	TRI-WAFER SENSOR BANK	MARK VAN DAL@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106330.6
US0400546	6-10-04	02-Packaging & Testing	MEASURING REVOLUTION SPEED VIA RPIIT-TAGS	PADRANG OMAHONY@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000844
AT040084	6-10-04	07-Identification	DATA PROCESSING WITH CIRCUIT MODELING	STEFAN WIJESER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000832.6
US040537	3-10-04	01-CMOS and embedded	MEMORY CONTROLLER FOR STREAMING DATA	TIM PONTIUS@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106274.6
NL041454	3-10-04	04-Audio/Video	UMTS SYNCHRO	ARTUR BURCHARD@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000844
FR040207	3-10-04	05-Mobile Communication	NON-VOLATILE MEMORY	MARK WALLIS@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60353004.3
US040525	2-10-04	03-RF Devices	PIPA ANTENNA	HANS BOEVE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4264403.8
FR040203	2-10-04	03-RF Devices	DISTRIBUTED DUPLEXER	KEVIN BOYLE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4264403.8
FR040206	2-10-04	05-Mobile Communication	TURBO DECODER	KEVIN BOYLE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000838
GB040242	2-10-04	05-Discrete and Multicomp	MAKING POWERDEVICES WITH STABLE LINEAR MODE OPERATIC GB	ANDREA ANGORA@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	426412.3
FR040204	1-10-04	01-CMOS and embedded	SELECTIVE Al TAN BARRIER DEPOSITION ON COPPER FOR CU-EP	ADAM BROWNE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000831.7
FR040205	1-10-04	01-CMOS and embedded	ENHANCEMENT OF CU METAL LINES CAPPING WITH AN AIR GAF EP	LAURENT COSSET@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000833.3
NL041348	1-10-04	04-Audio/Video	SATURATION AND OVERFLOW DETECTION FOR ARM EP	SERGEI SAWITZK@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106211.6
DE040298	29-10-04	05-Mobile Communication	FAST DIGITAL INTERFACE FOR AUTOMOTIVE SENSORS	MARCUS WESER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106218.7
NL041334	30-10-04	01-CMOS and embedded	ALUMINIUM OXIDE BASED ANTIFUSE TUNNEL BARRIER PHASE CH EP	HANS BOEVE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106181.3
DE040296	27-10-04	04-Audio/Video	ON-CHIP MONITOR IN TEST CHAIN	MARCEL PELGROM@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106128.4
NL041335	29-10-04	06-Connectivity	CURRENT LIMITING AMPLIFIER	JOHN HUG@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106137.5
FR040202	25-10-04	03-Packaging & Testing	BRIDGE BETWEEN A PAUSIBLE CLOCKED IP AND A NON-PAUSIBLE GB	JOSE PINEDA DE GYVEZ@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4262004
NL041327	25-10-04	05-Mobile Communication	METHOD FOR MODIFYING TRENCH IN SUBSTRATE	ANTON KEMMEREN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	5100384.8
NL041328	25-10-04	06-Connectivity	JITTER REDUCTION IN PLL TRANSFORMER CIRCUIT	PASCAL PHILIPPE@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	43000817.8
GB005004	25-10-04	01-CMOS and embedded	LITHOGRAPHIC RESOLUTION BEYOND 0.25 LAMBDA-DANA USING... MIXER	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106102.8
FR040201	24-10-04	02-Packaging & Testing	AUTOMATIC PARAMETER VALUE GENERATION FOR SIGNAL INTEG EP	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	5102583.3
NL041311	25-10-04	05-Mobile Communication	IN EAR HEADPHONE APPLICATION WITH SEPARATED ACOUSTIC AL EP	STEFAN ANDERBERGEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	425500
FR040203	24-10-04	06-Connectivity	RFID LABEL CONTROL SYSTEM FOR CONSUMERS	HARRY VEENDRICK@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4106070.4
NL041284	18-10-04	06-Connectivity	CO-SIMULATION OF A PROCESSOR DESIGN	ERICH KLEIN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60352986.9
NL041082	22-10-04	01-CMOS and embedded	DEVICE WITH A MATCHING STAGE	MICHAEL CERNUSCA@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4105871
FR040200	16-10-04	05-Mobile Communication	METHOD OF ACQUIRING GPS DATA IN A TIME-DIVISION MULTIPLE: GB	ROBERT GRUIJL@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	606292005
FR040204	16-10-04	05-Mobile Communication	PACKET PROCESS ARRANGEMENT AND APPROACH THEREFOR	ANDRE VAN BEZOOIJEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	398624
NL041266	15-10-04	01-CMOS and embedded	DELTA A-D CONVERTER	WILLEM GRIJNEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4300794.7
NL041333	15-10-04	01-CMOS and embedded	END-OF-TRANSMISSION SIGNALING WITHOUT DATE-ENCODING EP	YANN LE GUEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4300795.4
US040461	15-10-04	01-CMOS and embedded	ADABATIC CMOS DESIGN USING TRIPLE-WELL AND TRENCH CAP. EP	ADRIAN SPENCER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4105768.5
US040365	15-10-04	04-Audio/Video	FLASH TO ROM CONVERSION	GUILAUME DE CREMOUX@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	4105780.3
			VARIABLE INDUCTANCE CIRCUITRY FOR FREQUENCY CONTROL C US	KEVIN LOCKER@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				YVES RAMANZING@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				WILLEM GRIJNEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				YANN LE GUEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				GERARD DEN BESTEN@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				MART COENHAAR@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				ROB VERHAAR@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				YANN BOUITTENT@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081
				JAN WILLEM VAN DE WAERT@PHILIPS.COM	PHILIPS.COM	PHILIPS.COM	60628081

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C. Bosch Behaerzmaatschappij

Bundesdruckerei GmbH

Pub No.	Date	IPC Class	Inventor	Description	IPC Class	Pub No.	Date	IPC Class	Inventor	Description
US040424	15-nov-04	06-Connectivity	RODGER SCHUTTER@PHILIPS.COM	SYSTEM & METHOD FOR ON-CHIP JITTER INJECTION	US	60623070	2006/05/16/02-A1	US	60623070	SYSTEM & METHOD FOR ON-CHIP JITTER INJECTION
NLD041256	12-nov-04	04-Audio/Video	PETER NJUNTES@PHILIPS.COM	CLASS O POWER STAGE ERROR CORRECTION USING A PWM METEP	EP	4105729	2006/02/07/05-A1	EP	4105729	CLASS O POWER STAGE ERROR CORRECTION USING A PWM METEP
NLD041257	12-nov-04	05-Mobile Communication	FRANS SCROOPS@PHILIPS.COM	BOOSTED BUCK CONVERTER	EP	4105733,2	2006/02/07/05-A2	EP	4105733,2	BOOSTED BUCK CONVERTER
NLD041242	11-nov-04	05-Mobile Communication	LORENZO TRIPODI@PHILIPS.COM	FLAT CURRENT SOURCE	EP	4105701,9	2006/03/16/05-A2	EP	4105701,9	FLAT CURRENT SOURCE
DE040293	11-nov-04	06-Audio/Video	JOERG HABETHA@PHILIPS.COM	DEVICE AND METHOD FOR EVENT-TRIGGERED COMMUNICATION	EP	4105668,8	2006/05/13/05-A1	EP	4105668,8	DEVICE AND METHOD FOR EVENT-TRIGGERED COMMUNICATION
NLD041306	9-nov-04	02-Packaging & Testing	STEVEN DE CUYPER@PHILIPS.COM	ALTERNATIVE METHOD AND IMPLEMENTATION OF TESTING INTERF	GB	424786,4	2006/05/13/05-A1	GB	424786,4	ALTERNATIVE METHOD AND IMPLEMENTATION OF TESTING INTERF
US040450	4-nov-04	01-CMOS and embedded	CHORDAS@ATLAB RESEARCH-PHILIPS.COM	DEBUG GT CHANNEL MAINTAINS RESERVED DURING NORMAL OP	EP	4105602,9	2006/01/01/02/05-A1	EP	4105602,9	DEBUG GT CHANNEL MAINTAINS RESERVED DURING NORMAL OP
NLD041240	4-nov-04	02-Packaging & Testing	VIET NGUYENHOANG@PHILIPS.COM	HALF-BRIDGER INVERTING EN WERKWIJZIG TER VERVAARDIGING	EP	4105575,7	2006/05/04/08/04	EP	4105575,7	HALF-BRIDGER INVERTING EN WERKWIJZIG TER VERVAARDIGING
US040451	4-nov-04	02-Packaging & Testing	CHRIS WYLAND@PHILIPS.COM	INTEGRATED CIRCUIT NONOTUBE-BASED SUBSTRATE	US	60625452	2006/05/04/08/04	US	60625452	INTEGRATED CIRCUIT NONOTUBE-BASED SUBSTRATE
US040454	4-nov-04	02-Packaging & Testing	CHRIS WYLAND@PHILIPS.COM	NANOTUBE-BASED FILLER	US	60625443	2006/05/04/08/04	US	60625443	NANOTUBE-BASED FILLER
US040452	4-nov-04	10-Other	CHRIS WYLAND@PHILIPS.COM	NANOTUBE-BASED DIRECTIONALLY CONDUCTIVE ADHESIVE	US	60625437	2006/05/04/08/04	US	60625437	NANOTUBE-BASED DIRECTIONALLY CONDUCTIVE ADHESIVE
US040455	4-nov-04	10-Other	CHRIS WYLAND@PHILIPS.COM	NANOTUBE-BASED FLUID INTERFACE MATERIAL AND APPROACH	US	60625448	2006/05/04/08/04	US	60625448	NANOTUBE-BASED FLUID INTERFACE MATERIAL AND APPROACH
US040457	3-nov-04	02-Packaging & Testing	T.F. DAI@PHILIPS.COM	INNER BRIDGES FOR CHIP-TO-CHIP INTERCONNECTIONS IN A MUI	US	60625449	2006/05/04/08/04	US	60625449	INNER BRIDGES FOR CHIP-TO-CHIP INTERCONNECTIONS IN A MUI
GB040271	2-nov-04	01-CMOS and embedded	BARTER PAWLAK@PHILIPS.COM	A METHOD FOR COMPLETE RELAXATION OF SIGE BUFFER	GB	424290,5	2006/05/04/08/04	GB	424290,5	A METHOD FOR COMPLETE RELAXATION OF SIGE BUFFER
FR040184	26-oct-04	05-Mobile Communication	YVES RAMANZING@PHILIPS.COM	MPEG-4 VIDEO TIME STAMP MANAGEMENT FOR VIDEO EDITING	EP	4300745,3	2006/05/04/08/04	EP	4300745,3	MPEG-4 VIDEO TIME STAMP MANAGEMENT FOR VIDEO EDITING
US040442	26-oct-04	09-Discretes and Multimate	KLAAS JAN DELANGEN@PHILIPS.COM	SYSTEM FOR DIAGNOSING IMPEDANCES HAVING ACCURATE CUR	US	60522715	2006/05/04/08/04	US	60522715	SYSTEM FOR DIAGNOSING IMPEDANCES HAVING ACCURATE CUR
NLD041199	27-oct-04	03-RF Devices	PETER STEENEKEN@PHILIPS.COM	MEMS WITH A SEGMENTED REFLECTOR	EP	4105341,4	2006/05/04/08/04	EP	4105341,4	MEMS WITH A SEGMENTED REFLECTOR
NLD041200	27-oct-04	03-RF Devices	PETER STEENEKEN@PHILIPS.COM	MEMS WITH A SEGMENTED REFLECTOR	EP	4105342,2	2006/05/04/08/04	EP	4105342,2	MEMS WITH A SEGMENTED REFLECTOR
NLD041201	27-oct-04	03-RF Devices	PETER STEENEKEN@PHILIPS.COM	MEMS WITH A SEGMENTED REFLECTOR	EP	4105341,4	2006/05/04/08/04	EP	4105341,4	MEMS WITH A SEGMENTED REFLECTOR
GB040252	26-oct-04	09-Discretes and Multimate	ROBERT FIEFELD@PHILIPS.COM	FILTER POWER SAVING USING SIGMA DELTA SIGNAL STRENGTH	GB	423706,7	2006/05/04/08/04	GB	423706,7	FILTER POWER SAVING USING SIGMA DELTA SIGNAL STRENGTH
FR040187	22-oct-04	01-CMOS and embedded	JEAN-PHILIPPE JACQUEMIN@PHILIPS.COM	ELECTROMIGRATION CONTROL	EP	4300709,9	2006/05/04/08/04	EP	4300709,9	ELECTROMIGRATION CONTROL
GB040206	21-oct-04	01-CMOS and embedded	YUKIKO FURUKAWA@PHILIPS.COM	GATE STACK USING SETION AS CHANNEL, HIGH-K DIELECTRIC...	GB	423343,3	2006/05/04/08/04	GB	423343,3	GATE STACK USING SETION AS CHANNEL, HIGH-K DIELECTRIC...
NLD041171	21-oct-04	01-CMOS and embedded	JAN-FRISO BLANCHIERE@PHILIPS.COM	ANALOG REFRESH WHEN AVERAGE LEVEL BELOW THRESHOLD	EP	4105203,6	2006/05/04/08/04	EP	4105203,6	ANALOG REFRESH WHEN AVERAGE LEVEL BELOW THRESHOLD
DE040277	21-oct-04	08-Automotive	MARTIN LANKHORST@PHILIPS.COM	PHASE CHANGE MEMORY, PREFERRED RESISTANCE	EP	4105212,7	2006/05/04/08/04	EP	4105212,7	PHASE CHANGE MEMORY, PREFERRED RESISTANCE
US040418	20-oct-04	01-CMOS and embedded	DIRK WENZEL@PHILIPS.COM	BALDRATE RECOGNITION IN LIN-SYSTEMS	EP	4105213,5	2006/05/04/08/04	EP	4105213,5	BALDRATE RECOGNITION IN LIN-SYSTEMS
AT040030	20-oct-04	07-Identification	EUGEN ROEHL@PHILIPS.COM	DYNAMIC DEVELOPMENT PROCESS WITH DE-IONIZED WATER PU	US	6052391,6	2006/05/04/08/04	US	6052391,6	DYNAMIC DEVELOPMENT PROCESS WITH DE-IONIZED WATER PU
AT040030	20-oct-04	07-Identification	EUGEN ROEHL@PHILIPS.COM	DYNAMIC DEVELOPMENT PROCESS WITH DE-IONIZED WATER PU	US	6052391,6	2006/05/04/08/04	US	6052391,6	DYNAMIC DEVELOPMENT PROCESS WITH DE-IONIZED WATER PU
NLD041151	20-oct-04	08-Automotive	RUIJRO VASER@PHILIPS.COM	IC PAD PARTITIONING STRUCTURE AND METHOD OF MAKING	EP	4105175,6	2006/05/04/08/04	EP	4105175,6	IC PAD PARTITIONING STRUCTURE AND METHOD OF MAKING
FR040180	19-oct-04	02-Packaging & Testing	ROBERT TERECHKO@PHILIPS.COM	LIN-BUS TRANSMITTER WITH SYMMETRICAL SLOPES	EP	4105183	2006/05/04/08/04	EP	4105183	LIN-BUS TRANSMITTER WITH SYMMETRICAL SLOPES
GB040206	18-oct-04	09-Discretes and Multimate	ROBERT VAN VELDHOVEN@PHILIPS.COM	METHOD FOR MONITORING THE CACHE COHERENCE	EP	4105143,6	2006/05/04/08/04	EP	4105143,6	METHOD FOR MONITORING THE CACHE COHERENCE
AT040075	15-oct-04	07-Identification	JACQUES REBERG@PHILIPS.COM	SCALING A SIGMA DELTA MODULATOR, DYNAMIC RANGE ON DEM	EP	4300695,7	2006/05/04/08/04	EP	4300695,7	SCALING A SIGMA DELTA MODULATOR, DYNAMIC RANGE ON DEM
AT040077	15-oct-04	07-Identification	CHRISTIAN SCHERABON@PHILIPS.COM	POWER SAVING TECHNIQUE FOR DIGITALLY IMPLEMENTED RAD	GB	423011,6	2006/05/04/08/04	GB	423011,6	POWER SAVING TECHNIQUE FOR DIGITALLY IMPLEMENTED RAD
NLD041158	14-oct-04	01-CMOS and embedded	ERNEST HASELSTEINER@PHILIPS.COM	DEVICE FOR OPTIMUM EFFICIENCY AND SAFETY OF A DC VOLT	EP	4300682,4	2006/05/04/08/04	EP	4300682,4	DEVICE FOR OPTIMUM EFFICIENCY AND SAFETY OF A DC VOLT
US040070	14-oct-04	01-CMOS and embedded	PRABHAT AGARWAL@PHILIPS.COM	METHOD OF OPERATING A RFID SYSTEM	EP	4185067,5	2006/04/07/24-A	EP	4185067,5	METHOD OF OPERATING A RFID SYSTEM
NLD041156	14-oct-04	01-CMOS and embedded	PRABHAT AGARWAL@PHILIPS.COM	METHOD OF OPERATING A RFID SYSTEM	EP	4185037,8	2006/04/07/24-A	EP	4185037,8	METHOD OF OPERATING A RFID SYSTEM
US040421	14-oct-04	06-Connectivity	T.SATO@PHILIPS.COM	RANDOM NUMBER GENERATOR BASED ON PHYSICALLY UNCLON	EP	4104984,2	2006/04/07/24-A	EP	4104984,2	RANDOM NUMBER GENERATOR BASED ON PHYSICALLY UNCLON
US040421	14-oct-04	06-Connectivity	T.SATO@PHILIPS.COM	RANDOM NUMBER GENERATOR BASED ON PHYSICALLY UNCLON	EP	4104984,2	2006/04/07/24-A	EP	4104984,2	RANDOM NUMBER GENERATOR BASED ON PHYSICALLY UNCLON
NLD041138	14-oct-04	09-Discretes and Multimate	HILBERT ZHANG@PHILIPS.COM	WIMEDIA BANDWIDTH SHARING RULE	US	6061831,1	2006/04/07/24-A	US	6061831,1	WIMEDIA BANDWIDTH SHARING RULE
NLD041140	14-oct-04	09-Discretes and Multimate	JAN JAP KONING@PHILIPS.COM	HETERODYNEOUS USB-TO-IDE ADAPTER IS ONE ADAPTER COU	US	6061831,2	2006/04/07/24-A	US	6061831,2	HETERODYNEOUS USB-TO-IDE ADAPTER IS ONE ADAPTER COU
DE040270	13-oct-04	04-Audio/Video	GIUSEPPE BRILLO@PHILIPS.COM	DEEP PLUG FOR HIGH SIDE PDMOS IN SDI	EP	4105042,8	2006/04/07/26	EP	4105042,8	DEEP PLUG FOR HIGH SIDE PDMOS IN SDI
NLD041124	12-oct-04	05-Connectivity	MIHAI SANDULEANU@PHILIPS.COM	FRONTSIDE CONTACT ON SILICON-ON-INSULATOR SUBSTRATE	EP	4185047,7	2006/04/07/26-A1	EP	4185047,7	FRONTSIDE CONTACT ON SILICON-ON-INSULATOR SUBSTRATE
NLD041178	11-oct-04	05-Mobile Communication	ALEXANDER LAMPE@PHILIPS.COM	ALL N-TYPE TRANSISTOR HIGH-SIDE MIRROR	US	6061856,4	2006/04/07/26-A1	US	6061856,4	ALL N-TYPE TRANSISTOR HIGH-SIDE MIRROR
GB040204	9-oct-04	09-Discretes and Multimate	KIMLE PHAN@PHILIPS.COM	SWITCH DEVICE AND COMMUNICATION NETWORK	EP	4104984,2	2006/04/07/26-A1	EP	4104984,2	SWITCH DEVICE AND COMMUNICATION NETWORK
NLD041114	8-oct-04	04-Audio/Video	STEVEN PEAKE@PHILIPS.COM	OUTPUT STAGE	EP	4104984,1	2006/04/07/26-A2	EP	4104984,1	OUTPUT STAGE
NLD041113	8-oct-04	04-Audio/Video	HANS BREKELMANS@PHILIPS.COM	NON-LINEAR FREQUENCY AND PHASE MEASUREMENT SCHEME	EP	4104975,7	2006/04/07/26-A1	EP	4104975,7	NON-LINEAR FREQUENCY AND PHASE MEASUREMENT SCHEME
NLD041107	8-oct-04	07-Identification	EMERIC UGJEN@PHILIPS.COM	LOGARITHMIC MAGNETIC FIELD SENSORS AND CURRENT SE	EP	4104863,6	2006/04/07/19-A1	EP	4104863,6	LOGARITHMIC MAGNETIC FIELD SENSORS AND CURRENT SE
NLD041074	8-oct-04	07-Identification	DIMA PRIKHODKO, ZDZIMOFF UNKNOWN	PLAAR VOMCS WITH ZERO QGD	EP	422478,2	2006/03/28/01/02-A2	EP	422478,2	PLAAR VOMCS WITH ZERO QGD
NLD041060	5-oct-04	06-Connectivity	ANDRE VAN BEZOUJEN@PHILIPS.COM	CAPACITOR ARRAY	EP	4104945,3	2006/03/31/02-A1	EP	4104945,3	CAPACITOR ARRAY
NLD041062	5-oct-04	06-Connectivity	DERK REEFMANN@PHILIPS.COM	DDCC CONVERTER CHIP FOR SEVERAL VOLTAGES WITH SINGLE	EP	4300667,5	2006/03/31/02-A2	EP	4300667,5	DDCC CONVERTER CHIP FOR SEVERAL VOLTAGES WITH SINGLE
NLD041071	5-oct-04	01-CMOS and embedded	MARKUS MULDER@PHILIPS.COM	DUAL BIAS CONTROL CIRCUIT	EP	4104896,8	2006/03/31/02-A1	EP	4104896,8	DUAL BIAS CONTROL CIRCUIT
NLD041078	5-oct-04	09-Discretes and Multimate	ROBERT VAN VELDHOVEN@PHILIPS.COM	IMPEDANCE DETECTOR	EP	452272,7	2006/03/31/02-A2	EP	452272,7	IMPEDANCE DETECTOR
NLD041112	4-oct-04	04-Audio/Video	PIERRE DE GREEF@PHILIPS.COM	LUMB MULTI-BAND PHASE CODING WITH REDUCED TIMING REQUIR	FR	482272,7	2006/03/31/02-A2	FR	482272,7	LUMB MULTI-BAND PHASE CODING WITH REDUCED TIMING REQUIR
FR040169	4-oct-04	04-Audio/Video	PIERRE DE GREEF@PHILIPS.COM	LUMB MULTI-BAND PHASE CODING WITH REDUCED TIMING REQUIR	FR	482272,7	2006/03/31/02-A2	FR	482272,7	LUMB MULTI-BAND PHASE CODING WITH REDUCED TIMING REQUIR
NLD041057	30-sep-04	01-Discretes and Multimate	CAROLINE CARIN@PHILIPS.COM	VARACTOR BASED IMPEDANCE MATCHING NETWORK	EP	4104854,7	2006/03/31/05-A1	EP	4104854,7	VARACTOR BASED IMPEDANCE MATCHING NETWORK
NLD041041	30-sep-04	02-Packaging & Testing	DANIELLE BEENEN@PHILIPS.COM	OVERDRIVE SYSTEM FOR MOBILE LCD	EP	4300650,1	2006/03/31/05-A1	EP	4300650,1	OVERDRIVE SYSTEM FOR MOBILE LCD
NLD041056	30-sep-04	04-Audio/Video	ARNAUD HERBERT@PHILIPS.COM	HARD MACRO PARAMETRIZABLE PIN SIDE	EP	4104797,8	2006/03/31/05-A1	EP	4104797,8	HARD MACRO PARAMETRIZABLE PIN SIDE
FR040162	30-sep-04	05-Mobile Communication	PAUL BUCKNELL@PHILIPS.COM	PHASE CHANGE MEMORY, ADDITIONAL WORDLINE, PROGRAMM	EP	4104791,1	2006/03/31/05-A1	EP	4104791,1	PHASE CHANGE MEMORY, ADDITIONAL WORDLINE, PROGRAMM
AT040073	30-sep-04	08-Automotive	JEAN-LUC LUONG@PHILIPS.COM	PROTECTIVE COATING BASED ON INTERNAL POROUS STRUCTUR	EP	4300646,2	2006/03/31/05-A1	EP	4300646,2	PROTECTIVE COATING BASED ON INTERNAL POROUS STRUCTUR
DE040253	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	BAND SWITCH TECHN FOR INTEG, WIDE RANGE VOLT, CONTR, C	EP	4300640,1	2006/03/31/05-A1	EP	4300640,1	BAND SWITCH TECHN FOR INTEG, WIDE RANGE VOLT, CONTR, C
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...
US040356	29-sep-04	01-CMOS and embedded	WOLFGANG EUEHN@PHILIPS.COM	OPTIMISED EXTRACTION OF TRANSPORT...	US	6061819,5	2006/03/31/05-A1	US	6061819,5	OPTIMISED EXTRACTION OF TRANSPORT...

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USDA030373	29-sep-04	01-CMOS and embedded in TIM.POMTIUS@PHILIPS.COM	TWO-PHASE DATA TRANSFER PROTOCOL	US	60/614391
NL041025	29-sep-04	05-Mobile Communication	INDIRECT CURRENT SENSING CIRCUIT	EP	4104739.8
US040382	29-sep-04	05-Mobile Communication	HARMONIC GENERATION OF A FUNDAMENTAL FREQUENCY SYSTEM	US	60/614394
US040337	28-sep-04	01-CMOS and embedded in HKONGJANG.SUN@PHILIPS.COM	INTEGRATED SiC METAL THIN FILM RESISTORS FOR...	US	60/613998
NL041043	28-sep-04	04-Audio/Video	C MEM. ARRIBER: LOW LATENCY OK IF TIME REMAINS FOR VRT IN WIER	US	4104709.3
602631	28-sep-04	05-Mobile Communication	Improvement of the bandwidth of current-controlled DC/DC buck-boost or EP	EP	5300754.8
FR040164	28-sep-04	05-Mobile Communication	LIMITATION OF THE AVERAGE OUTPUT CURRENT OF CURRENT-Ci EP	EP	4300633.9
NL041023	27-sep-04	06-Connectivity	METHOD & SYS FOR COMMUNICATING WISTATIONS IN MULTIPLE US	US	60/614244
FR040183	27-sep-04	01-CMOS and embedded in VIET NGUYENHOANG@PHILIPS.COM	FLEXIBLE RINSING STEP IN A CMP TOOL	EP	4104586.3
082540	27-sep-04	02-Packaging & Testing	BOLTON PAPS WITH TEST LOGIC	EP	4300629.5
NL041226	27-sep-04	02-Packaging & Testing	Magnetic pointing device having a flexible-magnet, joystick	EP	5107850.9
NL041448	27-sep-04	02-Packaging & Testing	TIME AND OUTPUT DEPENDENT POINTER SPEED FOR ANALOG JO EP	EP	4102326.4
NL060251	27-sep-04	05-Mobile Communication	MAGNETIC SENSOR FOR INPUT DEVICES	EP	4106743
GR040196	25-sep-04	05-Mobile Communication	THREE AXIS MAGNETIC MODULE FOR JOYSTICK OR ACCELEROMETER	EP	5102254.9
FR040161	23-sep-04	01-CMOS and embedded in ERIC.VERNAKONI@PHILIPS.COM	FORMATION OF ULTRASHALLOW ULTRASTEER PHOSPHORUS PR GB	GB	421407.8
AT040071	23-sep-04	05-Mobile Communication	ANALOGUE TEST STRUCTURE FOR MEASURING POLY-TO-CONTAL EP	EP	4300618.8
NL041004	22-sep-04	05-Mobile Communication	NEW CONTACTING METHOD FOR A MOVED COIL VIA COATED FOIL EP	EP	4104636.9
NL041005	21-sep-04	04-Audio/Video	MEMORY CONTROL WITH SELECTIVE DATA RETENTION	EP	4104588.1
NL040398	21-sep-04	05-Mobile Communication	COMBINED SHUFFLE AND ROTATE OPERATION IN SINGLE CLOCKI EP	EP	4104559.2
AT040095	17-sep-04	07-Identification	PEAK VOLTAGE PROTECTION	EP	4104569.1
NL040388	16-sep-04	04-Audio/Video	ATTENDANCE CHECK OF OBJECTS	EP	4104517
US040363	16-sep-04	04-Audio/Video	A NEW VARIABLE GAIN AND CONTROLLED INPUT IMPEDANCE LNA EP	US	60/610754
US040354	16-sep-04	04-Audio/Video	MEMORY OPTIMIZATION FOR VIDEO PROCESSING	US	60/610751
NL040356	15-sep-04	04-Audio/Video	MULTI-LAYER VIDEO/GRAPHICS BLENDING INCLUDING IDENTIFYN US	US	60/610781
GB040198	15-sep-04	08-Discretes and Multimedia	SONGS WITH SELF ALIGNED ST	EP	4104455.3
GB040181	15-sep-04	08-Discretes and Multimedia	DIGITAL TEMPERATURE SENSORS AND CALIBRATION THEREOF GB	GB	420486.3
NL040387	14-sep-04	04-Audio/Video	CIRCUIT FOR THE PEAK BIASSING OF THE COLLECTOR CURRENT. GB	GB	430484.8
NL040388	14-sep-04	04-Audio/Video	LINEARIZATION OF UNIFORM PWM MODULATION IN CLASS D AMPI EP	EP	4104420.7
NL040388	14-sep-04	04-Audio/Video	DELAY CONTROL CIRCUIT AND METHOD	EP	4104425.6
NL040388	14-sep-04	05-Mobile Communication	CIRCUIT FOR DETECTION THE IMPEDANCE OF A LOAD	EP	4104433
NL040388	14-sep-04	05-Mobile Communication	DC-DC CONVERTER	EP	4104434.8
NL040388	13-sep-04	05-Mobile Communication	PLL	EP	4104413.2
GB040178	10-sep-04	04-Audio/Video	DIGITAL OPTIMAL BASED BIASING FOR LCD-PANEL, POWER REDLI GB	GB	420051.5
US040356	10-sep-04	06-Connectivity	A NEW TUNABLE CASCODE LNA W/FLAT GAIN RESPONSE OVER A US	US	60/606907
GB040172	9-sep-04	03-RF Devices	IMPROVEMENTS IN OR RELATING TO ANTENNA MATCHING IN VIDEO EP	EP	4300586.9
GB040171	8-sep-04	04-Audio/Video	METHOD FOR LOW-COST INTERCONNECTION IN SIMD ARCHTECT GB	GB	420004.4
NL040336	8-sep-04	01-CMOS and embedded in ANTERHEA.ABBO@PHILIPS.COM	INPUT HYSTERESIS BY MODULATION OF THRESHOLD VOLTAGE EP	EP	4104318.6
GB040174	8-sep-04	02-Packaging & Testing	BUILT IN SELF TEST INTERCONNECT MACHINE	GB	419868.5
FR040182	8-sep-04	04-Audio/Video	A SWITCHABLE BANDWIDTH TRACKING INPUT FILTER FOR IMPRO EP	EP	4300584.2
GB040175	8-sep-04	08-Discretes and Multimedia	LOW VOLTAGE TUNABLE TRENCH MOSFET	GB	419867.7
NL040357	3-sep-04	04-Audio/Video	LOW COST LCD OVERDRIVE SYSTEM	EP	4104268
FR040170	3-sep-04	08-Automotive	AT RUNTIME CONFIGURABLE MEMORY INTERFACE FOR MULTIPLE EP	EP	4104247.4
FR040171	3-sep-04	08-Discretes and Multimedia	A MERGED PINFET	GB	419556.6
NL040928	2-sep-04	08-Discretes and Multimedia	SELECTIVE EPITAXIAL GROWTH OF SILICON IN DEEP TRENCHES GB	GB	419558.2
NL040929	2-sep-04	01-CMOS and embedded in JOSINE.LOO@PHILIPS.COM	FULLY DEPLETED MOSFET WITH ULTRA-THIN CHANNEL	EP	4104204.5
US040336	2-sep-04	01-CMOS and embedded in RADJI.SURDEANU@PHILIPS.COM	DOUBLE GATE DEVICE WITH HIGHLY CONTROLLED CHANNEL THIK EP	EP	4104208
EP040238	2-sep-04	07-Identification	CONTACTING AND FILLING DEEP-TRENCH ISOLATION W/TUNGSTE US	US	60/606979
EP040237	2-sep-04	08-Automotive	CHIP ARRANGEMENT FOR IDENTIFICATION PAPERS	EP	4104216.8
NL040917	31-aug-04	02-Packaging & Testing	DISTRIBUTED COMMUNICATION SYSTEM	EP	4104223.7
NL040918	31-aug-04	02-Packaging & Testing	SUPPLY VOLTAGE COMPARTOR	EP	4104217.7
NL040919	31-aug-04	07-Identification	SIDE LENGTHS OF TEST CONTACTS SMALLER THAN 40 NM	EP	4104189
US040381	31-aug-04	06-Connectivity	CYCLED RECEIVER FOR MOBILE WIRELESS DEVICES	US	60/606833
FR040157	31-aug-04	07-Identification	METHOD TO MANUFACTURE A REID ANTENNA	EP	4104156.1
US040381	31-aug-04	07-Identification	IC WITH BUMPS HAVING DIFFERENT HEIGHTS	EP	4104176.5
US040348	30-aug-04	06-Connectivity	WIRELESS LAN DEVICE SUPPORT FOR PEER TO PEER NETWORKI US	US	60/605919
FR040145	27-aug-04	05-Mobile Communication	SHAPING OF A CIRCULAR WINDED VOICE COIL INTO ANOTHER SH EP	EP	4104121.1
US040346	27-aug-04	06-Connectivity	MASSIVELY SCALABLE PROGRESSIVE DOWNLOAD USING SLICING EP	EP	4300538.6
US040394	26-aug-04	07-Identification	METHODS AND APPARATUS FOR INTRASYSTEM AND INTERSYS US	US	60/605114
FR040144	26-aug-04	04-Audio/Video	METHODS AND APPARATUS FOR CHANNEL ESTIMATION FOR W US	US	60/605065
US040343	25-aug-04	06-Connectivity	TAG ANTENNA WITH A FOLDED DIPOLE STRUCTURE	EP	4104088.1
US040162S	24-aug-04	01-CMOS and embedded in CHARLES.RAZZELL@PHILIPS.COM	RF SELECTION SWITCH FOR MULTIPLE ANTENNA INPUT	EP	4300535.2
NL041040	24-aug-04	01-CMOS and embedded in JACOB.HOOKER@PHILIPS.COM	CLEAR CHANNEL ASSESSMENT FOR UWB COMMUNICATIONS SYS US	US	60/604769
US040342	23-aug-04	01-CMOS and embedded in ABBAS.RASTEGAR.296355@PF.UNKNOW	Method for Fabricating Dual Work Function Semiconductor Devices EP	EP	5105562.2
			DUAL WORK FUNCTION SEMICONDUCTOR DEVICES	EP	4104489.2
			HOT SOURCE CLEANING SYSTEM AND METHOD THEREFOR	US	60/6038915

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Patent No.	Title	Inventor	Classification	Date	Pub. No.	Pub. Date
GB040162	05-Mobile Communication	ANDY YULE@PHILIPS.COM	05-Mobile Communication	23-aug-04	418766.2	2005010202A1
US040324	01-CMOS and embedded pr	PETER MAGNE@PHILIPS.COM	01-CMOS and embedded pr	20-aug-04	60003483	2004010602A1
FR040140	03-RF Devices	KEVIN BYLE@PHILIPS.COM	03-RF Devices	20-aug-04	4255007.9	2004010602A1
US040336	04-Audio/Video	JAN-WILLEM VAN DE WAERT@PHILIP	04-Audio/Video	20-aug-04	60203103	2004010602A1
DE040224	07-Identification	ANDREAS GAKIS@PHILIPS.COM	07-Identification	20-aug-04	4104002.3	2004010602A1
GB040187	09-Discretas and Multimar	STEVEN PEAKE@PHILIPS.COM	09-Discretas and Multimar	20-aug-04	418337	2004010602A1
GB040166	05-Mobile Communication	MIKE D. JAMES@PHILIPS.COM	05-Mobile Communication	18-aug-04	418373.2	2004010602A1
GB040166	08-Discretas and Multimar	AMRITA DESHPANDE@PHILIPS.COM	08-Discretas and Multimar	17-aug-04	60502140	2004010602A1
NL040376	05-Mobile Communication	DIMA PRKHODKO.20200@PF.UNKNOW	05-Mobile Communication	13-aug-04	4103921.5	2004010602A1
FR040138	01-CMOS and embedded pr	MARKUS MULLER@PHILIPS.COM	01-CMOS and embedded pr	13-aug-04	4306536.8	2004010602A1
NL040387	04-Audio/Video	HANS BREKELMANS@PHILIPS.COM	04-Audio/Video	13-aug-04	4102912.4	2004010602A1
NL040381	05-Mobile Communication	DICK BUEHNER@PHILIPS.COM	05-Mobile Communication	13-aug-04	4103911.6	2004010602A1
NL040319	05-Mobile Communication	DICK BUEHNER@PHILIPS.COM	05-Mobile Communication	9-aug-04	4103918	2004010602A1
NL040380	05-Mobile Communication	STEVEN-JAN GERRITSEN@PHILIPS.	05-Mobile Communication	8-aug-04	4103925	2004010602A1
NL040380	05-Mobile Communication	ONNO KUIJKEN@PHILIPS.COM	05-Mobile Communication	8-aug-04	4103762	2004010602A1
NL040382	06-Connectivity	MUSTAFA ACAR.28234@PF.UNKNOW	06-Connectivity	6-aug-04	4183904.3	2004010602A1
AT040048	07-Identification	PRASHANT DEKATE@PHILIPS.COM	07-Identification	4-aug-04	4183765.3	2004010602A1
AT040030	07-Identification	JACQUES REBERG@PHILIPS.COM	07-Identification	4-aug-04	4260515.6	2004010602A1
NL040384	06-Connectivity	FRENK GRABER@PHILIPS.COM	06-Connectivity	3-aug-04	4103752.4	2004010602A1
NL040336	05-Mobile Communication	CHEE YU NG@PHILIPS.COM	05-Mobile Communication	2-aug-04	4103727.6	2004010602A1
FR040126	05-Mobile Communication	SEBASTIAN LESNER@PHILIPS.COM	05-Mobile Communication	30-jul-04	4103711	2004010602A1
GB040156	04-Audio/Video	PATRICK FULCHER@PHILIPS.COM	04-Audio/Video	29-jul-04	4306593.2	2004010602A1
NL040382	04-Audio/Video	ANDY MEGO@PHILIPS.COM	04-Audio/Video	29-jul-04	4186871.1	2004010602A1
FR040123	05-Mobile Communication	EDWIN SCHAPENDONK@PHILIPS.COM	05-Mobile Communication	29-jul-04	4103646.8	2004010602A1
FR040124	05-Mobile Communication	MARKUS HELFENSTEIN@PHILIPS.COM	05-Mobile Communication	29-jul-04	4300494.4	2004010602A1
FR040125	05-Mobile Communication	PETER BOPE@PHILIPS.COM	05-Mobile Communication	29-jul-04	4300496.9	2004010602A1
NL040381	05-Mobile Communication	MIMI SANDULEANU@PHILIPS.COM	05-Mobile Communication	29-jul-04	4300497.7	2004010602A1
AT040048	07-Identification	REINHARD ROGY@PHILIPS.COM	07-Identification	28-jul-04	4163647.5	2004010602A1
US040116	08-Discretas and Multimar	KEITH HEPPEINSTALL@PHILIPS.COM	08-Discretas and Multimar	27-jul-04	4103656.7	2004010602A1
US040315	01-CMOS and embedded pr	ROGER SCHUTTER@PHILIPS.COM	01-CMOS and embedded pr	27-jul-04	418882.9	2004010602A1
AT040046	07-Identification	MARK WALLIS@PHILIPS.COM	07-Identification	26-jul-04	60591834	2004010602A1
DE040198	07-Identification	VICTOR VENZ@PHILIPS.COM	07-Identification	26-jul-04	4103596.2	2004010602A1
FR040077	10-Other	CHRISTIAN LANGZ@PHILIPS.COM	10-Other	26-jul-04	4103592.7	2004010602A1
FR040111	10-Other	VOLKER TIMM.89699@PF.PHILIPS.COM	10-Other	26-jul-04	4103590.3	2004010602A1
FR040117	05-Mobile Communication	HEMO SCHELICHER@PHILIPS.COM	05-Mobile Communication	22-jul-04	4103570	2004010602A1
FR040112	05-Mobile Communication	RAINER PIETIG@PHILIPS.COM	05-Mobile Communication	22-jul-04	4300456.9	2004010602A1
DE040201	08-Automotive	MARCO RUFFINI.28305@PF.PHILIPS.	08-Automotive	22-jul-04	4300468.7	2004010602A1
DE040113	01-CMOS and embedded pr	ANDREA SCENINI@PHILIPS.COM	01-CMOS and embedded pr	22-jul-04	4103518.3	2004010602A1
8905009	01-CMOS and embedded pr	FRED HURKX@PHILIPS.COM	01-CMOS and embedded pr	20-jul-04	5102902.5	2004010602A1
IT040044	07-Identification	ACHIM HILBERS@PHILIPS.COM	07-Identification	20-jul-04	4183456.2	2004010602A1
GB040152	09-Discretas and Multimar	ADAM BROWN@PHILIPS.COM	09-Discretas and Multimar	20-jul-04	418175.8	2004010602A1
FR040152	08-Discretas and Multimar	JOHN CUTLER@PHILIPS.COM	08-Discretas and Multimar	20-jul-04	416174.1	2004010602A1
NL040383	02-Packaging & Testing	FRED HURKX@PHILIPS.COM	02-Packaging & Testing	19-jul-04	4183456.8	2004010602A1
FR040168	05-Mobile Communication	JOOP VAN LAMMEREN@PHILIPS.COM	05-Mobile Communication	15-jul-04	4300446	2004010602A1
US040302	05-Mobile Communication	GIUSEPPE MONTALBANO@PHILIPS.	05-Mobile Communication	15-jul-04	601598562	2004010602A1
FR040166	07-Identification	PAUL HURNER@PHILIPS.COM	07-Identification	16-jul-04	4103408.7	2004010602A1
FR040150	10-Other	RADU SURDEANU@PHILIPS.COM	10-Other	16-jul-04	415991.1	2004010602A1
NL0403780	01-CMOS and embedded pr	ANDRE MONTREE@PHILIPS.COM	01-CMOS and embedded pr	15-jul-04	4103562	2004010602A1
NL0403783	05-Mobile Communication	GUILAUME DE CREMONA@PHILIPS.C	05-Mobile Communication	13-jul-04	4183373.6	2004010602A1
NL0403786	02-Packaging & Testing	JEAN-CLAUDE SIX@PHILIPS.COM	02-Packaging & Testing	13-jul-04	4300442.3	2004010602A1
NL0403789	03-RF Devices	MARC VAN KLEEF@PHILIPS.COM	03-RF Devices	13-jul-04	4193314.3	2004010602A1
AT040042	07-Identification	HELMUT KRANBENTER@PHILIPS.COM	07-Identification	13-jul-04	4183342.4	2004010602A1
DE040188	09-Discretas and Multimar	ANDREAS BURY@PHILIPS.COM	09-Discretas and Multimar	8-jul-04	4193327.5	2004010602A1
DE040188	01-CMOS and embedded pr	WOLFGANG SCHNITZ@PHILIPS.COM	01-CMOS and embedded pr	8-jul-04	4103200.4	2004010602A1
DE040188	02-Packaging & Testing	KEES VAN KAAM@PHILIPS.COM	02-Packaging & Testing	7-jul-04	4103219.4	2004010602A1
DE040188	08-Automotive	LEO WOLTHEK@PHILIPS.COM	08-Automotive	7-jul-04	4103221	2004010602A1
AT040033	08-Automotive	LEO WOLTHEK@PHILIPS.COM	08-Automotive	6-jul-04	4103200.4	2004010602A1
DE040178	07-Identification	PETER RAGSAM@PHILIPS.COM	07-Identification	2-jul-04	4103132.9	2004010602A1
DE040178	04-Audio/Video	GERHARD RLETZ-KIRSCH@PHILIPS.	04-Audio/Video	2-jul-04	4103135	2004010602A1
DE040177	07-Identification	DIRK LUETZELBERGER@PHILIPS.COM	07-Identification	2-jul-04	4103135	2004010602A1

Patent No.	Date	Inventor	Title	IPC Class.	Company	Pub No.
AT0-00033	30-jun-04	JOACHIM SCHOBER@PHILIPS.COM	SIM MODULE FOR A MOBILE PHONE	02-Packaging & Testing	PHILIPS	4103068.5
DE040174	30-jun-04	MICHAEL WITTKER@PHILIPS.COM	TESTING OF AN APPLICATION CIRCUITRY WITHIN THE IC	02-Packaging & Testing	PHILIPS	4103081.8
DE040175	30-jun-04	ROBERT JOCHMERS@PHILIPS.COM	CONTENT MANAGING MODULE	04-Audio/Video	PHILIPS	4103093.4
DE040177	30-jun-04	MARTIN WAGNER_2@PHILIPS.COM	BIT-RATE-INDEPENDENT CODING OF DATA ON BUS SYSTEM	09-Automotive	PHILIPS	4103070.1
001231	10-jun-04	ROB VAN SCHALIK@PHILIPS.COM	Gate for vertical clocks of transmitter	10-Other	PHILIPS	4104806.4
NL040740	29-jun-04	F. DE BUYS@PHILIPS.COM	POLYPHASE INTERPOLATING FILTER WITH NOISE SHAPING, MODUL	04-Audio/Video	PHILIPS	4103016.4
NL040750	29-jun-04	F. DE BUYS@PHILIPS.COM	JITTER-FREE SAMPLE RATE CONVERSION	04-Audio/Video	PHILIPS	4103017.2
NL040770	29-jun-04	IGOR BLEDONOV@PHILIPS.COM	DOHERTY AMPLIFIER	04-Audio/Video	PHILIPS	4103046.1
GB040134	28-jun-04	MARK VAN DAL@PHILIPS.COM	METHOD FOR WORK FUNCTION ENGINEERING OF FULLY SILICIO	01-CMOS and embedded p	PHILIPS	4103086.5
NL040866	25-jun-04	HARPREET BHULLAR@PHILIPS.COM	LOW POWER INSTRUCTION PROCESSING CIRCUIT	08-Automotive	PHILIPS	4102954.8
DE040164	25-jun-04	MICHAEL HINZ@PHILIPS.COM	MEASURING OF POSITION AND ROTATIONAL SPEED	08-Discretles and Multimar	PHILIPS	4102955.4
NL040755	24-jun-04	LUUK TIEMEIJER@PHILIPS.COM	HIGH FREQUENCY TRANSISTOR LAYOUT	01-CMOS and embedded p	PHILIPS	4102847.1
NL040892	24-jun-04	PERI@PHILIPS.COM	ZERO POWER RADING	05-Mobile Communication	PHILIPS	4102838.4
US040234	24-jun-04	DIRK LUTZELBERGER@PHILIPS.COM	SUPERFRAME HAVING INCREASED DATA TRANSMISSION EFFICI	08-Connectivity	PHILIPS	60062592
AT040032	24-jun-04	LUUK TIEMEIJER@PHILIPS.COM	MOBILE PROFILE COMARISON	07-Identification	PHILIPS	4102846.3
NL040751	23-jun-04	ARNAUD ROSAY@PHILIPS.COM	PLAINEARE SPOEL MET MEER DAN TWEE AANSLUITINGEN	01-CMOS and embedded p	PHILIPS	4102816.8
FR040093	23-jun-04	ARNAUD ROSAY@PHILIPS.COM	DATE AND TIME RECOVERY FOR MOBILE PHONE	05-Mobile Communication	PHILIPS	4300390.4
FR040082	23-jun-04	NGUYEN TRI LUAN LE@PHILIPS.COM	AN OPTIMIZED LOW POWER Sallen & KEY FILTER	05-Mobile Communication	PHILIPS	4300393.8
NL040687	21-jun-04	CHRISTINE SCHENONGE@PHILIPS.COM	ADDRESSING STRATEGY FOR VITERBI	05-Mobile Communication	PHILIPS	4102633.2
NL040683	21-jun-04	VISHAL SURESH@PHILIPS.COM	SYSTEM FOR DEBUGGING SOFTWARE AND HARDWARE	02-Packaging & Testing	PHILIPS	4102834.1
FR040087	21-jun-04	MIUNO KUKARWIG@PHILIPS.COM	FEATURES FOR A MMIO MOBILE LINE A DIGITAL	04-Audio/Video	PHILIPS	4300395.3
FR040088	21-jun-04	ARNAUD ROSAY@PHILIPS.COM	PROCEDURE FOR FABRICATING A CAPACITIVE ELECTRODES SILL	04-Audio/Video	PHILIPS	4300396.5
DE040161	18-jun-04	ADAM MARHALMAGUI@PHILIPS.COM	VIDEO FORMAT DETECTOR	01-CMOS and embedded p	PHILIPS	4086874
GB040131	18-jun-04	MICHAEL PIETRYKI@PHILIPS.COM	LLP SIGNAL PROCESSING	04-Audio/Video	PHILIPS	4102811.9
NL040639	16-jun-04	XINYAN WU@PHILIPS.COM	STRAINED SILICON WITH SIGSE INCORPORATION	04-Audio/Video	PHILIPS	4102752.5
GB040172	16-jun-04	PHILIPPE MEUNIER-BEILLARD@PHILIPS.COM	LPF SIGNAL PROCESSING	04-Audio/Video	PHILIPS	4133963.3
NL040550	16-jun-04	KOEN MEINDS@PHILIPS.COM	FORWARD TEXTURE MAPPING 3D GRAPHICS SYSTEM	04-Audio/Video	PHILIPS	4102745.9
NL040631	16-jun-04	PIERRE WOERLEE@PHILIPS.COM	RESISTIVE MEMORY WITH PT DIODE AS SELECTION ELEMENT	04-Audio/Video	PHILIPS	4102744.2
NL040584	16-jun-04	KOEN MEINDS@PHILIPS.COM	INVERSE TEXTURE MAPPING 3D GRAPHICS SYSTEM	04-Audio/Video	PHILIPS	4102746.7
FR040083	16-jun-04	BERTRAND LEMELAT@PHILIPS.COM	STANDARD MICROPHONES INTERFACING SOLUTION	05-Mobile Communication	PHILIPS	4300390.5
NL040632	15-jun-04	ROB VAN SCHAIK@PHILIPS.COM	NON-VOLATILE MEMORY WITH ERASE GATE ON ISOLATION ZONE	05-Mobile Communication	PHILIPS	4102703.8
NL040635	15-jun-04	MAURICE NEUMAN@PHILIPS.COM	ENRICHUNG UND VERGÄHREN FLIER EIN MICROGERÄET	05-Mobile Communication	PHILIPS	4102706.1
NL040699	15-jun-04	MAURICE MEIJER@PHILIPS.COM	PERFORMANCE CONTROL WITH SLOW AND FAST LOOP	05-Mobile Communication	PHILIPS	4102713.7
NL040700	15-jun-04	FRANCESCO PESSANO@PHILIPS.COM	CLOSED LOOP CONTROL FOR PERFORMANCE TUNING	05-Mobile Communication	PHILIPS	4102719.4
NL040701	15-jun-04	ALAN CHANG@PHILIPS.COM	TRANSFER DESCRIPTOR FOR START AND COMPLETE TRANSACTI	05-Mobile Communication	PHILIPS	4102725.1
NL040702	15-jun-04	ALAN CHANG@PHILIPS.COM	INTERRUPT SCHEME FOR TO HANDLE REQUESTS FROM USB TRA	05-Mobile Communication	PHILIPS	4102727.7
NL040708	15-jun-04	ALAN CHANG@PHILIPS.COM	MSEC TRANSFER-BASED PROTOCOL FOR HS USB SLAVE	05-Mobile Communication	PHILIPS	4102728.5
NL040709	15-jun-04	ACHIM HILGERS@PHILIPS.COM	SECURITY MECHANISM FOR CORRELLOS TRANSDUCER SYSTEMS	05-Mobile Communication	PHILIPS	4102722.8
NL040710	15-jun-04	ERWIN HILZEN@PHILIPS.COM	WANGWIRES FOR POWER SEMICONDUCTOR DEVICES	05-Mobile Communication	PHILIPS	413310.4
AT040021	15-jun-04	DAVID RYON@PHILIPS.COM	SIM CONNECTOR LESS	05-Connectivity	PHILIPS	4300376.5
GB040127	15-jun-04	ERIC BERNASCONI@PHILIPS.COM	DEBUG & TRACE ON MULTILAYER A.I.B.	05-Connectivity	PHILIPS	60062595
FR040078	14-jun-04	ERIC BERNASCONI@PHILIPS.COM	HIGH-VOLTAGE DEVICE STRUCTURE	05-Mobile Communication	PHILIPS	4103133
FR040073	14-jun-04	TED LETAVIC@PHILIPS.COM	LOW JUNCTION ARCHITECTURE FOR DOUBLE GATE DEVICES	05-Mobile Communication	PHILIPS	4300325.5
FR040072	12-jun-04	RADI SURDENIL@PHILIPS.COM	NEW POWER AND SCALABLE BITE-RATE REGULATION FOR EMBEI	05-Mobile Communication	PHILIPS	4102830.6
FR040079	11-jun-04	MARKUS NEUMANN@PHILIPS.COM	SYNCHRONISATION SCHEME WITH COUPLED FREQUENCY DIVIDE	05-Mobile Communication	PHILIPS	4102845.1
NL040638	10-jun-04	BERTRAND VANDEWIELE@PHILIPS.COM	APPARATUS FOR RECEIVING SIGNALS VIA CHANNELS	05-Mobile Communication	PHILIPS	4102848.5
NL040608	10-jun-04	DIETRICH KARAYIDAS@PHILIPS.COM	10 BIT HIGH SPEED OFFSET FREE CYCLIC A/D CONVERTER	03-Discretles and Multimar	PHILIPS	4102816.2
EP040144	9-jun-04	CORO HEINRICH KOHSIEK@PHILIPS.COM	ONE-TIME CHALLENGE-RESPONSE AUTHENTICATION FOR	04-Audio/Video	PHILIPS	60062597
EP040145	9-jun-04	SANDER VAN RINGWOU@PHILIPS.COM	REDUCTION OF CRACKING IN LOW-K SPIN-ON-DIELECTRIC FILMS	07-Identification	PHILIPS	60062598
US040232	8-jun-04	HARBANS SACHDEV@PHILIPS.COM	A SINGLE OSCILLATOR ARCHITECTURE FOR WIDEBAND LO FREQ	01-CMOS and embedded p	PHILIPS	60062599
NL040835	8-jun-04	FATER SINGH@PHILIPS.COM	CALIBRATION METHOD FOR PASSIVE POLYPHASE FILTERS	04-Audio/Video	PHILIPS	4102965.1
US040243	7-jun-04	JAN VAN SINDEREN@PHILIPS.COM	CO-EXISTENCE OF 802.11 B/G AND BLUETOOTH	02-Packaging & Testing	PHILIPS	60062599
NL040590	4-jun-04	OLAF HIRSCH@PHILIPS.COM	IMPROVED MASK ETCHING	02-Packaging & Testing	PHILIPS	4102837
NL040572	28-mei-04	JAN KLOOSTERMAN@PHILIPS.COM	CLEANING A MASK SUBSTRATE	01-CMOS and embedded p	PHILIPS	600575177
NL040626	28-mei-04	JOSEF STOKKERMANN@PHILIPS.COM	DOT-COMMA GRAVITY COMPENSATOR	02-Packaging & Testing	PHILIPS	4102403.5
NL040606	28-mei-04	JOERME TUN@PHILIPS.COM	HOST BRIDGE	06-Connectivity	PHILIPS	4102368
US040257	28-mei-04	PEN LIU@PHILIPS.COM	EXTENDED CONVOLUTIONAL CODES	06-Connectivity	PHILIPS	60062598
IT040078	28-mei-04	HEIMO SCHEUCHER@PHILIPS.COM	CHIP WITH USEFUL LINES AND DURMY LINES	07-Identification	PHILIPS	60062598
GB04011E	28-mei-04	HEIMO SCHEUCHER@PHILIPS.COM	CHIPS WITH TWO GROUPS OF CHIP CONTACTS	07-Identification	PHILIPS	4102408.8
GB04011F	28-mei-04	HEIMO SCHEUCHER@PHILIPS.COM	REALISING LOCALLY SOLI-LIKE DEVICES ON A NORMAL BULK WAF	08-Discretles and Multimar	PHILIPS	60062598
GB04011G	28-mei-04	M.A.P. PERTUIS@EWI.TUDELFT.NL	BITSTREAM CONTROLLED DYNAMIC ELEMENT MATCHING	08-Discretles and Multimar	PHILIPS	4119971.5
NL040681	27-mei-04	JAN HOOGERBRUGGE@PHILIPS.COM	USING STATIC DATA AS PADDING BYTES	04-Audio/Video	PHILIPS	4102837.3



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NL040552	27-mei-04	04-Audio/Video	TON DITWIG@PHILIPS.COM	MRAM: REVERSED MAGNETIC TUNNEL JUNCTION, POWER REDUC	EP	4102362.3	200311022.A1
NL040554	27-mei-04	04-Audio/Video	MARCO BEKKOOL@PHILIPS.COM	SIGNAL PROCESSING APPARATUS	EP	4102359.8	200311020.A1
AT040026	27-mei-04	07-Identification	ACHIM HILGERS@PHILIPS.COM	TEMPERATURE DETECTION AND STORING METHOD	EP	4102355.6	200311025.A1
GB040113	25-mei-04	01-CMOS and embedded pr	JOSHUA LOO@PHILIPS.COM	PLANAR DOUBLE GATE MOSFET WITH SALICIDATION BEFORE SOI	GB	411821.5	200311019.A1
NL040555	25-mei-04	01-CMOS and embedded pr	PROBHAT AGARWAL@PHILIPS.COM	A NEW BICMOS DEVICE STRUCTURE FEATURING A COM	EP	4102284.9	200311018.A1
FR040071	25-mei-04	05-Mobile Communication	STEPHANE VALENTE@PHILIPS.COM	MPEG REDUCTION OF FLASHING EFFECT	EP	4300302.9	200311015.A1
US040226	25-mei-04	06-Connectivity	SCOTT GJO@PHILIPS.COM	METHOD AND APPARATUS FOR PASSING MESSAGES AND DATA B	US	6057464	200311015.A1
NL040546	24-mei-04	05-Mobile Communication	KOEN MEINDS@PHILIPS.COM	OPTIMISATION FOR TILED GEOMETRY STORAGE	EP	4102260.9	200311015.A1
US040227	21-mei-04	06-Connectivity	CHARLES RAZZELL@PHILIPS.COM	TRANSMITTER & RECEIVER FOR ULTRA-WIDEBAND OFDM SIGNAL	EP	6015730.5	200311015.A1
US040228	21-mei-04	06-Connectivity	HERMIE TIJH@PHILIPS.COM	SERVICE INFORMATION EXCHANGE & JOB MIGRATION FOR COLL	US	6015730.4	200311015.A1
FR040070	19-mei-04	04-Audio/Video	KWONG KAM CHODON@PHILIPS.COM	3 BAND TV RF INPUT CIRCUIT	EP	4300296.3	200311015.A1
NL040531	18-mei-04	01-CMOS and embedded pr	HANS BOEVE@PHILIPS.COM	DIGITAL MAGNETIC CURRENT SENSOR	EP	4102183.2	200311015.A1
FR040068	18-mei-04	05-Mobile Communication	EMMANUEL ARDICHVIL@PHILIPS.COM	TRANSPORT CHANNEL DECODING MANAGEMENT	EP	4300291.4	200311015.A1
NL040538	18-mei-04	06-Automotive	ARNOLD FRIEKE@PHILIPS.COM	TURBO DECODER INPUT REORDERING	EP	4300280.6	200311015.A1
FR040067	17-mei-04	09-Discretes and Multimar	PATRICK GALLUJ@PHILIPS.COM	DECODING OF INTERLACED COMPRESSED VIDEO AT A REDUCED	EP	4102156.0	200311015.A1
FR040064	14-mei-04	05-Mobile Communication	MARCO BEKKOOL@PHILIPS.COM	AMR-ANGLE SENSOR SENSOR	EP	4300280.7	200311015.A1
DE040124	14-mei-04	05-Mobile Communication	ARNOLD BOURGEOIS@PHILIPS.COM	DECODING OF INTERLACED COMPRESSED VIDEO AT A REDUCED	EP	4102117.1	200311015.A1
FR040062	13-mei-04	05-Mobile Communication	REINHARD BUCHHOLD@PHILIPS.COM	CALIBRATION TECHNIQUE FOR IP2 MIXERS	EP	4300277.3	200311015.A1
DE040122	13-mei-04	08-Automotive	BERND ELENDR@PHILIPS.COM	SHIELDED CONNECTION FOR COMMUNICATION NETWORK	EP	4102083.5	200311015.A1
NL040514	12-mei-04	04-Audio/Video	DIBAKAR DAS@PHILIPS.COM	DATA PROCESSING SYSTEM WITH TRACE CO-PROCESSOR	EP	4102049.6	200311015.A1
NL040544	11-mei-04	04-Audio/Video	PIERRE DE GREER@PHILIPS.COM	ADAPTIVE CONTRAST BOOSTER FOR MOBILE DISPLAYS	EP	4102033.9	200311015.A1
DE040118	11-08-04	09-Automotive	NICO BERCKMANS@PHILIPS.COM	CIRCUIT ARRANGEMENT AND METHOD OF OPERATING SUCH CIR	EP	4102025.6	200310976.A1
US040214	10-mei-04	02-Packaging & Testing	C.C. CHEN@PHILIPS.COM	CUT-OUT HEAT SLUGS FOR INTEGRATED CIRCUIT DEVICE PACKA	US	6015702.5	200310976.A1
GB040104	7-mei-04	05-Mobile Communication	MATTHEW BAKER@PHILIPS.COM	CONTROL OF TRANSMISSION FORMATS IN SOFT HANDOVER	GB	410218.9	200310976.A1
NL040480	6-mei-04	03-RF Devices	ARNOLD DEN DEKER@PHILIPS.COM	PASSI WITH LATERAL PINDIODES	EP	4101568.2	200310976.A1
NL040482	6-mei-04	03-RF Devices	CO VAN VEEEN@PHILIPS.COM	CHP-ON-CHIP WITH IMMENSIO SOLDERING BUMP	EP	4101563.9	200310976.A1
GB040102	6-mei-04	05-Mobile Communication	PAUL BUCKNELL@PHILIPS.COM	IMPROVED ARC SCHEME IN SOFT HANDOVER	GB	410170.1	200310976.A1
FR040067	5-mei-04	05-Mobile Communication	STEPHAN M KOCH@PHILIPS.COM	LOW POWER IC ARCHITECTURE IN STANDBY	EP	4101639.9	200310976.A1
DE040107	4-mei-04	02-Automotive	MARCO ROSGERO@PHILIPS.COM	DIRECTION DEPENDENT CHANNEL SELECTION FOR AD HOC NETW	EP	4101804.3	200310976.A1
NL040457	3-mei-04	04-Audio/Video	BART BARENBURG@PHILIPS.COM	PULLING-FREE IO GENERATION SYSTEM & METHOD	EP	4101890.4	200310976.A1
US040204	30-apr-04	01-CMOS and embedded pr	JAN PETER FRAMBACH@PHILIPS.COM	FLAT TUNER MODULE WITH STANDARD CONNECTOR	US	6015867.97	200310976.A1
NL040434	29-apr-04	04-Audio/Video	SOR-KIM LENG@PHILIPS.COM	USING ERROR DETECTION CIRCUITRY TO INCREASE MEMORY SP	EP	4300242.7	200310976.A1
AT040017	29-apr-04	04-Audio/Video	ERWALD FRASL@PHILIPS.COM	MEMORY CONTROLLER USING CONTROLLABLE DELAY UNIT	EP	4101849	200310976.A1
NL040473	29-apr-04	05-Mobile Communication	JAN VINK@PHILIPS.COM	MEMORY CONTROLLER USING CONTROLLABLE DELAY UNIT	EP	4101855.9	200310976.A1
NL040468	29-apr-04	09-Connectivity	DERK REEFMAN@PHILIPS.COM	OVAL SPEAKER MEMBRANE WITH REDUCED LATERAL FORCES	EP	4101951.6	200310976.A1
AT040020	29-apr-04	07-Identification	KLEEMENS BREITFLUSS@PHILIPS.COM	MIXING STAGE PRODUCES COLLISION SIGNAL	EP	4101835.6	200310976.A1
AT040023	29-apr-04	07-Identification	JACQUES REBERGA@PHILIPS.COM	SELECTING IDENTICAL CIRCUITS ON SERIAL BUS	EP	4101835.6	200310976.A1
US040202	29-apr-04	10-Other	BERNHARD SPIESS@PHILIPS.COM	TAG USED FOR MONITORING THE TIRE PRESSURE	EP	4101840.8	200310976.A1
NL040434	29-apr-04	01-CMOS and embedded pr	PADRAG OMACHONY@PHILIPS.COM	INTRUSION DETECTION DURING PROGRAM EXECUTION IN A COMI	EP	4101842.5	200310976.A1
NL0403870	27-apr-04	01-CMOS and embedded pr	WIBO VAN NOORT@PHILIPS.COM	P-STROPE PATTERN ON HIGH OHMIC SI SUBSTRATE	EP	4101770.8	200310976.A1
FR040040	27-apr-04	04-Audio/Video	JOSE PINEDA DE GYZE@PHILIPS.COM	SURFACE-PASSIVATED HIGH-RESISTIVITY SILICON	EP	4103876.5	05100603.A1
NL040418	27-apr-04	05-Mobile Communication	ARNAUD BOURGEOIS@PHILIPS.COM	INTEGRATED CIRCUIT LAYOUT	EP	4101784.1	200310976.A1
FR040045	27-apr-04	06-Connectivity	ARJAN BINK@PHILIPS.COM	LOW-COST RESIZING METHOD WITH SHARPNESS ENHANCEMENT	EP	4101748.4	200310976.A1
US040185	20-apr-04	06-Connectivity	FREDERIC SALVETTI@PHILIPS.COM	PIPELINED ASYNCHRONOUS INSTRUCTION PROCESSOR CIRCUIT	EP	40422	200310976.A1
US040186	20-apr-04	06-Connectivity	ELIE KHOURY@PHILIPS.COM	SACRIFICIAL MATERIAL USE FOR HOLLOW TRENCHES FABRICATI	EP	6015634.55	200310976.A1
US040188	20-apr-04	05-Mobile Communication	ELIE KHOURY@PHILIPS.COM	HIGH SPEED DIFFERENTIAL RECEIVER WIRAIL-TO-RAIL MODE DR	EP	6015634.54	200310976.A1
US040204	18-apr-04	02-Packaging & Testing	JUSTO BIAIRE@PHILIPS.COM	ULTRA LOW-POWER LVDS DRIVER W/BUILT-IN IMPEDANCE TERM	US	6015634.52	200310976.A1
NL040403	16-apr-04	05-Mobile Communication	ALAIN COUSIK@PHILIPS.COM	CANCELLATION OF RANDOM ERRORS OF UNIT ELEMENTS IN A DA	EP	4101625.4	200310976.A1
NL040401	15-apr-04	03-Mobile Communication	MARTIN HAEBSLER 2284@PHILIPS.COM	MATCHING COIL INTEGRATED ON TOP	EP	4300207	200310976.A1
FR040042	13-apr-04	05-Mobile Communication	OTMAR RENGERT@PHILIPS.COM	METHOD AND DEVICE FOR TRANSMITTING DATA PACKETS	EP	4101588.4	200310976.A1
FR040042	13-apr-04	05-Mobile Communication	WINFRID BIRTH@PHILIPS.COM	PHASE LOCKED LOOP CIRCUIT	EP	4101532.2	200310976.A1
FR040035	8-apr-04	03-RF Devices	GIUSEPPE MONTALBAND@PHILIPS.COM	DISCRETE 28MHZ OSCILLATOR	EP	4300203.9	200310976.A1
FR040038	6-apr-04	03-RF Devices	KEVIN BOYLE@PHILIPS.COM	IMPROVED SIR ESTIMATION	EP	4300186.6	05010035.A1
NL040342	1-apr-04	05-Mobile Communication	HANS BOEVE@PHILIPS.COM	DUAL PIFA ARRANGEMENT + MEANDERED SLOTS	EP	407001.8	200309040.A1
NL040365	31-mar-04	05-Mobile Communication	PETER BALTUS@PHILIPS.COM	THERMALLY STABLE REFERENCE VOLTAGE GENERATOR FOR MR	EP	4101382.5	200309040.A1
GB040028	31-mar-04	05-Mobile Communication	CHRIST BURDENS@PHILIPS.COM	REDUCING EVM IN A MULTICARRIER SIGNAL	EP	4101325.6	200309040.A1
NL040278	29-mar-04	09-Discretes and Multimar	CHRISTELLE LE CAM@PHILIPS.COM	PARALLEL ARRANGED POWER SUPPLIES	EP	4101326.9	200309040.A1
NL040320	29-mar-04	01-CMOS and embedded pr	YOUNI PONOMAREV@PHILIPS.COM	RESURF MOSFET WITH SI CAPPING LAYER	EP	407383.1	200309040.A1
NL040329	29-mar-04	06-Connectivity	MIHAI SANDULEANU@PHILIPS.COM	SELF ALIGNED SOURCE/ DRAIN CONTACTS IN DOUBLE-GATE FET	EP	4101291.5	05010035.A1
	29-mar-04	05-Discretes and Multimar	BAS PUTTER@PHILIPS.COM	PHASE-FREQUENCY DETECTOR	EP	4101276.2	200309040.A1
	29-mar-04	05-Discretes and Multimar	BAS PUTTER@PHILIPS.COM	SIGMA-DELTA CONVERTER WITH REDUCED INTER-SYMBOL INTER	EP	4101293.1	200309040.A1

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NLD40358	01-CMOS and embedded in	MARTIN LANKHORST@PHILIPS.COM	ELECTRIC DEVICE COMPRISING PHASE CHANGE MATERIAL	4101259.2	20050925.8	IMEC
US040181	02-Packaging & Testing	MUHAMMAD AZIMAN@PHILIPS.COM	A NEW EFFICIENT SOLUTION TO COVER RESISTIVE-OPEN DEFECTS IN SWITCHABLE LTCC ANTENNA MODULE	60556706	20050925.8	IMEC
US040185	07-Identification	HEIKO PELZER@PHILIPS.COM		4101236	20050925.8	IMEC
US040187	04-Audio/Video	ERWIN BELLER@PHILIPS.COM	METHOD OF MAKING ISOLATED SIC SURFACE ISLANDS	60556352	20050925.8	IMEC
NLD40297	01-CMOS and embedded in	WIBO VAN NOORT@PHILIPS.COM	USE OF DVB-AUDIO IN MOBILE TV RECEIVER	4101165.9	20050916.7	IMEC
GB040061	04-Audio/Video	GORKA GARCIA@PHILIPS.COM	INTEGRATED DEVICE IN A PCI EXPRESS HUB	406442.4	20050916.7	IMEC
US040184	08-Connectivity	DAVE EVOY@PHILIPS.COM	TRANSPORTING SYNCHRONIZATION EVENTS IN PCI EXPRESS LANES/PADS	60554505	20050916.7	IMEC
US040186	06-Connectivity	DAVE EVOY@PHILIPS.COM	DIRECTION CONTROL FOR DOUBLE GATE SOI PROCESSING	60554506	20050916.7	IMEC
US040188	19-mit-04	ROSE LINDO@IMEC.BE	FORMING A DAMASCENE STRUCTURE WITH AIR GAPS	4101087.7	1577594.2	IMEC
NLD40283	01-CMOS and embedded in	JOSINE LOO@IMEC.BE	FORMING A DAMASCENE STRUCTURE WITH AIR GAPS	4101087.6	1577594.2	IMEC
NLD40282	01-CMOS and embedded in	ROEL DAAMEN@PHILIPS.COM	ABRUPT AND DEFECT FREE JUNCTION FORMATION BY SPER	4101071.3	1577594.2	IMEC
NLD40276	16-mit-04	BARTEK PAWLAK@PHILIPS.COM	SMALL CRITICAL DIMENSIONS IN ORGANIC DIELECTRIC	4101072.9	1577594.2	IMEC
NLD40265	16-mit-04	GREJA VERHEIJDEN@PHILIPS.COM	FORMING A SEMICONDUCTOR DEVICE ON SOI USING SPER	4101078.6	1577594.2	IMEC
NLD40274	16-mit-04	BARTEK PAWLAK@PHILIPS.COM	METHOD OF TESTING AND TRIMMING OF SENSORS WITH REMOTE POSITIONING OF EOR AND F CD-IMPLANTATION	4006237.4	20050916.7	IMEC
DE040084	16-mit-04	MICHAEL HINZ@PHILIPS.COM	TAPERED UNIT CELL METAL OXIDE SEMICONDUCTOR HIGH-VOLTAGE FORMATION OF DUAL GATE TRANSISTOR	60553310	20050916.7	IMEC
NLD40280	15-mit-04	BARTEK PAWLAK@PHILIPS.COM		4101013.3	20050916.7	IMEC
US040065	15-mit-04	TED LETAVIC@PHILIPS.COM	FREQUENCY DIVIDER	4101016.6	20050916.7	IMEC
NLD40275	13-mit-04	BARTEK PAWLAK@PHILIPS.COM		4100996	20050916.7	IMEC
NLD40242	12-mit-04	JAN PETER FRAMBACH@PHILIPS.COM	ON-CHIP RING OSCILLATOR CALIBRATION	4100991.9	20050916.7	IMEC
NLD40284	11-mit-04	MUSTAFA ACAR.28234@FF.UNKNOWNS	MOBILE TERMINAL WITH REDUCED COMPONENTS	4100987.2	20050916.7	IMEC
NLD40268	10-mit-04	VISHAL CHOUDHARY@PHILIPS.COM	DYNAMIC SELECTION OF ACCESS STRATEGIES FOR SDRAM	4100984.6	20050916.7	IMEC
DE040068	10-mit-04	ECKHARD WALTERS@PHILIPS.COM	PIPELINE REDUCTION FOR ADAPTIVE WRITE BACK	4100975.4	20050916.7	IMEC
NLD40259	10-mit-04	ARTUR BURCHARD@PHILIPS.COM	PIPELINE REDUCTION FOR CRYPTOGRAPHY	4100962	20050916.7	IMEC
NLD40236	10-mit-04	ARJAN BINK@PHILIPS.COM	RSI TRENCHET PROCESS	405325.2	20050916.7	IMEC
GB040052	10-mit-04	GERHARD KOOPS@PHILIPS.COM	MPEG-2 DECODER WITH EMBEDDED BUFFER	4100928.7	20050916.7	IMEC
NLD40219	8-mit-04	HANS TICHELAAR@PHILIPS.COM	ELECTRONIC DEVICE WITH STRESS RELIEF ELEMENT	60553406	20050916.7	IMEC
US040144	5-mit-04	SCIENCE HABENICHT@PHILIPS.COM	DFT TECHNIQUE FOR STRESSING THE SELF-TIME SEMICONDUCTOR	60550416	20050916.7	IMEC
US040145	5-mit-04	MUHAMMAD AZIMAN@PHILIPS.COM	NEW TEST PATTERNS TO COVER RESISTIVE BRIDGE DEFECTS IN TEST ARRANGEMENT FOR RFID TRANSDUCERS	4100889.7	20050916.7	IMEC
US040146	5-mit-04	MUHAMMAD AZIMAN@PHILIPS.COM	NOVEL V-SHAPED ANTENNA	4100904.4	20050916.7	IMEC
AT040012	5-mit-04	ACHIM HILGERS@PHILIPS.COM	FLEXIBLE RF-SUBSYSTEM CONTROL INTERFACE	4300116.3	20050916.7	IMEC
FR040028	4-mit-04	JEAN-CLAUDE BINI@PHILIPS.COM	PROGRAMMABLE UP-DOWN CLOCK GENERATOR	4100876.4	20050916.7	IMEC
NLD40287	4-mit-04	FRANCESCO PESSOLANO@PHILIPS.COM	ALMOST-ONLINE RECORDING	4100873.1	20050916.7	IMEC
DE040062	4-mit-04	MCCENIGO@EXP-MATH.UNI-ESSEN.DE	VIDEO PROCESSING CIRCUIT AND METHOD OF VIDEO PROCESSING	4100847.5	20050916.7	IMEC
NLD40206	3-mit-04	PIETER VANDER WOLF@PHILIPS.COM	TRENCH WITH EXTRA DOPANT AROUND TRENCH	404747.8	20050916.7	IMEC
GB040056	3-mit-04	PHILIP RUTTER@PHILIPS.COM	A VERY LOW RESPECT TRENCHET	404751	20050916.7	IMEC
GB040055	3-mit-04	STEVEN PEAKE@PHILIPS.COM	WRAPPER CELL ARCHITECTURE FOR HIERARCHICAL CORES	605543650	20050916.7	IMEC
US040139	2-mit-04	STEVEN PEAKE@PHILIPS.COM	ADAPTIVE FEEDBACK SYSTEM FOR MPES DECODERS WITH CONSISTENT SYNCHRONIZATION MODULE	60548231	20050916.7	IMEC
NLD40245	1-mit-04	CHARLES RAZZEL@PHILIPS.COM	POWER AMPLIFIER OUTPUT IMPEDANCE CONTROL	4100805.3	20050916.7	IMEC
US040137	1-mit-04	TORSTEN FINK@PHILIPS.COM	SAMI VIRTUAL CARD RESET METHOD	404371.7	20050916.7	IMEC
NLD40164	1-mit-04	JOEY KESSEL@PHILIPS.COM	CLOCK FAIL DETECTION USING AN ASYNCHRONOUS PROCESSOR	4100784	20050916.7	IMEC
GB040044	27-Feb-04	KLEMS BREITLUS@PHILIPS.COM	IC INTRUSION DETECTION	4100769.9	20050916.7	IMEC
NLD40241	27-Feb-04	MARK DE CLERO@PHILIPS.COM	TRENCHES WITH SPACED SUB-TRENCH P-REGIONS	4100716.2	20050916.7	IMEC
NLD40156	24-Feb-04	ED HUANG@PHILIPS.COM	LIVE PICTURE INDEXING	40394.3	20050916.7	IMEC
GB040044	21-Feb-04	FRANCOIS AUDEON@PHILIPS.COM	MPEG VIDEO DECODING BASED ON SORTED MACROBLOCKS	4100653.4	20050916.7	IMEC
NLD40153	20-Feb-04	ONNO EERENBERG@PHILIPS.COM	SCALABLE SYSTEM CHIP	4100633.4	20050916.7	IMEC
US040038	20-Feb-04	MATTHIAS MUTH@PHILIPS.COM	HALF-SELENIUM RICHENING EN WERKWIJZE TER VERVAARDIGING EP UNIVERSAL DATA PATH TEST BUS	4100631.8	20050916.7	IMEC
US040166	19-Feb-04	RADU SURDANAU@PHILIPS.COM	2-STEP DECODER FOR DECODING VARIABLE LENGTH BLOCKS	4100653.7	20050916.7	IMEC
NLD40159	19-Feb-04	EDWIN VAN DALEN@PHILIPS.COM	DECENTRALIZED STREAM CONTROLLERS FOR DATA STREAM PROC METHODS & APPARATUS FOR WIRELESS PACKET HANDLING IN MULTIMEDIA	4100654.5	20050916.7	IMEC
NLD40157	19-Feb-04	AVNEESH MAHESHWARI@PHILIPS.COM	DIPLX-ANTENNA MODULE	60548387	20050916.7	IMEC
NLD40157	19-Feb-04	EDWIN VAN DALEN@PHILIPS.COM	MEMBRANE WITH SPRAYED PLASTIC MATERIAL ON IT	4100635.4	20050916.7	IMEC
US040131	19-Feb-04	ACHIM HILGERS@PHILIPS.COM	STILL TO VIDEO	4100628.9	20050916.7	IMEC
US040156	18-Feb-04	STEPH ANON@PHILIPS.COM	METAL VOID-METAL CAPACITANCE	4300084.3	20050916.7	IMEC
AT040008	17-Feb-04	PHILIPPE GENRICH@PHILIPS.COM	TEST OF PLL BY POWER SUPPLY VOLTAGE PROFILE	401482	20050916.7	IMEC
FR040021	17-Feb-04	AURELE HUMBERT@PHILIPS.COM	CONFIGURABLE SWITCHING NETWORK FOR DSP ARCHITECTURE	4100540.6	20050916.7	IMEC
FR040020	13-Feb-04	JOSE PINEA DE GIVEZ@PHILIPS.COM	LINKING REMOTE CONTROL TO AUDIO SOURCE	402952.6	20050916.7	IMEC
NLD40189	12-Feb-04	RENE VAN DEN BERG@PHILIPS.COM	HV DRIVES WITH FAST LV OPERATION	4100503.6	20050916.7	IMEC
NLD40134	11-Feb-04	IWO MERGLER@PHILIPS.COM				
GB040034	11-Feb-04	MAURITS STORMS@PHILIPS.COM				
NLD40142	11-Feb-04					

Patent No.	Filing Date	Inventor/Assignee	Title	IPC Class	Pub No.	Pub Date	IPC Class	Pub No.	Pub Date
NL040010	5-fab-04	10-Other	LATCH	02-Other	4100427.6	5-fab-04	EP	4100427.6	5-fab-04
US0402110	4-feb-04	MIHAL SANDULEANU@PHILIPS.COM		02-Packaging & Testing	505641261	4-feb-04	EP	505641261	4-feb-04
DE040034	30-jan-04	02-Packaging & Testing	METHOD & APPARATUS FOR INCREASING ROUTING DENSITY FOR US	02-Packaging & Testing	4100342.7	30-jan-04	US	4100342.7	30-jan-04
NL040075	29-jan-04	99-Discretes and Multimat	IC CHIP WITH INTEGRATED ESD-PROTECTION DEVICE	09-Discretes and Multimat	4100326	29-jan-04	EP	4100326	29-jan-04
US040095	29-jan-04	05-Mobile Communication	ECHO CANCELLER WITH INTERFERENCE-LEVEL CONTROLLED ST EP	05-Mobile Communication	1002402004151	29-jan-04	US	1002402004151	29-jan-04
DE040023	29-jan-04	06-Connectivity	ENHANCED SPECTRAL KEYING FOR WIRELESS ULTRA WIDE BAND US	06-Connectivity	4100326	29-jan-04	EP	4100326	29-jan-04
NL040078	29-jan-04	08-Automotive	CAR OWNER IDENTIFICATION	08-Automotive	4100313.8	29-jan-04	EP	4100313.8	29-jan-04
NL040019	28-jan-04	05-Discretes and Multimat	PROGRAMMABLE AND PAUSABLE CLOCK GENERATION UNIT	05-Discretes and Multimat	4100289	28-jan-04	EP	4100289	28-jan-04
NL040064	28-jan-04	02-Packaging & Testing	TEST-CHIP & TEST METHODOLOGY FOR LATCH-UP RISK ASSESSN EP	02-Packaging & Testing	4100300.5	28-jan-04	EP	4100300.5	28-jan-04
NL040097	28-jan-04	04-Audio/Video	ROM: INHIBIT 2ND AND PHASE OF READ CYCLE	04-Audio/Video	4100301.3	28-jan-04	EP	4100301.3	28-jan-04
US040074	28-jan-04	04-Audio/Video	FREE-RUNNING NUMERICALLY-CONTROLLED OSCILLATOR USING US	04-Audio/Video	60559725	28-jan-04	US	60559725	28-jan-04
NL040074	28-jan-04	04-Audio/Video	HF NOISE REDUCTION IN SIGMA-DELTA DIA CONVERTERS	04-Audio/Video	4100305.4	28-jan-04	EP	4100305.4	28-jan-04
NL040080	27-jan-04	07-Identification	PROTECTION AGAINST POWER ANALYSIS ATTACKS	07-Identification	4100279.1	27-jan-04	EP	4100279.1	27-jan-04
US040087	26-jan-04	06-Connectivity	FREQUENCY GENERATION FOR A MULTI-BAND OFDM BASED ULTR US	06-Connectivity	40959427	26-jan-04	US	40959427	26-jan-04
GB040023	24-jan-04	05-Mobile Communication	SOURCE GATED PHOTOTRANSISTOR	05-Mobile Communication	4091678	24-jan-04	GB	4091678	24-jan-04
US040056	23-jan-04	01-CMOS and embedded pr	PHILIPPE MELNIER-REILLARD@PHILIPS.COM	01-CMOS and embedded pr	507558909	23-jan-04	US	507558909	23-jan-04
US040041	20-jan-04	08-Automotive	METHOD OF FABRICATING A MONO-CRYSTALINE EMITTER	08-Automotive	4100189.4	20-jan-04	EP	4100189.4	20-jan-04
NL040062	19-jan-04	02-Packaging & Testing	VOLTAGE REGULATOR WITH SELECTABLE OUTPUT VOLTAGE	02-Packaging & Testing	60597800	19-jan-04	EP	60597800	19-jan-04
NL040065	19-jan-04	02-Packaging & Testing	ADVANCED CONTROL DEVICE FOR HOME ENTERTAINMENT UTILI2 US	02-Packaging & Testing	4100143.9	19-jan-04	EP	4100143.9	19-jan-04
NL040039	19-jan-04	04-Audio/Video	EFFICIENT SCAN PATTERN GENERATION FOR DIGITAL CIRCUITS EP	04-Audio/Video	4100141.3	19-jan-04	EP	4100141.3	19-jan-04
NL040061	19-jan-04	04-Audio/Video	MODULAR SOC TESTING	04-Audio/Video	4100147	19-jan-04	EP	4100147	19-jan-04
FR040008	16-jan-04	05-Mobile Communication	HP-RECOLORING FILTER FOR BALANCING LP FILTERING	05-Mobile Communication	4290121.5	16-jan-04	EP	4290121.5	16-jan-04
FR040006	15-jan-04	05-Connectivity	MATCHED-HIGHLOW SIDE SWITCHES WITH GOOD EMC PERFORM EP	05-Connectivity	4100111.6	15-jan-04	EP	4100111.6	15-jan-04
DE040007	13-jan-04	02-Packaging & Testing	ACTIVE PULL-UP FOR LOW POWER USB OTG DEVICES	02-Packaging & Testing	4300016.5	13-jan-04	EP	4300016.5	13-jan-04
GB040016	13-jan-04	04-Audio/Video	JTAG TEST ARCHITECTURE FOR MULTI-CHIP PACK	04-Audio/Video	4300088.4	13-jan-04	EP	4300088.4	13-jan-04
GB040016	13-jan-04	04-Audio/Video	POWER REDUCTION IN VLIW PROCESSORS	04-Audio/Video	400659.9	13-jan-04	EP	400659.9	13-jan-04
GB040016	13-jan-04	04-Audio/Video	FAST FORWARD-BACKGROUND MOTION DESCRIPTION	04-Audio/Video	400660.7	13-jan-04	EP	400660.7	13-jan-04
DE040006	13-jan-04	05-Mobile Communication	DIGITAL ALPHA SIGNAL PROCESSING	05-Mobile Communication	400662.3	13-jan-04	EP	400662.3	13-jan-04
GB040016	13-jan-04	05-Mobile Communication	SYNCHRONIZATION OF TIME BASE UNITS	05-Mobile Communication	4100086.6	13-jan-04	EP	4100086.6	13-jan-04
GB040016	13-jan-04	05-Mobile Communication	CRYPTO KEY BACK-UP SYSTEM	05-Mobile Communication	4100072	13-jan-04	EP	4100072	13-jan-04
DE040013	12-jan-04	04-Audio/Video	PULL CIRCUIT	04-Audio/Video	4100052.2	12-jan-04	EP	4100052.2	12-jan-04
US040015	9-jan-04	04-Audio/Video	REAL TIME 3:2 PULLDOWN DETECTION MONITORING DURING	04-Audio/Video	60535722	9-jan-04	US	60535722	9-jan-04
DE040015	8-jan-04	04-Audio/Video	DISTRIBUTION CANDIDATE MOTION VECTORS BASED ON LOCAL... US	04-Audio/Video	4100032.4	8-jan-04	EP	4100032.4	8-jan-04
DE040015	8-jan-04	09-Discretes and Multimat	METHOD TO MEASURE THE SPEED OF THE TARGET WHEEL	09-Discretes and Multimat	4100020.9	8-jan-04	EP	4100020.9	8-jan-04
DE040015	7-jan-04	09-Discretes and Multimat	AMR SENSOR ELEMENT FOR ANGULAR DETECTION	09-Discretes and Multimat	4100021.7	7-jan-04	EP	4100021.7	7-jan-04
FR040005	7-jan-04	09-Discretes and Multimat	DIVISION-FREE AFFINE TRANSFORMATION FOR TRIANGLE RENDE EP	09-Discretes and Multimat	4100011.8	7-jan-04	EP	4100011.8	7-jan-04
DE040005	6-jan-04	05-Mobile Communication	ELECTRONIC OPERATING W. QUANTUM DOT INSULATION LAYER EP	05-Mobile Communication	4100006.8	6-jan-04	EP	4100006.8	6-jan-04
DE040003	5-jan-04	02-Packaging & Testing	DELAY FAULT PULSE GENERATOR	02-Packaging & Testing	330076.1	5-jan-04	EP	330076.1	5-jan-04
GB030231	27-dec-03	02-Packaging & Testing	ROLLING AVERAGE DC OFFSET COMPENSATION	02-Packaging & Testing	3300291.6	27-dec-03	EP	3300291.6	27-dec-03
NL041251	24-dec-03	01-CMOS and embedded pr	A MAGNETIC BUILT-IN CURRENT SENSOR FOR I2C	01-CMOS and embedded pr	4103905.8	24-dec-03	EP	4103905.8	24-dec-03
NL031814	23-dec-03	04-Audio/Video	REDUCTION OF SHEAR ALIASING USING 1D FILTERING	04-Audio/Video	3300285.8	23-dec-03	EP	3300285.8	23-dec-03
FR030160	23-dec-03	05-Mobile Communication	UMTS RECEIVER SYMBOL SYNCHRONIZATION	05-Mobile Communication	3300284.1	23-dec-03	EP	3300284.1	23-dec-03
FR030161	23-dec-03	05-Mobile Communication	BLIND AICH DECODER FOR UMTS	05-Mobile Communication	3300286.6	23-dec-03	EP	3300286.6	23-dec-03
FR030162	23-dec-03	05-Mobile Communication	UMTS RECEIVER SYMBOL SYNCHRONIZATION	05-Mobile Communication	3300294.1	23-dec-03	EP	3300294.1	23-dec-03
FR030163	23-dec-03	05-Mobile Communication	SOLID PHASE EPITAXIAL REGROWTH WITH REDUCED DEPTH OD EP	05-Mobile Communication	3300282.5	23-dec-03	EP	3300282.5	23-dec-03
FR030164	23-dec-03	03-RF Devices	UNIVERSAL DEROTATOR FOR UMTS MODES	03-RF Devices	3104892.9	23-dec-03	EP	3104892.9	23-dec-03
FR030165	23-dec-03	03-RF Devices	IMPROVED DOUBLE CLAMPED PIEZO MEMS	03-RF Devices	3104694.5	23-dec-03	EP	3104694.5	23-dec-03
FR030166	23-dec-03	05-Mobile Communication	NETWORK ADAPTATION WITH SLICING	05-Mobile Communication	3300278.3	23-dec-03	EP	3300278.3	23-dec-03
FR030167	23-dec-03	07-Identification	UMTS RECEIVER SYMBOL SYNCHRONIZATION	07-Identification	3300281.7	23-dec-03	EP	3300281.7	23-dec-03
FR030168	23-dec-03	06-Connectivity	METHODS AND APPARATUS FOR TRANSMIT LATENCY REDUCTI US	06-Connectivity	60531408	23-dec-03	US	60531408	23-dec-03
US0302516	19-dec-03	01-CMOS and embedded pr	TRANSIENT COMPENSATION OF PLLS IN WIRELESS SYSTEMS	01-CMOS and embedded pr	3104837.4	19-dec-03	EP	3104837.4	19-dec-03
US0302517	19-dec-03	05-Connectivity	CLOCK DISTRIBUTION	05-Connectivity	3104949.9	19-dec-03	EP	3104949.9	19-dec-03
FR030154	18-dec-03	01-CMOS and embedded pr	SYNCHRONIZATION DURING ANTI-COLLISION	01-CMOS and embedded pr	3104781.4	18-dec-03	EP	3104781.4	18-dec-03
FR030155	18-dec-03	01-CMOS and embedded pr	TEMPERATURE-BASED DOMAIN-SPECIFIC RECONFIGURABLE LOGIC EP	01-CMOS and embedded pr	3104776.4	18-dec-03	EP	3104776.4	18-dec-03
FR030156	18-dec-03	01-CMOS and embedded pr	SOLID PHASE EPITAXIAL REGROWTH TO PRODUCE SHALLOW JUMP EP	01-CMOS and embedded pr	3104776.4	18-dec-03	EP	3104776.4	18-dec-03
FR030157	18-dec-03	01-CMOS and embedded pr	POCKET PROFILING AT LOW TEMPERATURES	01-CMOS and embedded pr	3104776.4	18-dec-03	EP	3104776.4	18-dec-03

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IPFLA Annex 1 Transfer Patents\_Final\_020808.xls

IPC Class	Inventor	Title	Inventor	Date	IPC Class	Inventor	Title	Inventor	Date	IPC Class	Inventor	Title	Inventor	Date
H01L 03/00	IMEC	SOLID PHASE EPITAXIAL REGROWTH FOR REDUCES LEAKAGE CL EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SOLID PHASE EPITAXIAL REGROWTH FOR REDUCES LEAKAGE CL EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SOLID PHASE EPITAXIAL REGROWTH FOR REDUCES LEAKAGE CL EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	SERIAL COMMUNICATION DEVICE CONFIGURABLE TO OPERATE ... US	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SERIAL COMMUNICATION DEVICE CONFIGURABLE TO OPERATE ... US	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SERIAL COMMUNICATION DEVICE CONFIGURABLE TO OPERATE ... US	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	METHOD OF FORMING STRAINED SI CHANNEL IN MOSFET STRUCT EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD OF FORMING STRAINED SI CHANNEL IN MOSFET STRUCT EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD OF FORMING STRAINED SI CHANNEL IN MOSFET STRUCT EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MEMORY EFFICIENT INSTRUCTION PROCESSING SCHEME	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MEMORY EFFICIENT INSTRUCTION PROCESSING SCHEME	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MEMORY EFFICIENT INSTRUCTION PROCESSING SCHEME	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DIGITAL FULL SYSTEM FOR TV APPLICATIONS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIGITAL FULL SYSTEM FOR TV APPLICATIONS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIGITAL FULL SYSTEM FOR TV APPLICATIONS	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	APPLICATOR FOR SAFETY RELATED APPLICATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	APPLICATOR FOR SAFETY RELATED APPLICATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	APPLICATOR FOR SAFETY RELATED APPLICATION	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	METHOD TO REDUCE SEED LAYER TOPOGRAPHY IN BICMOS PROC US	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD TO REDUCE SEED LAYER TOPOGRAPHY IN BICMOS PROC US	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD TO REDUCE SEED LAYER TOPOGRAPHY IN BICMOS PROC US	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	INDEPENDENT FLOOR CONTROL FOR VOICED POC	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	INDEPENDENT FLOOR CONTROL FOR VOICED POC	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	INDEPENDENT FLOOR CONTROL FOR VOICED POC	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	WIRED BONDED SEMICONDUCTOR DEVICE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	WIRED BONDED SEMICONDUCTOR DEVICE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	WIRED BONDED SEMICONDUCTOR DEVICE	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MULTI-VIEW RENDERING USING FORWARD MAPPING	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MULTI-VIEW RENDERING USING FORWARD MAPPING	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MULTI-VIEW RENDERING USING FORWARD MAPPING	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	STATION COMPRISING A RANGE RECEIVER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	STATION COMPRISING A RANGE RECEIVER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	STATION COMPRISING A RANGE RECEIVER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MAGNETIC FIELD SENSOR FOR MEASUREMENT OF OXYGEN	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MAGNETIC FIELD SENSOR FOR MEASUREMENT OF OXYGEN	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MAGNETIC FIELD SENSOR FOR MEASUREMENT OF OXYGEN	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	GROUND SIGNAL - GROUND (GSG) TEST STRUCTURE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	GROUND SIGNAL - GROUND (GSG) TEST STRUCTURE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	GROUND SIGNAL - GROUND (GSG) TEST STRUCTURE	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	SUBCHANNEL P REGION WITH FIELD PLATE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SUBCHANNEL P REGION WITH FIELD PLATE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SUBCHANNEL P REGION WITH FIELD PLATE	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	FIELD PLATE WITH HIGH DRIFT REGION DOPING GRADIENT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FIELD PLATE WITH HIGH DRIFT REGION DOPING GRADIENT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FIELD PLATE WITH HIGH DRIFT REGION DOPING GRADIENT	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DEEP TRENCH DIELECTRIC WITH GRADED GRADIENT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DEEP TRENCH DIELECTRIC WITH GRADED GRADIENT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DEEP TRENCH DIELECTRIC WITH GRADED GRADIENT	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	METHOD AND APPARATUS FOR SUPPORTING JOINT DETECTION IN CM	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD AND APPARATUS FOR SUPPORTING JOINT DETECTION IN CM	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD AND APPARATUS FOR SUPPORTING JOINT DETECTION IN CM	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	HANDOVER SCHEME FOR PEER-TO-PEER ENABLE WIRELESS SYNCH	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	HANDOVER SCHEME FOR PEER-TO-PEER ENABLE WIRELESS SYNCH	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	HANDOVER SCHEME FOR PEER-TO-PEER ENABLE WIRELESS SYNCH	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	ACTIVE FLOOR COMPENSATION DURING MRAM WRITE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ACTIVE FLOOR COMPENSATION DURING MRAM WRITE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ACTIVE FLOOR COMPENSATION DURING MRAM WRITE	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DATA RETENTION INDICATOR FOR MRAM	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DATA RETENTION INDICATOR FOR MRAM	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DATA RETENTION INDICATOR FOR MRAM	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	WRITE DISABLE OPTION FOR MRAM OPERATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	WRITE DISABLE OPTION FOR MRAM OPERATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	WRITE DISABLE OPTION FOR MRAM OPERATION	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	NON-HOMOGENEOUS SHIELDING OF AN MRAM CHIP IN THE MARK EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	NON-HOMOGENEOUS SHIELDING OF AN MRAM CHIP IN THE MARK EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	NON-HOMOGENEOUS SHIELDING OF AN MRAM CHIP IN THE MARK EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	CONTINUOUS OFFSET CALIBRATION FOR TFT APPLICATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CONTINUOUS OFFSET CALIBRATION FOR TFT APPLICATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CONTINUOUS OFFSET CALIBRATION FOR TFT APPLICATION	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	VOICE ENHANCEMENT BY BOOSTING UNVOICED, LEAVING VOICED EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	VOICE ENHANCEMENT BY BOOSTING UNVOICED, LEAVING VOICED EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	VOICE ENHANCEMENT BY BOOSTING UNVOICED, LEAVING VOICED EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	SEMICONDUCTOR DEVICE WITH A RESONATOR	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SEMICONDUCTOR DEVICE WITH A RESONATOR	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SEMICONDUCTOR DEVICE WITH A RESONATOR	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	CONTROLLING POWER CONSUMPTION PEAKS IN ELECTRONIC CIR EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CONTROLLING POWER CONSUMPTION PEAKS IN ELECTRONIC CIR EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CONTROLLING POWER CONSUMPTION PEAKS IN ELECTRONIC CIR EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	RFID CHIP IN AN ELECTRO-ACOUSTIC TRANSDUCER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	RFID CHIP IN AN ELECTRO-ACOUSTIC TRANSDUCER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	RFID CHIP IN AN ELECTRO-ACOUSTIC TRANSDUCER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	IEEE 802.11E SCHEDULE BIT INTERPRETATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	IEEE 802.11E SCHEDULE BIT INTERPRETATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	IEEE 802.11E SCHEDULE BIT INTERPRETATION	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	GENERAL PURPOSE GPS RF FRONT END FOR SOFTWARE GPS EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	GENERAL PURPOSE GPS RF FRONT END FOR SOFTWARE GPS EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	GENERAL PURPOSE GPS RF FRONT END FOR SOFTWARE GPS EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	ELECTRONIC DATA OR DOCUMENT CARRIER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ELECTRONIC DATA OR DOCUMENT CARRIER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ELECTRONIC DATA OR DOCUMENT CARRIER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	PLANNER DMMOS WITH TRENCHED SHIELD PLATE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	PLANNER DMMOS WITH TRENCHED SHIELD PLATE	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	PLANNER DMMOS WITH TRENCHED SHIELD PLATE	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	STABILISATION D'IMAGES VIDEO PAR ANALYSE D'HISTOGRAMMES FR	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	STABILISATION D'IMAGES VIDEO PAR ANALYSE D'HISTOGRAMMES FR	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	STABILISATION D'IMAGES VIDEO PAR ANALYSE D'HISTOGRAMMES FR	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	FORWARD CONVERTER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FORWARD CONVERTER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FORWARD CONVERTER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	FAST FREQUENCY OFFSET CORRECTION FOR BURSTED	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FAST FREQUENCY OFFSET CORRECTION FOR BURSTED	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FAST FREQUENCY OFFSET CORRECTION FOR BURSTED	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MEMS WITH ALMG ALLOY	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MEMS WITH ALMG ALLOY	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MEMS WITH ALMG ALLOY	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DRY ETCHING PROCESS FOR MEMS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DRY ETCHING PROCESS FOR MEMS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DRY ETCHING PROCESS FOR MEMS	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MULTIPLE BITS USED AS STATE STORAGE IN OPT PROCESS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MULTIPLE BITS USED AS STATE STORAGE IN OPT PROCESS	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MULTIPLE BITS USED AS STATE STORAGE IN OPT PROCESS	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	RE-USE OF ALREADY EXISTING LAYER AS SPIRIT LAYER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	RE-USE OF ALREADY EXISTING LAYER AS SPIRIT LAYER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	RE-USE OF ALREADY EXISTING LAYER AS SPIRIT LAYER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	ANSWER MODE SWITCHING BY MEANS OF FOLLOW-UP TIME	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ANSWER MODE SWITCHING BY MEANS OF FOLLOW-UP TIME	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ANSWER MODE SWITCHING BY MEANS OF FOLLOW-UP TIME	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	POWER OPTIMIZED COLLOCATED MOTION ESTIMATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	POWER OPTIMIZED COLLOCATED MOTION ESTIMATION	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	POWER OPTIMIZED COLLOCATED MOTION ESTIMATION	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DIVIDER BY 2	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIVIDER BY 2	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIVIDER BY 2	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	SWITCH	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SWITCH	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	SWITCH	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DUAL RESIDUE PIPE LINE ADC	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DUAL RESIDUE PIPE LINE ADC	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DUAL RESIDUE PIPE LINE ADC	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	ON-CHIP RESILV SENSE POINT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ON-CHIP RESILV SENSE POINT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	ON-CHIP RESILV SENSE POINT	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	METHOD AND APPARATUS FOR LOW EMI HIGH SPEED MULTI WIRE EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD AND APPARATUS FOR LOW EMI HIGH SPEED MULTI WIRE EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD AND APPARATUS FOR LOW EMI HIGH SPEED MULTI WIRE EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DIGITAL RIGHTS MANAGEMENT UNIT FOR A DIGITAL MANAGEMEN EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIGITAL RIGHTS MANAGEMENT UNIT FOR A DIGITAL MANAGEMEN EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DIGITAL RIGHTS MANAGEMENT UNIT FOR A DIGITAL MANAGEMEN EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	TOO STRONG ANALOG PREFILTER COMPENSATED BY DIGITAL POC EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	TOO STRONG ANALOG PREFILTER COMPENSATED BY DIGITAL POC EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	TOO STRONG ANALOG PREFILTER COMPENSATED BY DIGITAL POC EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	VOLTAGE CONVERTER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	VOLTAGE CONVERTER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	VOLTAGE CONVERTER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	MIMO TRANSMITTER AND RECEIVER FOR LOW SCATTERING ENVI EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MIMO TRANSMITTER AND RECEIVER FOR LOW SCATTERING ENVI EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	MIMO TRANSMITTER AND RECEIVER FOR LOW SCATTERING ENVI EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	CHARGE PUMP CURRENT LIMITER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CHARGE PUMP CURRENT LIMITER	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	CHARGE PUMP CURRENT LIMITER	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	METHOD FOR COMPARING SWING CURVE AMPLITUDES	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD FOR COMPARING SWING CURVE AMPLITUDES	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	METHOD FOR COMPARING SWING CURVE AMPLITUDES	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	AVOIDANCE OF SILICIDE TO CHANNEL SHORT CIRCUIT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	AVOIDANCE OF SILICIDE TO CHANNEL SHORT CIRCUIT	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	AVOIDANCE OF SILICIDE TO CHANNEL SHORT CIRCUIT	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	POCKET MANUFACTURING FOR ADVANCED ULTRA-SHALLOW JUMP EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	POCKET MANUFACTURING FOR ADVANCED ULTRA-SHALLOW JUMP EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	POCKET MANUFACTURING FOR ADVANCED ULTRA-SHALLOW JUMP EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	FORMATION OF DEVICE WITH SILICON-OXYNITRIDE DIELECTRIC L EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FORMATION OF DEVICE WITH SILICON-OXYNITRIDE DIELECTRIC L EP	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	FORMATION OF DEVICE WITH SILICON-OXYNITRIDE DIELECTRIC L EP	IMEC	2005/05/26/26/26
H01L 03/00	IMEC	DUAL WORK FUNCTION METAL GATE INTEGRATION SCHEME	IMEC	2005/05/26/26/26	H01L 03/00	IMEC	DUAL WORK FUNCTION METAL GATE INTEGRATION SCHEME	IMEC	2005/05/26					

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Pub No.	Class	Inventor	App No.	Class	Description	Pub No.
NL031221	15-04-03	RACHID EL WAFFAQUI@PHILIPS.COM	3103907.8	EP	ELECTRONIC CIRCUIT FOR BIPOLAR SIGNALS	2002080494A1
NL031227	14-04-03	KEES SCHRETTERS@PHILIPS.COM	3103387.2	EP	LOW COST INPUT CIRCUIT FOR SIMPS	2004060725A1
FR030120	13-03-03	STYLVAIN DUVILLARD@PHILIPS.COM	3292336.8	EP	LIMITS DIVIDER	2002005975A1
FR030121	13-03-03	SEBASTIEN PROUET@PHILIPS.COM	3292341.4	EP	UPCONVERTER MIXER	2002008736A1
NL031198	13-02-03	MERMAN LEFFING@PHILIPS.COM	3103768.2	EP	BOOST CONVERTER	2003080203A1
NL031193	10-02-03	HARM COENEN@PHILIPS.COM	3103758.3	EP	2-LAYER BGA WITH HIGH SI / EMC PERFORMANCE	2002008663A1
NL031228	10-01-03	MART COENEN@PHILIPS.COM	3103757.5	EP	FLIP CHIP PACKAGE WITH LOWER GROUND BOUNCE VOLTAGE	2002008664A2
NL031188	10-04-03	MART COENEN@PHILIPS.COM	3103751.9	EP	POWER SAVING IN A TRANSMITTER	2002008665A1
NL031187	6-04-03	FRANS SCHOOFS@PHILIPS.COM	3103692.9	EP	FIELD SHAPING FOR MRAM	2002008666A1
DE030341	6-04-03	HANS PETER LOEBL@PHILIPS.COM	3103694.5	EP	RESONATOR STRUCTURE AND METHOD OF PRODUCING IT	2002008667A1
DE030342	6-04-03	BOB MILSON@PHILIPS.COM	3103696.7	EP	THIN-FILM BULK ACOUSTIC WAVE LADDER FILTER WITH FAN-SHAPED INTEGRATION OF TFM AND SA INTO A SINGLE CHIP	2002008668A1
AT030055	6-04-03	ERNEST HASELSTEINER@PHILIPS.COM	3103696.8	EP	IMPROVED MPES DECODE RELATING TO FRAME BUFFER	2002008669A1
GB030172	4-04-03	DAVID PENNA@PHILIPS.COM	3233294	GB	IMPROVED MPES DECODE RELATING TO DCT	2002008670A2
GB030173	2-04-03	RICHARD MILLER SMITH@PHILIPS.COM	3230038	GB	START-UP VIDEO ON MOBILE DEVICES	2002008671A1
FR030116	2-04-03	LAURENT TRICAUD@PHILIPS.COM	3300141.3	EP	ELECTRICAL SHIELDING IN STACKED DIES BY USING CONDUCTIVE DUAL MODULUS PRESOLDER	2002008672A1
US030390	1-04-03	HENK THOONEN@PHILIPS.COM	6050782A	US	DIRECT SPREAD MULTI-CARRIER QOFDM LDMB TRANSMISSION SYS	2002008673A1
US030375	1-04-03	DOMINIE LEENERTS@PHILIPS.COM	6050775.3	EP	COMPACT 2 TRANSISTOR MEMORY CELL	2002008674A1
US030395	1-04-03	CHARLES RAZZELL@PHILIPS.COM	6050772.2	EP	FEC IN PACKET HEADER WITH PARTIAL CRC PROTECTION	2002008675A1
FR030117	30-09-03	PHILIPPE GENTRIK@PHILIPS.COM	6050769.9	EP	1 3.2 GBPS, 10MPY, 18SPS SKEW CMOS LVDS TRANSMITTER	2002008676A1
US030368	30-09-03	BRAO DAVIS.21180@FF.UNKONOWN.ORG	6050748.8	US	HYBRID BIPOLAR-MOS TRENCH SEMICONDUCTOR DEVICE...	2002008677A1
US030376	30-09-03	BRAD DAVIS.21180@FF.UNKONOWN.ORG	6050747.1	US	LATERAL THIN-FILM SOI DEVICE HAVING A FIELD PLATE...	2002008678A1
US030377	30-09-03	BRAD DAVIS.21180@FF.UNKONOWN.ORG	6050740	US	HYBRID BIPOLAR-MOS TRENCH SEMICONDUCTOR DEVICE...	2002008679A1
NL031196	28-09-03	TED LETAVIC@PHILIPS.COM	6050737.5	EP	A METHOD & APPARATUS FOR TESTING AN IC	2002008680A1
DE030336	26-09-03	HARALD VRANKEN@PHILIPS.COM	10344847	DE	METHOD FOR COMPILING A SOURCE-CODE PROGRAM	2002008681A1
NL031141	25-09-03	JOERG FISCHER@PHILIPS.COM	3103575.1	EP	FRAME-RATE UP CONVERSION USING MOTION BLUR	2002008682A1
NL031162	25-09-03	KOEN MEINDS@PHILIPS.COM	3103568.7	EP	ACTIVE CURRENT SENSING AND APPLICATION OF SUCH IN A VRM	2002008683A1
NL031128	23-09-03	FRANS SCHOOFS@PHILIPS.COM	3103545.6	EP	ADAPTIVE FILTER	2002008684A1
NL031129	23-09-03	PIERLUIGI D'ALESSANDRO@PHILIPS.COM	3103519.9	EP	ROBUST INITIAL SYNCHRONIZATION FOR RECEIVERS	2002008685A1
US030356	23-09-03	STEFAN MUELLER-WEINFORTE@PHILIPS.COM	6050565.8	EP	MASTER/SLAVE DIRECT MEMORY ACCESS HARDWARE & SOFTWARE	2002008686A1
FR030106	23-09-03	GUIDO FREDERIX@PHILIPS.COM	6050560	EP	STACKED LNA-MIXER	2002008687A1
FR030104	22-09-03	HENRI MARIE@PHILIPS.COM	3300129.8	EP	LOW SUPPLY VOLTAGE AND HIGH SPEED LOGIC CIRCUITS	2002008688A1
US030344	22-09-03	LIONEL GUIRAUD@PHILIPS.COM	6050483	EP	METHOD TO RESOLVE TSF TIMER AMBIGUITY OF IEEE...	2002008689A1
US030345	19-09-03	AMJAD SOOMRO@PHILIPS.COM	6050480.8	US	DYNAMIC CONTROL OF CAPACITANCE ELEMENTS IN FIELD...	2002008690A1
US030325	19-09-03	JOHN PETRUZZELLO@PHILIPS.COM	6050439	US	METAL FUSE WIRE REDUCED HEAT DISSIPATION TOWARDS THE SUB	2002008691A2
US030320	19-09-03	PIEBE ZILISTRA@PHILIPS.COM	6050467.3	US	FUSE STRUCTURE FOR MAINTAINING PASSIVATION INTEGRITY	2002008692A1
US030336	19-09-03	PIEBE ZILISTRA@PHILIPS.COM	6050445.2	US	METHOD OF FORMING DIELECTRIC LAYERS WITH LOW...	2002008693A1
FR030103	19-09-03	JEAN-MARC YANNI@PHILIPS.COM	3300123.6	EP	TEST OF A SECRET MODULE	2002008694A1
CN030003	19-09-03	RAY FENG@PHILIPS.COM	3124300.4	CN	METHOD AND SYSTEM FOR HANDOVER BETWEEN WIRELESS LOC	2002008695A1
NL031110	19-09-03	KEYI WU@PHILIPS.COM	3103471.3	EP	DIGITAL PEAK DETECTOR FOR A MAGNETORESISTIVE SENSOR IC	2002008696A1
NL031139	19-09-03	JACCO VAN DE VELDEN@PHILIPS.COM	3103461.2	EP	METHODS TO ENHANCE WLAN SECURITY	2002008697A2
NL031097	19-09-03	RENE VAN DIEN BERG@PHILIPS.COM	3103470.2	EP	MULTI DSP ARCHITECTURE LOCAL-READ/REMOTE-WRITE	2002008698A1
NL031109	19-09-03	BOB VAN SCHAIJK@PHILIPS.COM	3103397	EP	POWER SAVING OPERATION OF AN APPARATUS WITH A CACHE	2002008699A1
NL031096	19-09-03	GEERT VANDERHEIJDEN@PHILIPS.COM	3103389.6	EP	ELECTRONIC CIRCUIT WITH A CHAIN OF PROCESSING ELEMENTS	2002008700A1
NL031140	19-09-03	AD PEETERS@PHILIPS.COM	3103383	EP	METHOD AND APPARATUS FOR CURRENT-MODE MULTI-BIT DACS	2002008701A1
DE030324	17-09-03	LUCIEN BREEMS@PHILIPS.COM	3103357.4	EP	ELECTROMECHANICAL TRANSDUCER WITH TEMPERATURE STABLE	2002008702A1
NL030229	10-09-03	STEFFEN DREWS@PHILIPS.COM	3103917.5	EP	VARIABLE IMPEDANCE CIRCUIT USING UNARY CELLS	2002008703A1
NL030155	8-09-03	J.T.M.VAN BEEK@PHILIPS.COM	3209930.9	EP	COHERENT DATA ACQUISITION OVER FRAGMENTED CAPTURE	2002008704A1
NL031072	5-09-03	HENK VISSER@PHILIPS.COM	3209935.2	EP	UNIVERSAL POWER CONTROLLER	2002008705A1
NL031078	3-09-03	IVMO MERGLER@PHILIPS.COM	3103279	EP	FIELD IMPROVEMENT THROUGH CACHE MODULE REMAPPING	2002008706A1
DE030313	3-09-03	ARJAN BINK@PHILIPS.COM	3103273.9	EP	FORMATION OF EXTENSIONS IN DOUBLE-GATE DEVICES	2002008707A1
NL031087	2-09-03	PAUL WIELAGE@PHILIPS.COM	3102688.3	EP	7 TRANSISTOR LATCH	2002008708A1
CN030021	2-09-03	THOMAS DUERBAUM@PHILIPS.COM	6049895.5	EP	FAILURE PREDICTION FOR PARALLEL MOSFETS	2002008709A1
US030064	1-09-03	KARL MICHAEL LENSEN@PHILIPS.COM	3301015.8	CN	A POWER CONTROL MECHANISM IN WLAN	2002008710A1
NL031020	29-08-03	KEYI WU@PHILIPS.COM	3103252.7	EP	INTEGRATED CIRCUIT WITH SIGNATURE COMPUTATION	2002008711A1
US030303	29-08-03	STEPHANE BRIERE@PHILIPS.COM	10398832.5	DE	METHOD OF DETECTING IEEE 802.11A SIGNALS	2002008712A1
US030300	29-08-03	MURALI SANDULEANU@PHILIPS.COM	6049883.8	US	SYSTEM AND METHOD FOR WIRELESS COMMUNICATION	2002008713A1
US030282	28-08-03	VOLKER AUER@PHILIPS.COM	6049885.5	US	POWER DELIVERY SYSTEM HAVING CASCADED BUCK STAGES	2002008714A1
US030282	28-08-03	ZHANG CHUNRUI@PHILIPS.COM		US	METHOD & SYSTEM FOR PASSEBAND RIPPLE CANCELLATION IN CAS	2002008715A1

Patent No.	Inventor	Class	Abstract	Date	Pub No.	Pub Date	Pub No.	Pub Date	Pub No.	Pub Date
US0302284	DLAE HIRSCH@PHILIPS.COM	06-Connectivity	SYSTEM & METHOD FOR ENERGY EFFICIENT SIGNAL DETECTION	26-aug-03	60499456	31023312	60499456	31023312	60499456	31023312
US0302287	GREG GOODHUE@PHILIPS.COM	09-Discretize and Multimize	IN A MICROCONTROLLER WHERE AN INSTRUCTION IS LOGICALLY	26-aug-03	60499301	31023317	60499301	31023317	60499301	31023317
US0302308	BERND OFFERMANN@PHILIPS.COM	02-Packaging & Testing	FLATPACK DEVICE WITH RIPPED LEADS	27-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0302346	STEVEN THEUWEN@PHILIPS.COM	03-RF Devices	LDMOS WITH STEPPED SHIELD AND GND	27-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0302811	PAUL HUMMER@PHILIPS.COM	07-Identification	IDENTIFICATION OF A CONTACTLESS CARD IN AN ISO 7816 ENVIRONMENT	27-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0302817	MARTIN WAGNER_2@PHILIPS.COM	08-Automotive	CIRCUIT FOR SUPPORT OF A MICROCONTROLLER	26-aug-03	60497996	31023317	60497996	31023317	60497996	31023317
US0302863	PEN LI@PHILIPS.COM	06-Connectivity	WIRELESS DEVICE WITH DYNAMIC FRAGMENTATION THRESHOLD	26-aug-03	60497725	31023317	60497725	31023317	60497725	31023317
US0302880	PEN LI@PHILIPS.COM	10-Other	DATA SEGREGATION AND FRAGMENTATION IN A WIRELESS NETWORK	25-aug-03	33000934	31023317	33000934	31023317	33000934	31023317
US0302888	LONNIE GOFF_21237@PF.PHILIPS.COM	04-Audio/Video	DYNAMIC RETENTION OF HARDWARE REGISTER CONTENT IN A CPU	22-aug-03	33000934	31023317	33000934	31023317	33000934	31023317
US0302895	DAVID LEGOFF@PHILIPS.COM	04-Audio/Video	BACKWARD COMPATIBLE MULTI-CARRIER TRANSMISSION SYSTEM	16-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0302923	LONNIE GOFF_21237@PF.PHILIPS.COM	04-Audio/Video	HARDWARE REGISTER ACCESS VIA TASK TAG ID	16-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0303132	THOMAS DUERBAUM@PHILIPS.COM	09-Discretize and Multimize	DC-DC CONVERTER DEVICE AND METHOD	15-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0303276	KEVIN BOYLE@PHILIPS.COM	03-RF Devices	PIFA OPTIMISED FOR HUMAN INTERACTION	15-aug-03	3182119	31023317	3182119	31023317	3182119	31023317
US0303379	O.P. GANGWAL@PHILIPS.COM	04-Audio/Video	LOCAL CUSTOMISED (DATA DEPENDENT) SIMD PROCESSING	15-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0304027	02-Packaging & Testing	02-Packaging & Testing	CALIBRATION OF TESTER AND TESTBOARD BY GOLDEN SAMPLE	14-aug-03	60496153	31023317	60496153	31023317	60496153	31023317
US0305377	HENK VISSER@PHILIPS.COM	01-CMOS and embedded IC	IMPROVED ERASE AND READ SCHEME FOR CHARGE TRAPPING MEMORY	13-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0305987	05-Mobile Communication	05-Mobile Communication	MP ADAPTATION BY RESPECTIVE FLAGS	12-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0306275	STEPHANE VALETTE@PHILIPS.COM	09-Discretize and Multimize	OPERATION AND CIRCUITRY OF A POWER CONVERTER AND CONTROL	12-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0306276	THOMAS DUERBAUM@PHILIPS.COM	09-Discretize and Multimize	AUTO ALIGNMENT OF MULTIPLE SERIAL BYTE LINES	12-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0306277	DAVE EVOY@PHILIPS.COM	06-Connectivity	METHOD FOR MEASURING OPTIMAL FILM THICKNESS	12-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0306286	DAVID ZIGER@PHILIPS.COM	01-CMOS and embedded IC	SWITCHABLE ADC FOR COMPUTATIONALLY EFFICIENT GPS PROCESSING	8-aug-03	604934234	31023317	604934234	31023317	604934234	31023317
US0307131	SAUL DOOLEY@PHILIPS.COM	05-Mobile Communication	BLOCK RESTRICTED MATCHING PURSUIT	6-aug-03	60493099	31023317	60493099	31023317	60493099	31023317
US0308025	STEPHANE VALETTE@PHILIPS.COM	05-Mobile Communication	MODULE WITH PAIR-WISE DIFFERENT CONNECTION PLATES	5-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0308042	PAUL HUMMER@PHILIPS.COM	07-Identification	ADC BOOTSTRAP CIRCUIT	5-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0308935	01-CMOS and embedded IC	01-CMOS and embedded IC	PREVENTION OF SILICIDATION BRIDGING	4-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0308936	GOVEERY GEELING@PHILIPS.COM	05-Discretize and Multimize	MEASURING THE EFFECT OF FLARE ON LINE WIDTH	4-aug-03	60492130	31023317	60492130	31023317	60492130	31023317
US0309227	FRANK NEULLY@PHILIPS.COM	01-CMOS and embedded IC	BSS SWITCH MODULE FOR WIRELESS LAN DEVICES	1-aug-03	60492199	31023317	60492199	31023317	60492199	31023317
US0309244	08-Connectivity	08-Connectivity	GATE MODULATION METHOD OF A SYNCHRONOUS RECTIFIER	1-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309245	REINHOLD ELFERICH@PHILIPS.COM	06-Connectivity	METHOD OF MULTICHANNEL AND CONVERSION	1-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309259	09-Discretize and Multimize	09-Discretize and Multimize	GROUND ARCH FOR WIREBOND BALL GRID ARRAYS	1-aug-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309266	JENS BRETSCHNEIDER@PHILIPS.COM	09-Discretize and Multimize	RESAMPLING STRATEGY DEPENDS ON SIZE OF PRIMITIVE	31-Jul-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309267	CHRIST WYLAND@PHILIPS.COM	02-Packaging & Testing	DYN. SEL. OF COMM. SERVICES IN RESOURCE CONSTRAINED ENVIRONMENT	30-Jul-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309268	ARTUR BURKHARD@PHILIPS.COM	04-Audio/Video	MEMORY ORGANIZATION ALLOWING SINGLE CYCLE POINTER ADDRESS	30-Jul-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309269	05-Discretize and Multimize	05-Discretize and Multimize	MICROCONTROLLER WITH AN INTERRUPT STRUCTURE HAVING PRIORITY	30-Jul-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309270	PETER SCHOLTENS@PHILIPS.COM	09-Discretize and Multimize	OPTIMISING MEMORY ALLOC. IN CONSTRAINED ENVIRONMENT	30-Jul-03	31023317	31023317	31023317	31023317	31023317	31023317
US0309271	GERARD HUBERT_058509@PF.UNKNOW.N	10-Other	PIPELINED LONG-INTEGRER MULTIPLIER	28-Jul-03	317570	31023317	317570	31023317	317570	31023317
US0309272	04-Audio/Video	04-Audio/Video	PIFA WITH DISCRETE DUAL-BANDING	26-Jul-03	317505.1	31023317	317505.1	31023317	317505.1	31023317
US0309273	03-RF Devices	03-RF Devices	MULTIPLE APPLICATIONS IN ONE MAP XLET	24-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309274	01-CMOS and embedded IC	01-CMOS and embedded IC	SHIELDED INDUCTOR IN LOW-K BACK END	24-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309275	08-RF Devices	08-RF Devices	COMPACT IMPEDANCE TRANSFORMATION CIRCUIT	23-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309276	03-RF Devices	03-RF Devices	DEVICE AND METHOD FOR COMPOSING CODES	22-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309277	04-Audio/Video	04-Audio/Video	ANTENNA SWITCH WITH ADAPTIVE FILTER	22-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309278	05-Mobile Communication	05-Mobile Communication	BTMMJT: WRITE STRATEGY	22-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309279	10-Other	10-Other	UNTRIMMED CRYSTAL OSCILLATOR	22-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309280	05-Mobile Communication	05-Mobile Communication	AMPLIFYING CIRCUIT COMPRISING AN ENVELOPE MODULATED I/Q	18-Jul-03	307275.5	31023317	307275.5	31023317	307275.5	31023317
US0309281	07-Identification	07-Identification	DUAL INTERFACE SIM WITH HF TRIGGERED WAKEUP	16-Jul-03	3102228.3	31023317	3102228.3	31023317	3102228.3	31023317
US0309282	05-Mobile Communication	05-Mobile Communication	ENHANCING MULTIPATH FOR MIMO	17-Jul-03	316692.3	31023317	316692.3	31023317	316692.3	31023317
US0309283	08-Discretize and Multimize	08-Discretize and Multimize	CAPACITOR LEAKAGE COMPENSATION CIRCUIT	17-Jul-03	3102188.3	31023317	3102188.3	31023317	3102188.3	31023317
US0309284	05-Mobile Communication	05-Mobile Communication	METAL BUMP WITH AN INSULATION FOR THE SIDE WALLS AND NEUTRAL	16-Jul-03	3102188.3	31023317	3102188.3	31023317	3102188.3	31023317
US0309285	05-Mobile Communication	05-Mobile Communication	INPUT STAGE RESISTANT AGAINST NEGATIVE VOLTAGE SWINGS	16-Jul-03	3102188.3	31023317	3102188.3	31023317	3102188.3	31023317
US0309286	08-Discretize and Multimize	08-Discretize and Multimize	COST EFFICIENT SUPPLY FOR LEBS	16-Jul-03	3300061.3	31023317	3300061.3	31023317	3300061.3	31023317
US0309287	07-Identification	07-Identification	ENCODER WITH TEMPORAL FILTERING	16-Jul-03	3102188.3	31023317	3102188.3	31023317	3102188.3	31023317
US0309288	05-Mobile Communication	05-Mobile Communication	USE OF PARALLEL XOR OPERATION FOR SOFTWARE GPS	16-Jul-03	3300061.3	31023317	3300061.3	31023317	3300061.3	31023317
US0309289	09-Discretize and Multimize	09-Discretize and Multimize	MOBILE PHONE WITH SOUND CONVEYING MEANS	15-Jul-03	316609.9	31023317	316609.9	31023317	316609.9	31023317
US0309290	09-Discretize and Multimize	09-Discretize and Multimize	TRENCHLET WITH THICKER INTERSECTION INSULATOR	12-Jul-03	3102155.8	31023317	3102155.8	31023317	3102155.8	31023317
US0309291	01-CMOS and embedded IC	01-CMOS and embedded IC	PROCESS OF POLYMER AND POST-ASH RESIDUES REMOVAL	12-Jul-03	316362.3	31023317	316362.3	31023317	316362.3	31023317
US0309292	04-Audio/Video	04-Audio/Video	HYBRID COIL (AIRCIL) + PRINTED COIL IN CASCADE	11-Jul-03	3102155.8	31023317	3102155.8	31023317	3102155.8	31023317
US0309293	06-Connectivity	06-Connectivity	IMPROVED FREQUENCY DETERMINATION	11-Jul-03	3102122.3	31023317	3102122.3	31023317	3102122.3	31023317
US0309294	07-Identification	07-Identification	RECEIVER FOR RECEIVING FREQUENCY SIGNALS	11-Jul-03	3102105.8	31023317	3102105.8	31023317	3102105.8	31023317
US0309295	08-Automotive	08-Automotive	ROBUST LOW COST RF ASK TRANSMITTER AND RECEIVER	11-Jul-03	3102105.8	31023317	3102105.8	31023317	3102105.8	31023317

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App No	Date	Inventor	Device / Field	Abstract	Pub No
DE0300106	10-Jul-03	03-RF Devices	03-001187	03-001187	310169.2
DE0300240	16-Jul-03	04-Audio/Video	04-002126	04-002126	3102073.8
GB0301106	10-Jul-03	05-Mobile Communication	05-002541	05-002541	310168.4
NL0300813	10-Jul-03	06-Connectivity	06-006569	06-006569	3102094.4
NL0300806	9-Jul-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
NL0300809	9-Jul-03	04-Audio/Video	04-006569	04-006569	3102094.4
GB0301107	7-Jul-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
NL0300795	6-Jul-03	04-Audio/Video	04-006569	04-006569	3102094.4
FR0300787	4-Jul-03	03-RF Devices	03-006569	03-006569	3102094.4
FR0300117	4-Jul-03	05-Mobile Communication	05-006569	05-006569	3102094.4
NL0300833	2-Jul-03	01-CMOS and embedded	01-006569	01-006569	3102094.4
DE0302230	2-Jul-03	05-Mobile Communication	05-006569	05-006569	3102094.4
NL0300780	2-Jul-03	01-CMOS and embedded	01-006569	01-006569	3102094.4
DE0300525	2-Jul-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
DE0300231	30-Jun-03	01-CMOS and embedded	01-006569	01-006569	3102094.4
DE0300233	30-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
US0300224	30-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
US0300225	30-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
AT0300397	30-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
DE0300228	27-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
FR0300373	27-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
NL0300765	27-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300767	27-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300768	26-Jun-03	03-RF Devices	03-006569	03-006569	3102094.4
US0301199	25-Jun-03	01-CMOS and embedded	01-006569	01-006569	3102094.4
DE030213	25-Jun-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
DE030214	25-Jun-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
NL0300719	25-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
US0300184	25-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
NL0300701	25-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
NL0300702	25-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
NL0300714	25-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
US0301166	25-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
AT0300034	25-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
AT0300035	25-Jun-03	07-Identification	07-006569	07-006569	3102094.4
DE0300228	25-Jun-03	07-Identification	07-006569	07-006569	3102094.4
DE0300225	25-Jun-03	07-Identification	07-006569	07-006569	3102094.4
US0301182	25-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
US0301187	25-Jun-03	10-Other	10-006569	10-006569	3102094.4
NL0300713	24-Jun-03	03-RF Devices	03-006569	03-006569	3102094.4
FR0300069	23-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
GB0300094	21-Jun-03	07-Identification	07-006569	07-006569	3102094.4
GB0300105	21-Jun-03	07-Identification	07-006569	07-006569	3102094.4
GB0301108	21-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
FR0300667	20-Jun-03	01-CMOS and embedded	01-006569	01-006569	3102094.4
FR0300668	20-Jun-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
NL0400226	20-Jun-03	03-RF Devices	03-006569	03-006569	3102094.4
NL0300712	20-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
GB0300098	20-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
GB0300097	20-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300722	19-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300723	19-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300692	18-Jun-03	02-Packaging & Testing	02-006569	02-006569	3102094.4
NL0300690	18-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
DE0300222	16-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
NL0300715	16-Jun-03	09-Discretes and Multimat	09-006569	09-006569	3102094.4
DE0300212	17-Jun-03	07-Identification	07-006569	07-006569	3102094.4
NL0300688	16-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
NL0300671	16-Jun-03	05-Mobile Communication	05-006569	05-006569	3102094.4
DE0300210	12-Jun-03	06-Connectivity	06-006569	06-006569	3102094.4
DE0300204	12-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4
DE0300205	12-Jun-03	04-Audio/Video	04-006569	04-006569	3102094.4

Pub. No.	Class.	Inventor	Title	IPC Class.	Appl. No.	IPC Class.	Pub. No.	Class.	Inventor	Title	IPC Class.	Appl. No.
DE030206	07-Identification	MARTIN KAONER@PHILIPS.COM	RESISTOR CHAIN SENSOR		3101717.1	EP	3101717.1	EP				
DE030203	07-Identification	MOCENISQ@EP-MATH.UINLIESSEN.DE	DEPENDENCE MEASURES AGAINST DPA		3101716.9	EP	3101716.9	EP				
DE030202	07-Identification	THOMAS ROTTSCHAELER@PHILIPS.COM	DEPENDENCE MEASURES AGAINST DPA		3101719.7	EP	3101719.7	EP				
NL030096	01-CMOS and embedded	ADVAASSEM@PHILIPS.COM	POWER GRID STRATEGY		3101700.7	EP	3101700.7	EP				
NL0301163	01-CMOS and embedded	TED LETAVIC@PHILIPS.COM	A THIN LAYER SILICON ON INSULATOR BI-DIRECTIONAL MULTI-CARRIER COMMUNICATION SYSTEM I		3101775.4	US	3101775.4	US				
NL030337	06-Connectivity	BRIAN DE BART@PHILIPS.COM	MULTI-CARRIER COMMUNICATION SYSTEM II		3101684.4	EP	3101684.4	EP				
NL030338	06-Connectivity	EMERIC ULIJEN@PHILIPS.COM	CAPACITOR OR COIL DCCC IN SAME PACKAGE		3101686.7	EP	3101686.7	EP				
FR030062	07-Identification	HARTMUT HABEN@PHILIPS.COM	MASTER-NODE FOR A LIN-NETWORK		3503.10	FR	3503.10	FR				
DE030207	09-Automotive	TED LETAVIC@PHILIPS.COM	HIGH-SIDE CMOS IN AN INTEGRATED SOI-PROCESS.		3101704.8	US	3101704.8	US				
US030162	09-Discrete and Multichip	JOSE PINEDA DE GUYEZ@PHILIPS.COM	TUBE		6814775.42	US	6814775.42	US				
NL030695	05-Mobile Communication	ERIC KLUMPERINK@PHILIPS.COM	SWITCHED TRANSCONDUCTOR MIXER		3101676.5	EP	3101676.5	EP				
NL030707	05-Mobile Communication	MANUEL COLLADES@PHILIPS.COM	FREQUENCY MULTIPLEXED ARCHITECTURE		3101685	EP	3101685	EP				
NL030828	07-Identification	ROBERT JOCKEMSEN@PHILIPS.COM	SECURING DATA IN N-V-MEMORY		3101644.3	EP	3101644.3	EP				
NL030956	05-Mobile Communication	RICHARD PERKINS@PHILIPS.COM	ADAPTIVE INTERMODULATION DISTORTION FILTER FOR D-F REECE US		6047575.5	US	6047575.5	US				
US030157	04-Jun-03	BARTEK PAVLAK@PHILIPS.COM	FORMATION OF JUNCTIONS IN SILICIDES WITH REDUCED THERMAL BIL		3101599.3	US	3101599.3	US				
NL030623	01-CMOS and embedded	NEAL WINGEN@PHILIPS.COM	CIRCUIT TESTING ARRANGEMENT AND APPROACH THEREFOR		6047523.9	US	6047523.9	US				
US030198	02-Packaging & Testing	BART SMOLDERS@PHILIPS.COM	PICS WITH PLL-FUNCTION		3076725.5	EP	3076725.5	EP				
NL030664	03-RF Devices	KEITH BAKER@PHILIPS.COM	SECURE CARD TERMINAL		3101610.8	EP	3101610.8	EP				
GB030081	09-Discrete and Multichip	MARTIN RAY GROVER@PHILIPS.COM	EDGE TERMINATION USING LATERAL MOSFETS		312512.7	GB	312512.7	GB				
GB030082	09-Discrete and Multichip	MARTIN RAY GROVER@PHILIPS.COM	EDGE TERMINATION WITH LATERAL MOSFETS AND ZENERS		312514.3	GB	312514.3	GB				
US030145	06-Connectivity	YIFENG ZHANG@PHILIPS.COM	CLOSED LOOP IQ IMBALANCE DETECTION		312257.1	US	312257.1	US				
GB030080	09-Discrete and Multichip	BRENDAN KELLY@PHILIPS.COM	UNDERCURRENT SENSE ARRANGEMENT AND METHOD		3101562.1	EP	3101562.1	EP				
NL030591	01-CMOS and embedded	HARRY VEENDRIK@PHILIPS.COM	SIST ARCHITECTURE		3101637.3	EP	3101637.3	EP				
DE030188	07-Identification	ATUL KATOCH@PHILIPS.COM	OPERATING LONG ON-CHIP BUSES		SG00030000152	WO	SG00030000152	WO				
NL030629	04-Audio/Video	KWONG KAM CHOON@PHILIPS.COM	CHANNEL SELECTOR		3101534	EP	3101534	EP				
NL030555	04-Audio/Video	JOSE PINEDA DE GUYEZ@PHILIPS.COM	ACTIVITY MONITOR		3101539.9	EP	3101539.9	EP				
DE030187	06-Connectivity	ARIE KOPPELAAR@PHILIPS.COM	PHASE TRACKER		3101533.2	EP	3101533.2	EP				
DE030189	09-Discrete and Multichip	MARTIN KADNER@PHILIPS.COM	PHASE DETECTOR		3101538.1	EP	3101538.1	EP				
DE030151	01-CMOS and embedded	HANS-MARTIN RUTTER@PHILIPS.COM	MONOLITHIC INTEGRATED PUNCH-THROUGH-DIODE		3101515.9	EP	3101515.9	EP				
NL030518	09-Discrete and Multichip	HANS-MARTIN RUTTER@PHILIPS.COM	FORMATION OF AIR GAPS		6047320.9	US	6047320.9	US				
US030125	07-Identification	PIA TUJLS@PHILIPS.COM	SECURITY COATING IC WITH CHALLENGE-RESPONSE MECHANISM EP		3101473.7	EP	3101473.7	EP				
NL030534	02-Packaging & Testing	BONNIE SEXTON@PHILIPS.COM	METHOD AND APPARATUS FOR A SMALL HARDWARE...		3101445.8	EP	3101445.8	EP				
NL030577	02-Packaging & Testing	MOHAMMED AJMANE@PHILIPS.COM	DETECTING RESISTIVE OPEN DEFECTS		3101428.5	EP	3101428.5	EP				
DE030177	02-Packaging & Testing	MOHAMMED AJMANE@PHILIPS.COM	AT SPEED TESTING FOR RESISTIVE OPEN DEFECTS		3101471.5	EP	3101471.5	EP				
DE030175	09-Automotive	PETER FUHRMANN@PHILIPS.COM	CONFIGURABLE MESSAGE MEMORY		3101434.3	EP	3101434.3	EP				
DE030178	09-Automotive	JUERGEN INDOVONICK@PHILIPS.COM	RESPONDER WITH PROTECTION AGAINST RELAY ATTACK		3101433.5	EP	3101433.5	EP				
DE030179	09-Automotive	JOERN LINGERMANN@PHILIPS.COM	TIME-TRIGGERED COMMUNICATION SYSTEM I		3101433.6	EP	3101433.6	EP				
NL030482	01-CMOS and embedded	YUKIKO FURUKAWA@PHILIPS.COM	METHOD TO PREVENT PHOTORESIST DEFORMATION DURING DR EP		3300016.9	EP	3300016.9	EP				
FR030050	04-Audio/Video	STERHANE MUTZ@PHILIPS.COM	INTELLIGENT CACHE FOR MPEG		3101353.2	EP	3101353.2	EP				
NL030573	08-Automotive	ALAN CHANG@PHILIPS.COM	USB - EMBEDDED USB HOST PROTOCOL FOR BULK TRANSACTION US		3101355.5	EP	3101355.5	EP				
NL030572	08-Automotive	RUUDY VISSER@PHILIPS.COM	SWITCHING CURRENT SOURCES FOR DRIVING A BUS		SG2003000128	WO	SG2003000128	WO				
NL030575	05-Discrete and Multichip	XAVIER XC QIAN@PHILIPS.COM	WIRELESS COMMUNICATION RECEIVER HAVING AN ADC WITH A LCH		31077.2	WO	31077.2	WO				
NL030574	06-Connectivity	CHEE YUNG@PHILIPS.COM	USB - USB 2.0 EMBEDDED HOST ARCHITECTURE		310928.7	US	310928.7	US				
DE030071	03-RF Devices	LEE CHEE EE@PHILIPS.COM	USB - SLAVE EMBEDDED HIGH SPEED USB HOST		6046960.7	US	6046960.7	US				
DE030070	09-Discrete and Multichip	KEVIN BOYLE@PHILIPS.COM	MULTI-BAND ANTENNA OPERATION COMBINING PIFA AND NOTCH GB		6046967	US	6046967	US				
US030111	09-Discrete and Multichip	ANCO HERING@PHILIPS.COM	INCREASING BREAKDOWN VOLTAGE BY EMBEDDING HIGH-K DIELECTR		3101239	EP	3101239	EP				
US030117	05-Mobile Communication	JENS ROEYER@PHILIPS.COM	HIERARCHICAL MEMORY ACCESS VIA PIPELINING		6046858.4	US	6046858.4	US				
US030108	06-Connectivity	GERHARD RUNZE@PHILIPS.COM	MIXED DIGITAL AND ANALOGUE GAIN CONTROL		6046868.53	US	6046868.53	US				
US030106	01-CMOS and embedded	PARAG GARG@PHILIPS.COM	WLAN Scheduler		182300306199	WO	182300306199	WO				
US030105	01-CMOS and embedded	YUJI YAMAGUCHI@PHILIPS.COM	A METHOD AND A LITHOGRAPHIC STRUCTURE FOR MEASURING LE US		3101275	EP	3101275	EP				
US030104	01-CMOS and embedded	YUJI YAMAGUCHI@PHILIPS.COM	A NEW OVERLAY STRUCTURE FOR MEASURING PROCESS INDICAT		68469070	US	68469070	US				
US030155	07-Identification	ERNEST BRESSAU@PHILIPS.COM	START PATTERN VARIATION IN TRANSMISSION PROTOCOL		68469069	US	68469069	US				
US030102	07-Identification	DIREK WENZEL@PHILIPS.COM	SMART AUTHENTICATING CARD		3101272.7	EP	3101272.7	EP				
US030103	07-Identification	MIROSLAV TRAJKOVIC@PHILIPS.COM	MESSAGE TAGGING IN COMMUNICATION NETWORKS		3101262.8	EP	3101262.8	EP				
US030158	09-Automotive	SRINIVAS GUPTA@PHILIPS.COM	RECEIVER FRONT-END WITH LOW-POWER CONSUMPTION AND DR EP		310410.6	GB	310410.6	GB				
DE030157	09-Automotive	MANFRED ZINKE@PHILIPS.COM	LONG TERM PREDICTION OF EPHEMERIS DATA		310411.4	GB	310411.4	GB				
GB030062	05-Mobile Communication	ZHENHUA WANG@PHILIPS.COM	TEMPORAL PREDICTION PER SUB-IMAGE		3300011.8	EP	3300011.8	EP				
GB030063	07-Identification	AMITES SARKAR@PHILIPS.COM	METHOD OF SYNCHRONIZATION OF CLOCKS		3101255.2	EP	3101255.2	EP				
FR030047	05-Mobile Communication	BRUCE MURRAY@PHILIPS.COM	CARRIER RECOVERY BASED DEMODULATION		3101254.5	EP	3101254.5	EP				
FR030046	08-Automotive	CECILE DUFOUR@PHILIPS.COM										
DE030147	09-Automotive	JOERN LINGERMANN@PHILIPS.COM										
NL030454	04-Audio/Video	HANS VAN NIEUWENBURG@PHILIPS.COM										

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Patent No.	IPC Class.	Inventor	Applicant	Title	Pub No.	Pub Date
NL030453	08-Connectivity	MARC VLEMMINGS@PHILIPS.COM	PHILIPS.COM	MULTISTAGE FREQUENCY COMPRESSION	3101248.8	2004/03/05
DE030145	08-Automotive	PETER FUHRMANN@PHILIPS.COM	PHILIPS.COM	LOW-SAFETY COMMUNICATION SYSTEM	3101235.4	2004/02/24
NL030460	03-RF Devices	THOMAS ROEDLE@PHILIPS.COM	PHILIPS.COM	LOW-DISTORTION FET WITH MULTIPLE THRESHOLDS	3101224.8	2004/02/24
US030094	01-CMOS and embedded pr	HAROLD ZIGER@PHILIPS.COM	PHILIPS.COM	SYSTEM AND METHOD FOR CHARACTERIZING LITHOGRAPHY EFF US	60466490	2004/01/29
NL030481	02-Packaging & Testing	DAVID WRANKEN@PHILIPS.COM	PHILIPS.COM	MERGE FILL	3101166.8	2004/01/29
DE030143	04-Audio/Video	ALAN YEO, CHIN LEONG@PHILIPS.COM	PHILIPS.COM	RECEIVER CIRCUIT WITH ADJUSTABLE IF AMPLIFIER GAIN	5520033000108	2004/06/02
DE030144	04-Audio/Video	FRANK MITSCHULLAT@PHILIPS.COM	PHILIPS.COM	COMBINED POLYPHASE ZOOM-FILTER AND PEAKING-FILTER	3101181	2004/06/02
NL030440	04-Audio/Video	ERKST, NORHOL T, 021386@PHILIPS.COM	PHILIPS.COM	MIXER-SYSTEM WITH GAIN-BLOCKS AND SWITCHES	3101180.2	2004/06/02
AT030027	29-Optics	STEFAN POSCH@PHILIPS.COM	PHILIPS.COM	SELECTING SEND MODE DEPENDENT ON AVAILABLE ENERGY	3101168.7	2004/06/02
DE030142	07-Identification	FRANK BODE@PHILIPS.COM	PHILIPS.COM	METHOD TO CALIBRATE THE RESONANT FREQUENCY OF LC RES. EP	3101180.1	2004/06/02
FR030044	01-CMOS and embedded pr	REBIA EL FARHANE, 27193@PHILIPS.COM	PHILIPS.COM	GE-SILICON CAP.	350127	2004/05/28
US030093	05-Mobile Communication	AL PARS, 2634@PHILIPS.COM	PHILIPS.COM	QUADRATURE MODULATOR AND CALIBRATION METHOD	60465127	2004/05/28
NL030438	08-Automotive	VOLKER GIBREX, 28548@PHILIPS.COM	PHILIPS.COM	COMMON MODE VOLTAGE GENERATION	3101136.4	2004/05/28
NL030405	05-Mobile Communication	PETER G. BLANKENHOF@PHILIPS.COM	PHILIPS.COM	DUAL GATE RF POWER LOMOS	3101105.9	2004/05/28
NL030398	03-RF Devices	JOHN GAJADHARSSINGH@PHILIPS.COM	PHILIPS.COM	A SCHEME FOR SECURE IC'S	3101086	2004/05/28
NL030397	02-Optics	FRANCESCO PESSOLANO@PHILIPS.COM	PHILIPS.COM	METHOD FOR DIELECTRIC LAYER THICKNESS MEASUREMENT	3101051.5	2004/05/28
NL030391	17-Optics	PRASHANT MAJHI@PHILIPS.COM	PHILIPS.COM	FRONT-END SENSITIVITY BOOSTER	3101559.8	2004/05/12
DE030128	04-Audio/Video	XAVIER LAMERRECHT@PHILIPS.COM	PHILIPS.COM	METHOD FOR CONTROLLING A VGA	3101043.6	2004/05/12
NL030379	16-Optics	HANS BREKELMANS@PHILIPS.COM	PHILIPS.COM	DATA PROCESSING IN WHICH CONCURRENTLY EXECUTED PROCES	3101030.9	2004/05/12
NL030381	16-Optics	O.P. GANGWAL@PHILIPS.COM	PHILIPS.COM	MULTI-FILE COMPILATION	3101032.0	2004/05/12
FR030039	07-Identification	EMERIC LIGUEN@PHILIPS.COM	PHILIPS.COM	CURRENT PROTECTION FOR LOG REGULATOR	350111	2004/05/12
NL030436	09-Discretes and Multicom	RICHARD BARKER@PHILIPS.COM	PHILIPS.COM	SAMPLING TECHNIQUE FOR PROTECTED POWER DEVICES	3076102.7	2004/05/12
G0000046	15-Optics	BART BARENBRUG@PHILIPS.COM	PHILIPS.COM	GRAPHICS; SECOND PASS HAS LESS SHEAR	30874.1	2004/05/12
NL030433	14-Optics	JOHN CUTLER@PHILIPS.COM	PHILIPS.COM	A SOLENOID DRIVER	3076127.4	2004/05/12
DE030122	11-Optics	RAF ROOYERS@PHILIPS.COM	PHILIPS.COM	PULSE DETECTION FOR UWB RECEIVERS	3100985.3	2004/05/12
NL030402	05-Mobile Communication	MARKUS NEUMANN@PHILIPS.COM	PHILIPS.COM	CONFIGURATION OF INTEGRATED CIRCUITS	3100987.1	2004/05/12
G0000036	08-Optics	BENNO KRABENBERG@PHILIPS.COM	PHILIPS.COM	PROTECTION OF INTEGRATED CIRCUITS	3100987.1	2004/05/12
DE030102	8-Optics	ANDY YULE@PHILIPS.COM	PHILIPS.COM	INCORPORATING GPS INTO DIGITAL CAMERAS	30834.6	2004/05/12
NL030348	8-Optics	KEES VAN BERKEL@PHILIPS.COM	PHILIPS.COM	CONFIGURABLE MULTI-STEP LFSR	3100836	2004/05/12
DE030100	7-Optics	MATTHIAS WEINDEL@PHILIPS.COM	PHILIPS.COM	DIGITAL AMPLIFIER	3100819.4	2004/05/12
US030068	05-Mobile Communication	JAVIER ESGUEVILLAS, 22210@PHILIPS.COM	PHILIPS.COM	DIGITAL AMPLIFIER	3100820.2	2004/05/12
DE030102	7-Optics	THOMAS DUJREANU@PHILIPS.COM	PHILIPS.COM	DIGITAL AMPLIFIER	3100822.8	2004/05/12
NL030355	4-Optics	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	LINEAR AMPLIFIER	3100800.4	2004/05/12
US030068	05-Mobile Communication	SANTANU DUTTA@PHILIPS.COM	PHILIPS.COM	LINEAR AMPLIFIER	60460230	2004/05/12
US030078	31-mnt-03	DAN AVERY@PHILIPS.COM	PHILIPS.COM	RUNTIME CONFIGURABLE VIRTUAL VIDEO PIPELINE	60458206	2004/05/12
US030072	31-mnt-03	DAN AVERY@PHILIPS.COM	PHILIPS.COM	CONFIGURATION SERVER	60459294	2004/05/12
US030090	31-mnt-03	DAN AVERY@PHILIPS.COM	PHILIPS.COM	CONFIGURATOR GRAPHIC USER INTERFACE (GUI)	60459327	2004/05/12
NL030297	31-mnt-03	QAN AVERY@PHILIPS.COM	PHILIPS.COM	HARDWARE CONFIGURATOR	3100846.9	2004/05/12
AT030014	31-mnt-03	AD. VAN DEN ENGEN@PHILIPS.COM	PHILIPS.COM	UP AND DOWN SAMPLE RATE CONVERTER	3100837.8	2004/05/12
NL030357	28-mnt-03	HENRIK PRZYBYLA, 20458@PHILIPS.COM	PHILIPS.COM	METHOD TO GRANT MODIFICATION RIGHTS FOR A SMART CARD	3100827.2	2004/05/12
NL030347	28-mnt-03	PHILIPPE MELNIER-BEILLARD@PHILIPS.COM	PHILIPS.COM	HIGH-N-TYPE CONCENTRATION AND P-PROFILES IN SHEPTAXY	3100832	2004/05/12
DE030087	28-mnt-03	RADU SURDULEANU@PHILIPS.COM	PHILIPS.COM	MULTI-LAYER HIGHLY ACTIVATED POLY GATE ELECTRODE	5520033000086	2004/05/12
NL030301	26-mnt-03	TOH KONG LIM@PHILIPS.COM	PHILIPS.COM	ELECTRONIC RF SWITCH AND TERRESTRIAL APPLICATIONS IN PC WO	3590662.2	2004/05/12
NL030319	28-mnt-03	BRUNO MOTTE@PHILIPS.COM	PHILIPS.COM	STEP AGC AMP IN TUNER IC ADJUSTED IN VBI	3100826.1	2004/05/12
NL030320	28-mnt-03	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	FAST LINEAR PHASE DETECTOR	3100826.1	2004/05/12
NL030360	28-mnt-03	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	LINEAR PHASE DETECTOR	3100827.9	2004/05/12
NL030306	28-mnt-03	M.P. VANDERHEIJDEN@PHILIPS.COM	PHILIPS.COM	NEUTRALIZATION OF FEEDBACK CAPACITANCE IN RF AMPL.	3100792.5	2004/05/12
NL030307	28-mnt-03	MIHAI SANDULEANU@PHILIPS.COM	PHILIPS.COM	TRACK AND HOLD CIRCUIT	3100736.2	2004/05/12
NL030285	27-mnt-03	FRANCESCO PESSOLANO@PHILIPS.COM	PHILIPS.COM	BRANCH BASED ACTIVITY MONITORING	3100726.5	2004/05/12
NL030296	27-mnt-03	GIUSEPPE GRILLI@PHILIPS.COM	PHILIPS.COM	ADAPTIVE BIASING OF A POWER AMPLIFIER VIA A SWITCH MODUL	3100720.6	2004/05/12
DE030092	21-mnt-03	MARION MATTERS@PHILIPS.COM	PHILIPS.COM	SERIES LC-RESONATOR	3100697.6	2004/05/12
NL030289	20-mnt-03	KIMLE PHAM@PHILIPS.COM	PHILIPS.COM	MTRAM; READ WHILE WRITING ZIG-ZAG	3100778.1	2004/05/12
DE030086	19-mnt-03	JOACHIM BRILKA@PHILIPS.COM	PHILIPS.COM	CIRCUIT AND METHOD FOR CONTROLLING TRANSISTORS	3100715.6	2004/05/12
NL030287	19-mnt-03	ETHIMIOS TSILIOUKAS@PHILIPS.COM	PHILIPS.COM	REDUCED LENGTH MICRO STRIP FILTER	60455037	2004/05/12
NL030381	19-mnt-03	NIK LAMBERT@PHILIPS.COM	PHILIPS.COM	UNIVERSAL MEMORY WITH PROGRAMMABLE ACCESS CONTROL	3100708.1	2004/05/12
US030058	19-mnt-03	MARCO BERQU@PHILIPS.COM	PHILIPS.COM	TYPE CONVERSION UNIT IN A MULTIPROCESSOR SYSTEM	60455037	2004/05/12
DE030076	17-mnt-03	BILL REDMAN-WHITE@PHILIPS.COM	PHILIPS.COM	QUADRATURE CLOCK DIVIDER	3100652.1	2004/05/12
DE030077	14-mnt-03	CEES RONDA@PHILIPS.COM	PHILIPS.COM	SEMICONDUCTING DEVICE COMPR. AN ISOLATION LAYER	3100652.1	2004/05/12
DE030078	14-mnt-03	NORMAN NOLTE, 26707@PHILIPS.COM	PHILIPS.COM	TWO-DIMENSIONAL DATA MEMORY	3100654.7	2004/05/12
US030056	14-mnt-03	BURKHARD LAUE@PHILIPS.COM	PHILIPS.COM	DEVICE FOR GENERATING A 3D-VIDEO SIGNAL	60454833	2004/05/12
US030060	14-mnt-03	ZHENHUA WANG@PHILIPS.COM	PHILIPS.COM	SIMPLE FULLY-DIFFERENTIAL MOS SINE WAVE SHAPE	60454833	2004/05/12
US030061	14-mnt-03	LASZLO HARS, 22011@PHILIPS.COM	PHILIPS.COM	METHOD AND APPARATUS OF RETAINING MAXIMUM SPEED...	60454833	2004/05/12
US030061	14-mnt-03	LASZLO HARS, 22011@PHILIPS.COM	PHILIPS.COM	IMPLEMENTATION OF METASTABILITY BASED...	60454833	2004/05/12
US030061	14-mnt-03	LASZLO HARS, 22011@PHILIPS.COM	PHILIPS.COM	ELECTRONIC CIRCUIT FOR RANDOM NUMBER GENERATION	60454833	2004/05/12

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Patent No.	Inventor	Title	IPC Class.	Pub No.	Pub Date	Pub Type	Pub Description
US0300462	14-nnt-03	07-Identification	07-Identification	US	60464637	US	60464637
US0300463	14-nnt-03	07-Identification	07-Identification	US	60464815	US	60464815
FR030027	13-nnt-03	02-Packaging & Testing	02-Packaging & Testing	EP	32306333.1	EP	32306333.1
DE0300271	12-nnt-03	04-Audio/Video	04-Audio/Video	EP	31008333.1	EP	31008333.1
DE0300275	12-nnt-03	05-Mobile Communication	05-Mobile Communication	EP	31008229.9	EP	31008229.9
US0300253	12-nnt-03	06-Connectivity	06-Connectivity	US	60464837	US	60464837
DE0300271	11-nnt-03	04-Audio/Video	04-Audio/Video	EP	31006003.4	EP	31006003.4
NL0301159	10-nnt-03	05-Mobile Communication	05-Mobile Communication	EP	31004983.5	EP	31004983.5
NL0301162	10-nnt-03	05-Mobile Communication	05-Mobile Communication	EP	31004984.5	EP	31004984.5
US0300280	7-nnt-03	02-Packaging & Testing	02-Packaging & Testing	US	60465049	US	60465049
NL0301162	6-nnt-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	3100554.9	EP	3100554.9
NL0301193	6-nnt-03	04-Audio/Video	04-Audio/Video	EP	3100555.8	EP	3100555.8
AT030011	5-nnt-03	05-Mobile Communication	05-Mobile Communication	EP	3100496.3	EP	3100496.3
AT0300101	27-feb-03	04-Audio/Video	04-Audio/Video	EP	3100464.9	EP	3100464.9
AT0300100	27-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100444.3	EP	3100444.3
NL0301165	25-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100487.1	EP	3100487.1
FR0300118	25-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100407	EP	3100407
US0300034	24-feb-03	04-Audio/Video	04-Audio/Video	US	60464807	US	60464807
NL0301156	20-feb-03	04-Audio/Video	04-Audio/Video	EP	3100407	EP	3100407
NL0401131	20-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100378.1	EP	3100378.1
DE0300954	19-feb-03	04-Audio/Video	04-Audio/Video	EP	3100379.1	EP	3100379.1
FR0300117	18-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100379.1	EP	3100379.1
FR0300113	18-feb-03	02-Packaging & Testing	02-Packaging & Testing	EP	3100370	EP	3100370
NL0301173	18-feb-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	3100367.6	EP	3100367.6
NL0301175	18-feb-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	3100368.4	EP	3100368.4
DE0300175	15-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100333.8	EP	3100333.8
DE0300252	15-feb-03	08-Automotive	08-Automotive	EP	3100317.1	EP	3100317.1
DE0300408	13-feb-03	08-Automotive	08-Automotive	EP	3100318.9	EP	3100318.9
DE0300122	12-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100284.7	EP	3100284.7
NL0301116	11-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100284.7	EP	3100284.7
NL0301110	11-feb-03	03-RF Devices	03-RF Devices	EP	3075586.2	EP	3075586.2
NL030045	11-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100278.3	EP	3100278.3
DE030045	11-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100267.4	EP	3100267.4
DE030046	11-feb-03	05-Mobile Communication	05-Mobile Communication	EP	60448339	EP	60448339
DE030048	11-feb-03	02-Packaging & Testing	02-Packaging & Testing	EP	60445822	EP	60445822
DE030049	10-feb-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	3100256.3	EP	3100256.3
US030022	10-feb-03	05-Mobile Communication	05-Mobile Communication	US	60464837	US	60464837
FR030011	7-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100217.2	EP	3100217.2
DE030042	7-feb-03	05-Mobile Communication	05-Mobile Communication	EP	3100214	EP	3100214
NL030078	3-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100207.4	EP	3100207.4
NL030080	3-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100211.8	EP	3100211.8
NL030081	3-feb-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100183.7	EP	3100183.7
NL030088	31-jan-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100181.1	EP	3100181.1
NL030121	31-jan-03	05-Mobile Communication	05-Mobile Communication	EP	3100172	EP	3100172
FR030005	29-jan-03	06-Connectivity	06-Connectivity	EP	3100175.3	EP	3100175.3
NL030101	29-jan-03	06-Connectivity	06-Connectivity	EP	3100174.6	EP	3100174.6
NL030102	29-jan-03	02-Packaging & Testing	02-Packaging & Testing	EP	3100124.1	EP	3100124.1
NL030095	29-jan-03	02-Packaging & Testing	02-Packaging & Testing	EP	3100125.9	EP	3100125.9
DE030072	28-jan-03	05-Mobile Communication	05-Mobile Communication	EP	3100110.5	EP	3100110.5
DE030073	27-jan-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100084.6	EP	3100084.6
DE030074	27-jan-03	03-RF Devices	03-RF Devices	EP	3100077.1	EP	3100077.1
NL030088	20-jan-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	60439986	EP	60439986
NL030082	17-jan-03	09-Discretics and Multimat	09-Discretics and Multimat	EP	60439972.2	EP	60439972.2
NL030041	15-jan-03	02-Packaging & Testing	02-Packaging & Testing	EP	3100047.4	EP	3100047.4
NL030052	14-jan-03	04-Audio/Video	04-Audio/Video	EP	3100032.6	EP	3100032.6
US0300513	16-jan-03	10-Other	10-Other	US	60439986	US	60439986
DE030432	15-jan-03	06-Connectivity	06-Connectivity	EP	3100046.6	EP	3100046.6
NL030066	14-jan-03	01-CMOS and embedded pr	01-CMOS and embedded pr	EP	3100047.4	EP	3100047.4
US030014	14-jan-03	02-Packaging & Testing	02-Packaging & Testing	US	60439986	US	60439986
NL030010	14-jan-03	07-Identification	07-Identification	EP	3100032.6	EP	3100032.6
DE030014	14-jan-03	07-Identification	07-Identification	EP	3100032.6	EP	3100032.6
DE030015	10-jan-03	10-Identification	10-Identification	EP	3100032.6	EP	3100032.6

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IPC Class	Inventor	Company	Title	Pub No
G05G0011	07-Identification	REINHARD MEIER@PHILIPS.COM	FAST Binarisation of FingerPrint Images	3100025
G05G0012	07-Identification	REINHARD MEIER@PHILIPS.COM	IDENTIFY REGION OF INTEREST OF FINGERPRINT IMAGES	3100026.8
G05G0098	05-Mobile Communication	ALEXANDER STEINER@PHILIPS.COM	CONTROL OF THE SUPPRESSION OF ACOUSTIC SHOES IN...	3100016.9
G05G0099	05-Mobile Communication	ALEXANDER STEINER@PHILIPS.COM	NON-LINEAR ACOUSTIC ECHO CANCELLER FOR USE WITH...	3100017.7
A03G0001	07-Identification	MARKUS HARNISCH@PHILIPS.COM	NFC TO INITIATE BLUETOOTH COMMUNICATION	3100020.1
A03G0002	07-Identification	KLEMONS BREITFUSS@PHILIPS.COM	MODE CHANGING BY MEANS OF A MARKED RF-FIELD	3100022.7
G05G0010	09-Discreties and Multimarks	STEFAN BUTZMANN@WEB.DE	METHOD TO USE AMB-SENSORS FOR DETECTION OF LINEAR DISP	3100018.5
G05G0011	04-Audio/Video	WINFRIED JANSSEN@PHILIPS.COM	FIELDSTRENGTH DETECTION	3100007.8
G05G0012	04-Audio/Video	ANTHONY MOREL@PHILIPS.COM	METHOD AND APPARATUS FOR ENCODING/DECODING DYNAMIC (CN	2158390
G05G0044	05-Mobile Communication	JAN HOOGERBRUGGE@PHILIPS.COM	SAVING RAM WRITE POWER, READ-OR-WRITE	2080588.2
G05G0045	05-Mobile Communication	ALEX LIANG 25115@PP.PHILIPS.COM	METHOD AND DEVICE TO MAINTAIN SYNCHRONIZATION TRACKING; CN	2100463.2
F02G0145	05-Mobile Communication	ALEX LIANG 25115@PP.PHILIPS.COM	METHOD AND DEVICE FOR MULTI-USER DETECTION SIMPLIFIED D CN	2100465.7
G05G0034	03-RF Devices	DOMINIQUE BRUNEL@PHILIPS.COM	MULTI-ANTENNA SOLUTION FOR MOBILE HANDSET	2230258.4
G05G0035	03-RF Devices	XULIZHOU@DSF.UFL.EDU	A SMART ANTENNA SOLUTION FOR MOBILE HANDSET	2100403.7
F02G0031	04-Audio/Video	NOBUI NEGISHI 14410@PF.PHILIPS.COM	A SMART ANTENNA SOLUTION FOR MOBILE HANDSET	2100403.9
G05G0036	05-Mobile Communication	ZS3K91@128.COM	A POWER CONTROL SOLUTION FOR MOBILE HANDSET	02-382481
G05G0037	05-Mobile Communication	ANDY YULE@PHILIPS.COM	GPS TUNING	2158894.1
B02G0043	01-CMOS and embedded pr	VINCENT VENEZIA@PHILIPS.COM	METHOD OF FORMING FULLY SILICIDED GATE ON MOS TRANSISTE EP	2080133.1
B02G0044	01-CMOS and embedded pr	PETER MAGNÉE@PHILIPS.COM	MAKING DEVICES USING SELF-ALIGNED POLY SEED LAYER ON O EP	2080508.1
B02G0045	01-CMOS and embedded pr	PAUL SIMON@PHILIPS.COM	ANTI-VIA FIRST APPROACH FOR DUAL DAMASCENE PATTERNING EP	2080507.3
NL021491	02-Packaging & Testing	PIETER HOCHSTENBACH@PHILIPS.COM	DSP MEGA PEM	2080535.4
US020610	02-Packaging & Testing	OTTO STENIGUSCH@PHILIPS.COM	CONNECTING MULTIPLE TEST ACCESS PORT CONTROLLERS ON A US	2080664.2
NL021478	04-Audio/Video	ROB BEUKER@PHILIPS.COM	GENERIC APPARATUS FOR RE-ORDERING VIDEO DATA FOR DISPLI US	60/435395
US020628	04-Audio/Video	RICHARD SHENG@PHILIPS.COM	ENHANCING VIDEO IMAGES DEPENDS ON PRIOR...	2080425.2
DE020034	01-CMOS and embedded pr	GUNNAR NITSCH@PHILIPS.COM	VERFAHREN ZUR HERSTELLUNG EINER VERBINDUNG ZWISCHEN DE	60/435237
DE020042	01-CMOS and embedded pr	ROB VAN SCHAIK@PHILIPS.COM	VERTICAL SPILT GATE NM CELL AND METHOD OF FABRICATION EP	2080620.4
NL021446	01-CMOS and embedded pr	JAN HAISMA 01090@PP.PHILIPS.COM	SHOCK-PROTECTION FOR ION-BEAM SYSTEM	2080620.4
NL030258	01-CMOS and embedded pr	JOHN VAN DER VEN@PHILIPS.COM	ELECTRIC DEVICE COMPRISING PHASE CHANGE MATERIAL EP	2080620.8
NL031068	01-CMOS and embedded pr	MARTIN LANKHORST@PHILIPS.COM	ELECTRIC DEVICE COMPRISING PHASE CHANGE MATERIAL EP	3103338.4
NL021368	04-Audio/Video	MARTIN LANKHORST@PHILIPS.COM	ELECTRIC DEVICE COMPRISING PHASE CHANGE MATERIAL EP	3103340
NL021330	05-Mobile Communication	JARVAN SINDEREN@PHILIPS.COM	MIXER SYSTEM	2080396.1
US020654	05-Mobile Communication	EDWIN RUPKEMA 25641@PF.UNKNOWN	COMBINED BEST EFFORT GUARANTEED THROUGHPUT DATA SCH EP	2080377.1
NL021332	05-Mobile Communication	ANDRE VAN BEZOUJEN@PHILIPS.COM	POWER AMPLIFIER WITH BIAS FEEDBACK	60/434552
NL021331	05-Mobile Communication	VICKRAM VATHULYA@PHILIPS.COM	MINIMIZING 1F CONFIGURATION AND METHOD	2080300.3
NL020179	05-Connectivity	EDWIN RUPKEMA 25641@PF.UNKNOWN	FRAME SYNCHRONISING DEVICE AND METHOD	2080489.1
NL020180	01-CMOS and embedded pr	KARLS-MICHEL LENNSEN@PHILIPS.COM	CONTENTION RESOLUTION COVERING ALL PORTS OF A DATA SW EP	31000402.1
NL021289	02-Packaging & Testing	KARLS-MICHEL LENNSEN@PHILIPS.COM	PROTECTION OF AN MRAM DEVICE AGAINST TAMPERING EP	3100403.9
US020611	02-Packaging & Testing	TOM KAMPSCHREUR@PHILIPS.COM	HARDWARE SECURITY FEATURES FOR MRAM	2080360.7
US020612	02-Packaging & Testing	CARL KNUDSEN@PHILIPS.COM	WIREBONDING METHOD AND APPARATUS	60/434520
US020615	04-Audio/Video	XAVIER PRUVOST@PHILIPS.COM	TAMPER-RESISTANT PACKAGING AND APPROACH	60/434323
US020617	04-Audio/Video	PADRAIG O'MAHONY@PHILIPS.COM	THRESHOLD-BASED SCHEDULING FOR NETWORK INTERFACE	2100097
DE020317	09-Discreties and Multimarks	DAVE EVOY@PHILIPS.COM	TUNER WITH IMPROVED AGC	60/434822
US020616	09-Discreties and Multimarks	STEFAN BUTZMANN@WEB.DE	CODE DOWNLOAD IN A SYSTEM HAVING MULTIPLE INTEGRATED.. US	60/434798
US020618	09-Discreties and Multimarks	ANDREA NIEDERKORN@PHILIPS.COM	DEDICATED ENCRYPTED VIRTUAL CHANNEL IN A MULTI-CHANNEL EP	2102756.6
US020619	09-Discreties and Multimarks	ANNIE GOFF 31237@PF.PHILIPS.COM	METHOD TO DETECT A LINEAR DISPLACEMENT	60/434824
US020623	03-RF Devices	RAINER PETIUS@PHILIPS.COM	PHASE CORRECTED MILLER COMPENSATION OF COPPER	2102777.6
US020624	03-RF Devices	DANISH AL@PHILIPS.COM	ENCAPSULATED HARDWARE CONFIGURATION CONTROL	2080530.7
US020625	04-Audio/Video	JOHN YARBOROUGH@PHILIPS.COM	NON-RECIPROCAL CIRCUIT ELEMENT	60/434113
US020626	04-Audio/Video	CHRIS JOLY 21899@PF.PHILIPS.COM	TEMPERATURE COMPENSATED P-C OSCILLATOR	60/433742
US020627	04-Audio/Video	FRANS SEUNINK 02564@PF.UNKNOWN	POLY-SILICON STRINGER FUSE	60/433746
US020628	04-Audio/Video	ARVIND SUNDARARAJAN@PHILIPS.COM	SELF ADAPTABLE BIAS CIRCUIT FOR ENABLING DYNAMIC... US	60/433753
US020629	04-Audio/Video	RICHARD SHEN@PHILIPS.COM	INTER SET BOX TOP PERSONAL MESSAGING WITHOUT... US	60/433752
US020630	05-Mobile Communication	ARVIND SUNDARARAJAN@PHILIPS.COM	METHOD FOR MOSAIC PROGRAM GUIDE	2102763.5
US020631	05-Mobile Communication	CHRIS JOLY 21899@PF.PHILIPS.COM	SELF ADAPTABLE BIAS CIRCUIT FOR ENABLING DYNAMIC... EP	3240584.1
US020632	07-Identification	STEFFEN SCHULZE@BBK.DE	LOCALLY ADAPTIVE GABOR-FILTERING IN THE SPATIAL DOMAIN EP	2102763.5
US020633	08-Discreties and Multimarks	MICHAEL IN 2ANOT@PHILIPS.COM	THICK TRENCH BOTTOM OXIDE BY OXIDATION OF POLYSI EXPOSE GB	228217.9
US020634	08-Discreties and Multimarks	ERWIN HUIZEN@PHILIPS.COM	TRENCH MOSFET WITH SIDEWALL GATE	20805582.A1
US020635	08-Discreties and Multimarks	ERWIN HUIZEN@PHILIPS.COM	THICK TRENCH BOTTOM OXIDE BY OXIDATION OF POLY-SI GB	228210
US020636	08-Discreties and Multimarks	ERWIN HUIZEN@PHILIPS.COM	THICK TRENCH BOTTOM OXIDE BY OXIDISING POROUS SI GB	228212.6

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US0200574	02-Packaging & Testing	JAMAYO@PHILIPS.COM	MATCHED VARIABLE RESISTOR STRUCTURES TO...	2004052592A1	US	60/433111	
GB0200222	03-RF Devices	SOB.MILSDM@PHILIPS.COM	ELECTRO-ACOUSTIC RESONATOR	2004052593A1	EP	2258613.5	
NL021411	03-RF Devices	LUIJK.TIEMEIJER@PHILIPS.COM	PLANAR INDUCTIVE COMPONENT WITH PATTERNED GROUND SHEET	2004052594A1	EP	2080328.4	
IN0200007	04-Audio/Video	S.NAVANEET.EASWARAN@PHILIPS.COM	LOW LOCK TIME DELAY LOCKED LOOPS USING TIME...	2004052595A1	US	60/433213	
IN0200008	04-Audio/Video	S.NAVANEET.EASWARAN@PHILIPS.COM	COARSE DELAY TUNER CIRCUITS WITH EDGE SUPPRESSION...	2004052596A1	US	60/433373	
NL021408	04-Audio/Video	LEO.RUTTENBERG.2@PHILIPS.COM	TRACKING OF TUNER FILTERS WITH ARBITRARY OSCILLATOR...	2004052597A1	US	60/433366	
US020578	06-Connectivity	RONNIE.SEXTON@PHILIPS.COM	SMALL HARDWARE IMPLEMENTATION OF THE SUBBYTE...	2004052598A1	US	60/433365	
CH020037	04-Audio/Video	JOACHIM.REINER.1@PHILIPS.COM	ONE-TIME PROGRAMMABLE MEMORY DEVICE	1574952.A	EP	2102734.7	
US020562	04-Audio/Video	JAN.HOOPERBRUGGE@PHILIPS.COM	COUNTER BASED STRIDE PREDICTION FOR DATA PREFETCH	2004052599A1	US	60/432755	
US020565	05-Mobile Communication	SALEH.OSMAN.24@PHILIPS.COM	PRESERVING LINEARITY OF AN ISOLATOR-FREE POWER...	2004052600A1	US	60/432696	
US020557	05-Mobile Communication	SALEH.OSMAN.24@PHILIPS.COM	PRESERVING LINEARITY OF AN ISOLATOR-FREE POWER...	2004052601A1	US	60/432897	
US020542	05-Mobile Communication	G.BURNS@PHILIPS.COM	DATA-FLOW SYNCHRONIZED EMBEDDED FIELD PROGRAMMABLE...	2004052602A1	US	60/432871	
US020543	06-Connectivity	G.BURNS@PHILIPS.COM	MODULAR INTEGRATION OF A SYSTOLIC ARRAY PROCESSOR...	2004052603A1	US	60/432881	
US020563	12-Identific	SANTANU.DUTTA@PHILIPS.COM	CONFIGURABLE MEMORY PARTITIONING IN HARDWARE	2004052604A1	US	60/432756	
NL021367	06-Connectivity	JOHN.FLEUNIS@PHILIPS.COM	SMART CARD WITH SELECTED DISPLAY	2004052605A1	US	60/432881	
FR020137	03-Packaging & Testing	BENOIT.AGNUS@PHILIPS.COM	RF BIIST METHOD WITH AUTOMATIC TEST IN TX CHAIR	2004052606A1	FR	215658	
DE020906	06-Audio/Video	ANDREAS.KOELLMANN@PHILIPS.COM	CMOS OVERTONE XTAL-OSCILLATOR	2004052607A1	EP	2102730.5	
FR020140	11-Identific	ERIC.BARRAU@PHILIPS.COM	MULTIPLE-PASS PIXEL SYSTEM	2004052608A1	EP	2293132.3	
NL021229	05-Mobile Communication	JOS.BERGERVOET@PHILIPS.COM	THREE PHASE MIKEX SYSTEM	2004052609A1	EP	2080247.6	
DE020305	09-Discret	AXEL.NAETHE@PHILIPS.COM	TRANSISTOR WITH A SW-AREA BETWEEN BASE AND EMITTER	2004052610A1	EP	2102737.3	
US020512	10-Identific	CHRIS.WYLAND@PHILIPS.COM	HIGH DENSITY PACKAGE INTERCONNECT WIRE BOND STRIP LINE...	2004052611A1	US	60/431642	
NL0203201	03-RF Devices	J.T.MAVANBEK@PHILIPS.COM	PIEZORESISTIVE MEMS RESONATOR	2004052612A1	EP	2080220.3	
NL021246	03-RF Devices	THEO.RUIJS@PHILIPS.COM	MEMS ARRAY	2004052613A1	EP	2080219.5	
NL021343	07-Identific	P.M.TUYLS@PHILIPS.COM	EFFICIENT IMPLEMENTATION OF ZERO-KNOWLEDGE PROTOCOLS...	2004052614A1	US	228780.5	
NL021358	09-Discret	LUDWIG.MATLAB.RESEARCH@PHILIPS.COM	INTEGRATED HALF-BRIDGE POWER CIRCUIT	2004052615A1	US	60/432302	
US020527	01-CMOS and embedded pr	WIEBE.DEBOER@PHILIPS.COM	SYSTEM AND METHOD FOR SUPPRESSION OF WAFER TEMPERAT...	2004052616A1	US	101637363	
DE020297	05-Mobile Communication	PIERLUIGI.DALESSANDRO@PHILIPS.COM	METHOD FOR PHASE-GAIN IMBALANCE ESTIMATION AND COMPEN...	2004052617A1	EP	2080114.8	
US020477	05-Mobile Communication	SIFEN.LUO.17623@PF.UNKNOWN.ORG	AMPLIFIER CIRCUIT HAVING AN EXTENDED WILSON...	2004052618A1	US	60/431642	
US020505	05-Mobile Communication	SIFEN.LUO.17623@PF.UNKNOWN.ORG	SELF-ALIGNED SHALLOW TRENCH ISOLATION WITH IMPROVED...	2004052619A1	US	60/431640	
BE020039	6-Identific	ROB.VAN.SCHAALJK@PHILIPS.COM	FRAME DRIVE APPARATUS AND METHOD	2004052620A1	EP	10637673	
NL021316	05-Mobile Communication	RIAN.DE.PAUN@PHILIPS.COM	POLYPHASE FILTERS WITH RESTORED SYMMETRY	2004052621A1	US	2080103.1	
NL021114	05-Mobile Communication	RON.SBRUKERS@PHILIPS.COM	POLYPHASE MEMORY EMBEDDED IN 3T STRUCTURE	2004052622A1	EP	2080119.7	
NL021190	5-Identific	BANS.PHAM@PHILIPS.COM	METHOD FOR TRUE RANDOM NUMBER GENERATION...	2004052623A1	EP	2080114.8	
NL021194	05-Mobile Communication	FERDINAND.SLIJUS@PHILIPS.COM	METHOD FOR IMPROVING THE RECEIVED RESOLUTION OF A CO...	2004052624A1	US	60/430876	
US020508	5-Identific	SAM.MITCHELL.26337@PHILIPS.COM	SEAMLESS AGILE DECODER	2004052625A1	US	60/430930	
US020485	4-Identific	ROB.BELKER@PHILIPS.COM	SYNCHRONIZATION OF SIGNALS	2004052626A1	US	60/430616	
NL021323	03-RF Devices	MURALL.MALI@PHILIPS.COM	METHOD AND APPARATUS FOR A DUAL MODE TELEPHONE...	2004052627A1	US	60/430918	
US020482	4-Identific	ARAVIND.SOUNDARARAJAN@PHILIPS.COM	METHOD OF AUTOMATICALLY TESTING AUDIO/VIDEO...	2004052628A1	US	60/430817	
US020484	4-Identific	RAMANATHAN.MEENAKSHISUNDARAM.2@PHILIPS.COM	STEREO SIGNAL COMMUNICATION USING BLUETOOTH...	2004052629A1	US	60/430916	
S020481	4-Identific	MARK.KELLER.25488@PF.PHILIPS.COM	DYNAMIC MEMORY USE, RELEASE OF ESP	2004052630A1	EP	10256587.2	
DE020240	4-Identific	STEVEN.AERTS@PHILIPS.COM	PARTLY ENCRYPTING OF DATA	2004052631A1	DE	10256586.4	
DE020290	4-Identific	MARKUS.FEUSER@PHILIPS.COM	CHIP WITH MICROPROCESSOR AND INTEGRATED CALCULATION...	2004052632A1	DE	10256587.2	
DE020283	07-Identific	THOMAS.BEHLING@PHILIPS.COM	NON-LINEAR DISTRIBUTION OF VOLTAGE STEPS IN...	2004052633A1	US	60/430320	
NL021504	4-Identific	FRANK.BUDZELAAR@PHILIPS.COM	REGISTER FILE GATING TO REDUCE MICROPROCESSOR POWER...	2004052634A1	US	60/430884	
NL021505	10-Other	MANISH.GARG@PHILIPS.COM	SOFTWARE-BASED CONTROL OF MICROPROCESSOR POWER...	2004052635A1	US	60/430884	
US020285	29-Identific	ANDREI.TEREMCHOK@PHILIPS.COM	PASSIVE KEYLESS ENTRY SYSTEM	2004052636A1	US	10259888.9	
NL021245	07-Identific	JUERGEN.NOWOTTNICK@PHILIPS.COM	MULTIPLIER WITH LOOKUP TABLES	2004052637A1	US	2080103.1	
US020211	29-Identific	GERARD.HUBERT.06569@PF.UNKNOWN	AUTOMATIC DEADTIME REDUCTION	2004052638A1	US	2080103.1	
US020212	09-Discret	PHIL.RUTTEN@PHILIPS.COM	FEEDBACK SIGNAL FOR POWER SWITCHING CIRCUIT	2004052639A1	US	2080103.1	
NL021242	29-Identific	NICK.WHEELER.22764@PF.PHILIPS.COM	ACTIVE WRITE FIELD SHAPING FOR MRAM	2004052640A1	US	20800005.9	
NL021243	29-Identific	HANS.BOEVE@PHILIPS.COM	1T1MTJ.MRAM ARCHITECTURE WITH SHARED DIGIT LINE	2004052641A1	EP	2080006.6	
NL021246	01-CMOS and embedded pr	HANS.BOEVE@PHILIPS.COM	METHOD TO DETECT THE LIKELY ONSET OF THERMAL RELAXATI...	2004052642A1	EP	2080007.4	
DE020287	03-RF Devices	GAVIN.PHILLIPS@PHILIPS.COM	DECOUPLING CAPACITOR	2004052643A1	DE	10259888.9	
NL021186	29-Identific	MARION.MATTERS@PHILIPS.COM	SUPPRESSION OF NOISE IN AN INTEGRATED CIRCUIT	2004052644A1	DE	10259888.9	
NL021168	05-Mobile Communication	JOSE.PINEDA.DE.GYVEZ@PHILIPS.COM	CURRENT RE-ROUTING SCHEME FOR SERIAL PROG. MRAM	2004052645A1	EP	2080103.1	
NL021220	27-Identific	PATRICK.MELWISSEN@PHILIPS.COM	LOW COMPLEXITY EQUALIZER FOR MOBILE HANDBSET	2004052646A1	EP	2080103.1	
FR020129	27-Identific	TON.DITTEWIG@PHILIPS.COM	DECOUPLING INITIALIZATION AND INITIATION OF OVERHEAD LO...	2004052647A1	EP	2080103.1	
US020220	27-Identific	ERIC.DESMICHT@PHILIPS.COM	SECURED LOW COST STB	2004052648A1	US	2080103.1	
US020220A	25-Identific	ROMAN.DILOISY@PHILIPS.COM	LOW-F PRE-PREAMBLE ANTENNA DIVERSITY RECEIVER	2004052649A1	US	2080103.1	
US020220B	25-Identific	ADRIAN.PAYNE@PHILIPS.COM	CLEAN SWITCHING OF GATE DRIVERS LOW VOLTAGE VL...	2004052650A1	US	2080103.1	
NL021143	25-Identific	MARTIN.DAUM@PHILIPS.COM	CENTRAL ESD PROTECTION FOR BUS PINS	2004052651A1	EP	2080103.1	
DE020275	22-Identific	HENK.GOEZEN@PHILIPS.COM	FLASH ATTACK WATCH DOG	2004052652A1	DE	2080103.1	

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DE020278	07-Identification	DETLEF MUELLER, 2@PHILIPS.COM	SECURED CONDITIONAL BRANCH OPERATION	10254658.4	2004/04/15
DE020279	07-Identification	JUERGEN SCHROEDER@PHILIPS.COM	RANDOM NUMBER GENERATOR FOR MICROCONTROLLER	10254657.8	2004/04/15
US0200465	06-Discrete and Multimed	JAN-WILLEM VAN DE WAERDT@PHILIP	USING CACHE MISS PATTERN TO ADDRESS PREDICTIOI US	604928285	2004/03/16
DE020283	04-Audio/Video	MARTIN MAIER@PHILIPS.COM	METHOD AND DEVICE FOR DETERMINING THE SAMPLE FREQUEN	10254468.7	2004/03/16
DE020284	05-Mobile Communication	ERNALD PRASL@PHILIPS.COM	LOW HEIGHT DIAPHRAGM WITH STIFFENING GROOVES	21026151.8	2004/03/16
AT020208	07-Identification	FRANZ AMTMRANN@PHILIPS.COM	CHECK SUM GENERATION WITH TWO START VALUES	2102616.6	2004/03/16
DE020270	07-Identification	MATHIAS WAGNER@PHILIPS.COM	MICROELECTRONIC MEMORY CHIP WITH INTEGRAL LIGHT SENSO	10254325.9	2004/03/16
DE020271	07-Identification	REINHARD MEIER@PHILIPS.COM	DETERMINATION OF SUPPORTING SURFACE OF FINGERPRINT	10254320.8	2004/03/16
DE020272	07-Identification	WOLFGANG BUHR@PHILIPS.COM	GENERATION OF KEY CODES FOR NV MEMORIES	3180723.4	2004/03/16
DE030094	07-Identification	SOENKE OSTERLUN@PHILIPS.COM	MODIFICATION OF ERROR CORRECTION CIRCUIT	50427542	2004/03/16
US020452	04-Audio/Video	JAN-WILLEM VAN DE WAERDT@PHILIP	SDRAM ADDRESS MAPPING OPTIMIZED FOR TWO-DIMENSIONAL / US	2079784	2004/03/16
US020453	03-EE Devices	JAN-WILLEM LOEKE@PHILIPS.COM	DUPLEXER WITH BAW AND TUNING INDUCTORS	2079784	2004/03/16
US020454	04-Audio/Video	RMEUSEN@PHILIPS.COM	METHOD FOR A RECEIVER	2079808.8	2004/03/16
US020455	06-Connectivity	LUKAS LEYTEN@PHILIPS.COM	TRAVELLING-WAVE AMPLIFIER	60427413	1588178.A
US020456	09-Automotive	BILL DONALDSON@PHILIPS.COM	INTEGRATED FLOATING POWER SUPPLY WITH REDUCED ELECTR US	60427410	2004/02/26
US020457	08-Automotive	BILL DONALDSON@PHILIPS.COM	INTEGRATED FLOATING POWER SUPPLY WITH LOGIC LEVEL CON US	60427422	2004/02/26
US020458	08-Automotive	KLAAS-JAN DE LANGEN@PHILIPS.COM	COMPACT LEVEL-SHIFTING CIRCUIT WITH IMPROVED EME FOR CI US	60427661	2004/02/26
US020459	08-Automotive	KLAAS-JAN DE LANGEN@PHILIPS.COM	FAST TURN-ON BUS TRANSMITTER WITH CONTROLLED SLEW RAI US	60427633	2004/02/26
US020460	08-Automotive	EDMOND TOY@PHILIPS.COM	SYSTEM FOR DATA COMMUNICATION USING A NON-TERMINATING US	60427197	2004/02/26
US020461	08-Automotive	BILL DONALDSON@PHILIPS.COM	PRECHARGE AND PROTECTION CIRCUIT FOR HIGH POWER CHAR US	2102595.2	2004/02/26
US020462	08-Automotive	CHARLES RAZZEL@PHILIPS.COM	AUTOMATIC GAIN CONTROL USING SIGNAL AND INTERFERENCE F US	10253164.6	2004/02/26
US020463	04-Audio/Video	FRANCESCO MAIONE@PHILIPS.COM	ADAPTIVE HYPERESIS FOR REDUCED SWING SIGNALING CIRCUIT EP	2102594.5	2004/02/26
US020464	04-Audio/Video	KARL-RAGMAR RIEMSCHEIDER@PHILIP	WIRELESS MEASURING AND CHARGE CONTROL SYSTEM FOR THE DE	2102594.5	2004/02/26
US020465	04-Audio/Video	FRANCESCO MAIONE@PHILIPS.COM	POWER CONVERTER	2102594.5	2004/02/26
FR020114	09-Discrete and Multimed	KARL-RAGMAR RIEMSCHEIDER@PHILIP	REDUCING VOIDDING PROBLEM IN CU-DAMASCENE	2102594.5	2004/02/26
FR020115	01-CMOS and embedded p	THOMAS DUERBAUM@PHILIPS.COM	SEMICONDUCTOR DEVICE CHANNEL TERMINATION	226402.6	2004/02/26
GB020162	01-CMOS and embedded p	ROYCE L KOWIS@PHILIPS.COM	MODULE WITH A FIBRE-REINFORCED RESIN FOIL	2102597.2	2004/02/26
AT020284	12-nov-02	REINHARD ROGY@PHILIPS.COM	FOLDED-FLEX BONDWIRE-LESS MULTICHIP POWER PACKAGE	10252168.2	2004/02/26
US020466	09-Identification	SHATIL HAQUE@PHILIPS.COM	NOVEL DISPLAY WITH ENHANCED IMAGE QUALITY FOR LOW POW DE	10252165.4	2004/02/26
DE020251	04-Audio/Video	KNUT KIESCHNICK@PHILIPS.COM	SYSTEMCHIP FOR CALIBRATION OF INTERNAL INTERFACE	2102553.1	2004/02/26
SG020030	08-Automotive	MATTHIAS MUTH@PHILIPS.COM	A TUNER WITH GROUNDED OUTER WALL	2102552.3	2004/02/26
SG020031	08-Automotive	KWONG KAM CHOOK@PHILIPS.COM	FLY TUNER FOR TV TUNERS	2102552.3	2004/02/26
AT020263	08-nov-02	HEIMO SCHEUCHER@PHILIPS.COM	PHOTO DETECTOR	2079655.1	2004/02/26
US020094	07-Identification	HANS VOORMAN@UNKNOWN.ORG	IMPROVE REAL-TIME-CLOCK PRECISION ON MOBILE PHONES	2102547.3	2004/02/26
US020095	07-Identification	HANS VOORMAN@UNKNOWN.ORG	INSTANT FILE SAVE ON MRAM	2102547.3	2004/02/26
US020096	07-Identification	ROBERT JOCHENSEN@PHILIPS.COM	POWER CONVERTER WITH PEAK CLAMP OVER-VOLTAGE PROTECT EP	225812.7	2004/02/26
US020097	07-Identification	ARLAN STRUKER@PHILIPS.COM	TRENCHMOS WITH NIP HALO	2079660.9	2004/02/26
US020098	07-Identification	MIHAI SANDULEANU@PHILIPS.COM	PIL WITH UNBALANCED QUADRICORRELATOR	10251787.8	2004/02/26
US020099	07-Identification	MIHAI SANDULEANU@PHILIPS.COM	PIL WITH BALANCED QUADRICORRELATOR	10251787.8	2004/02/26
US020100	07-Identification	GERALD ECKERT, 27018@PF.UNKNOW.N	FINGERPRINT VERIFICATION	10251787.8	2004/02/26
US020101	07-Identification	SIFEN LIO, 17623@PF.UNKNOW.N	SINGLE SELF-BIASED CASCADE AMPLIFIER CIRCUIT	10251787.8	2004/02/26
US020102	07-Identification	PIERRE DE GREEF@PHILIPS.COM	LINE SCANNING IN A DISPLAY	2102532.7	2004/02/26
US020103	07-Identification	GERHARD RUNZE@PHILIPS.COM	EFFICIENT IMPLEMENTATION OF DIGITAL FIXED-POINT FILTER	102501555.1	2004/02/26
US020104	07-Identification	ERIC DESBONNETS@PHILIPS.COM	PIL DEMODULATOR WITH GAIN CONTROL	2252708.1	2004/02/26
US020105	07-Identification	GILSEPPE MOKTALBANO@PHILIPS.COM	CHANNEL ESTIMATION FOR LIMITS RANGE RECEIVER	3290294.2	2004/02/26
US020106	07-Identification	SAUL DOOLEY@PHILIPS.COM	GPS RECEIVER WITH DUAL ANTENNAE	2079613.8	2004/02/26
US020107	07-Identification	ANDRIES HEKSTRA@PHILIPS.COM	TRELLIS BASED RECEIVER	2079647.2	2004/02/26
US020108	07-Identification	JORDAN VESHTAROV@PHILIPS.COM	INTEGRATED TEMPERATURE COMPENSATED BIAS CIRCUIT	10282450	2004/02/26
US020109	07-Identification	BENEDIKT MIKA@PHILIPS.COM	NOISE REDUCTION FOR PHASE LOCKED LOOP	10282450	2004/02/26
US020110	07-Identification	XUEJUN FAN@PHILIPS.COM	JUNCTION TEMPERATURE MEASUREMENT OF SEMICONDUCTOR US	2079526.6	2004/02/26
US020111	07-Identification	GULLAUME DE CREMOUX@PHILIPS.COI	8-DIRECTIONAL DOUBLE NMOS SWITCH	10281827	2004/02/26
US020112	07-Identification	A.M.M. POELEN@PHILIPS.COM	IMPROVE PICTURE AESTHETICS during switch-on	2102460.6	2004/02/26
US020113	07-Identification	CHRIS SPEERS@PHILIPS.COM	CHARGE SHARING IN THE ROW DRIVER OF LOW POWER AMLC	1024688.6	2004/02/26
US020114	07-Identification	WOLFGANG FURTER@PHILIPS.COM	METHOD AND DEVICE FOR GENERATING A CLOCK SIGNAL	2079466.5	2004/02/26
US020115	07-Identification	HARNAK DE GROOT@PHILIPS.COM	APPARATUS AND MODULE FOR THE EXCHANGE OF DATA SIGNALS EP	10280749	2004/02/26
US020116	07-Identification	YIFENG ZHANG@PHILIPS.COM	SINGLE OSCILLATOR DSSS AND OFDM RADIO RECEIVER	10280853	2004/02/26
US020117	07-Identification	YIFENG ZHANG@PHILIPS.COM	QUADRATURE MISMATCH COMPENSATION	60454504	2004/02/26
US020118	07-Identification	ALI FOTOWAT-AHMADY, 26346@PF.UNKN	BAND EDGE GAMPHASE COMPENSATION FOR BROADBAND OPTI US	2079476.4	2004/02/26
US020119	07-Identification	FRANS WIDERSHOVEN@PHILIPS.COM	SELF-ALIGNED 2-BITS "DOUBLE POLY-CMP" FLASH MEMORY CELL EP	10279703	2004/02/26
US020120	07-Identification	FRANS WIDERSHOVEN@PHILIPS.COM	THERMAL-CONDUCTIVE SUBSTRATE PACKAGE	2079467.3	2004/02/26
US020121	07-Identification	J.T.M.VAN BEEK@PHILIPS.COM	METHOD OF MANUFACTURING OF MEMS	2079467.3	2004/02/26
US020122	07-Identification	IGOR BLEBNOV@PHILIPS.COM	PREDISTORTION LINEARIZER	2079392.3	2004/02/26
US020123	07-Identification	ONNO SEERENBERG@PHILIPS.COM	SUB-PICTURES MAPPED ON MACROBLOCK GRID	2079392.3	2004/02/26

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STMicroelectronics

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Pub No	App No	Inventor	Priority Date	IPC Class	Title	IPC Class	Pub Date	App Date	IPC Class	App Date
NL021021	21-14432	IMEC	2004/02/27	06-Connectivity	FAILSAFE METHOD AND CIRCUIT	EP	20783654	US6772984	2004/02/27	2004/02/27
NL021020	18-04102	IMEC	2003/08/27	04-Audio/Video	FREQUENCY INDEPENDENT VOLTAGE DIVIDER	EP	20793336	US6674378	2003/08/27	2003/08/27
NL021018	18-04102	IMEC	2003/08/27	08-Automotive	DATA PROCESSING APPARATUS IDENTIFYING A CLOCK FREQUENCY	EP	20793341	US6661058	2003/08/27	2003/08/27
NL021019	17-08102	IMEC	2003/03/10	08-Automotive	DATA PROCESSING APPARATUS THAT PROCESSES INCOMING BR	EP	20793336	US6661058	2003/03/10	2003/03/10
NL021013	17-08102	IMEC	2003/03/10	08-Connectivity	AMPLITUDE DETECTOR	EP	20793341	US6661058	2003/03/10	2003/03/10
NL020986	14-04102	IMEC	2002/08/15	01-CMOS and embedded p	TRANSMIT AND RECEIVE ANTENNA SWITCH	EP	20793341	US6661058	2002/08/15	2002/08/15
NL020983	11-04102	IMEC	2001/08/10	01-CMOS and embedded p	METHOD AND APPARATUS FOR FORMING EPIAXIAL LAYERS	EP	20793341	US6661058	2001/08/10	2001/08/10
NL020975	11-04102	IMEC	2001/08/10	04-Audio/Video	ELECTRIC DEVICE COMPRISING PHASE CHANGE MATERIAL	EP	20793341	US6661058	2001/08/10	2001/08/10
US020267	11-04102	IMEC	2001/08/10	07-Identification	PARALLELIZATION OF INSTRUCTION DEPENDS ON THEIR ADDRESS	US	20793341	US6661058	2001/08/10	2001/08/10
US020365	10-04102	IMEC	2001/08/10	06-Connectivity	ENCRYPTION CIRCUIT ARRANGEMENT AND METHOD THEREOF	US	20793341	US6661058	2001/08/10	2001/08/10
NL020853	7-04102	IMEC	2000/08/10	02-Packaging & Testing	DMA CONTROLLER FOR USB AND LIKE APPLICATIONS	US	20793341	US6661058	2000/08/10	2000/08/10
NL020844	4-04102	IMEC	2000/08/10	04-Audio/Video	FETCH IN L2 SUPPRESSED IF WRITEMASK INDICATES ALL DATA	US	20793341	US6661058	2000/08/10	2000/08/10
US020236	4-04102	IMEC	2000/08/10	09-Discrete and Multimar	NEAR ZERO QED TRENCHEMOS	US	20793341	US6661058	2000/08/10	2000/08/10
US020227	3-04102	IMEC	2000/08/10	09-Discrete and Multimar	TRENCHMOS - DMOS HYBRID	US	20793341	US6661058	2000/08/10	2000/08/10
BE020028	3-04102	IMEC	2000/08/10	01-CMOS and embedded p	METHOD AND APPARATUS FOR FORMING EPIAXIAL LAYERS	EP	20793341	US6661058	2000/08/10	2000/08/10
BE020027	3-04102	IMEC	2000/08/10	01-CMOS and embedded p	MAKING STI REGIONS USING AN AUXILIARY LAYER OF SIGE	EP	20793341	US6661058	2000/08/10	2000/08/10
NL020964	3-04102	IMEC	2000/08/10	05-Mobile Communication	ADAPTIVE CONTROL OF DUCESCENT CURRENT FOR POWER AMPL	US	20793341	US6661058	2000/08/10	2000/08/10
US020364	2-04102	IMEC	2000/08/10	08-Connectivity	CAPACITOR COUPLED DYNAMIC BIAS BOOSTING CIRCUIT FOR A	US	20793341	US6661058	2000/08/10	2000/08/10
US020362	2-04102	IMEC	2000/08/10	08-Connectivity	LOW LATENCY RADIOBASEBAND INTERFACE PROTOCOL	US	20793341	US6661058	2000/08/10	2000/08/10
US020366	20-04102	IMEC	2000/08/10	02-Packaging & Testing	METHOD OF PACKAGING HEAT-SPREADER INTEGRATED...	US	20793341	US6661058	2000/08/10	2000/08/10
US020353	20-04102	IMEC	2000/08/10	02-Packaging & Testing	DIGITAL CONTROLLER WITH TWO CONTROLLED PATHS	US	20793341	US6661058	2000/08/10	2000/08/10
US020356	20-04102	IMEC	2000/08/10	09-Discrete and Multimar	LATERAL INSULATED GATE BIPOLAR PIMOS DEVICE	US	20793341	US6661058	2000/08/10	2000/08/10
GB020161	28-04102	IMEC	2000/08/10	02-Packaging & Testing	RF IC TESTING	GB	2225563	US6674378	2000/08/10	2000/08/10
US020345	28-04102	IMEC	2000/08/10	09-Discrete and Multimar	COMPENSATED SENSEMOS	US	2225563	US6674378	2000/08/10	2000/08/10
US020320	26-04102	IMEC	2000/08/10	05-Mobile Communication	LINEAR HALF-RATE PHASE DETECTOR FOR CLOCK RECOVERY & I	US	2225563	US6674378	2000/08/10	2000/08/10
GB020092	25-04102	IMEC	2000/08/10	02-Packaging & Testing	VOLTAGE-CONTROLLED OSCILLATOR PRESETTING CIRCUIT	GB	2225563	US6674378	2000/08/10	2000/08/10
FR020101	25-04102	IMEC	2000/08/10	03-RF Devices	INPUT POWER CALCULATION IN TUNERS (PSSI)	FR	2111838	US6674378	2000/08/10	2000/08/10
US020341	25-04102	IMEC	2000/08/10	06-Connectivity	RF ELECTRO-SWITCH	US	2111838	US6674378	2000/08/10	2000/08/10
SG030011	24-04102	IMEC	2000/08/10	06-Connectivity	SMART CARD INTERF. EMBEDDED IN THE CONNECT.	EP	20786964	US6674378	2000/08/10	2000/08/10
US020342	24-04102	IMEC	2000/08/10	06-Connectivity	HEAD END HAVING CHANNEL PRESELECTION	US	20786964	US6674378	2000/08/10	2000/08/10
NL020875	20-04102	IMEC	2000/08/10	02-Packaging & Testing	SOFTWARE USING VIRTUAL HAL TECHNOLOGY	WO	50300220000239	US6674378	2000/08/10	2000/08/10
NL020874	20-04102	IMEC	2000/08/10	04-Audio/Video	USB HOST WITH QTG PIPE	WO	50300220000239	US6674378	2000/08/10	2000/08/10
NL020909	20-04102	IMEC	2000/08/10	04-Audio/Video	IDDD MONITOR	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020908	20-04102	IMEC	2000/08/10	04-Audio/Video	DATA PROCESSING SCHEME BASED ON DELAY FORECAST	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020943	20-04102	IMEC	2000/08/10	04-Audio/Video	LC OSCILLATOR WITH CROSS-COUPLED PIMOS	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020904	20-04102	IMEC	2000/08/10	09-Discrete and Multimar	IMPROVED QUADRATURE REDUCTION	EP	20786906	US6674378	2000/08/10	2000/08/10
US020326	18-04102	IMEC	2000/08/10	04-Audio/Video	ADAPTIVE LEADING EDGE BLANKING CIRCUIT	US	22183716	US6674378	2000/08/10	2000/08/10
FR020086	18-04102	IMEC	2000/08/10	04-Audio/Video	BLOCK-BASED ROTATION OF ARBITRARY-SHAPED IMAGES	EP	20786906	US6674378	2000/08/10	2000/08/10
US020323	17-04102	IMEC	2000/08/10	05-Mobile Communication	PARALLEL SISO WITH NEXT ITERATION INITIALISATION	US	20786906	US6674378	2000/08/10	2000/08/10
NL020885	17-04102	IMEC	2000/08/10	05-Mobile Communication	SYSTEM METHOD FOR FULLY SYNTHESIZABLE SUPERRELIABLE	US	60471589	US6674378	2000/08/10	2000/08/10
NL020886	17-04102	IMEC	2000/08/10	05-Mobile Communication	PRESERVING LINEARITY OF A POWER AMPLIFIER	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020885	17-04102	IMEC	2000/08/10	05-Mobile Communication	DDQ TEST TECHNIQUE	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020886	17-04102	IMEC	2000/08/10	05-Mobile Communication	REDUCED CHIP TESTING SCHEME AT WATER LEVEL	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020887	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020888	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020889	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020890	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020891	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020892	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020893	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020894	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020895	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020896	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020897	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020898	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020899	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020900	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020901	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020902	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020903	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020904	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020905	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020906	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020907	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020908	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020909	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020910	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020911	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020912	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020913	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020914	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020915	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020916	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020917	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020918	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020919	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020920	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020921	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020922	17-04102	IMEC	2000/08/10	05-Mobile Communication	METHOD FOR ANALYSING AND MODIFICATION OF A FOOTPRINT	EP	20786906	US6674378	2000/08/10	2000/08/10
NL020923	17-04102	IM								



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No.	Pub. No.	IPC Class.	IPC Class. Description	Inventor(s)	IPC Class.	IPC Class. Description	Date	Pub. No.	IPC Class.	IPC Class. Description	Inventor(s)	Date	Pub. No.
NL020790	30-aug-02	02-Packaging & Testing	IC WITH ID CODES	LEON VAN DE LOGT@PHILIPS.COM	EP	2078568,9	2003021025A2	NL020783	30-aug-02	04-Audio/Video	VERSION PROGRAMMABLE CIRCUIT MODULE	EP	2078604,2
DE020353	30-aug-02	06-Connectivity	VERFAHREN ZUR IDENTIFIZIERUNG VON PROGRAMMIERBAREN ...	JENS DAVID@PHILIPS.COM	DE	10240770,3	2003022298A2	IT020025	30-aug-02	06-Connectivity	DECISION FEEDBACK EQUALISER	EP	2078603,4
NL020312	30-aug-02	08-Automotive	PL	REIVCO,VAN DE BEEK@PHILIPS.COM	EP	2078581,2	2003022157A1	US020033	30-aug-02	09-Discretes and Multimeters	TRANSITION DETECTION AT INPUT OF INTEGRATED CIRCUIT DEVI	EP	2078578,8
EP020097	30-aug-02	04-Audio/Video	PROSS-COMPENSATED CMOS CURRENT REFERENCE	PAUL ANDREWS@PHILIPS.COM	EP	102321897	2003021539A2	FR020069	30-aug-02	05-Mobile Communication	ELECTRONIC DEVICE WITH DATA STORAGE DEVICE	EP	2078548,1
FR020069	30-aug-02	05-Mobile Communication	RECONFIGURABLE ELECTRONIC DEVICE WITH DUAL PORT MEMO	KASIA LEUTEN-HONKAW@PHILIPS.COM	EP	2078548,9	2003021365A2	EP020184	30-aug-02	02-Packaging & Testing	DUAL TIP REVERSIBLE BONDING TOOL	EP	2102240,5
EP020184	30-aug-02	02-Packaging & Testing	DUAL TIP REVERSIBLE BONDING TOOL	RAMIL N.VASQUEZ@PHILIPS.COM	EP	2102240,5	2003021659A1	FR020198	30-aug-02	07-Identification	COMBINING AN INTERNAL AND EXTERNAL CHARGE PUMP	EP	2102227,2
FR020198	30-aug-02	07-Identification	COMBINING AN INTERNAL AND EXTERNAL CHARGE PUMP	FRIEDBERT RIEDEL@PHILIPS.COM	EP	2102227,2	2003021659A1	FR020199	30-aug-02	07-Identification	METHOD FOR SHAPING THE IQ SIGNALS IN WIRELESS TDMA TRA	EP	2102229,6
FR020199	30-aug-02	07-Identification	METHOD FOR SHAPING THE IQ SIGNALS IN WIRELESS TDMA TRA	PHILIPPE GENTRUC@PHILIPS.COM	EP	2102229,6	2003021659A1	FR020202	30-aug-02	05-Mobile Communication	USING RTSP MUTE FOR FILE SWITCHING	EP	2102331,4
FR020202	30-aug-02	05-Mobile Communication	USING RTSP MUTE FOR FILE SWITCHING	MANISH GARG@PHILIPS.COM	EP	2102331,4	2003021659A1	FR020203	30-aug-02	07-Identification	TAG WITH A PRESENCE-CONFIRMATION SIGNAL GENERATOR	EP	2102393,9
FR020203	30-aug-02	07-Identification	TAG WITH A PRESENCE-CONFIRMATION SIGNAL GENERATOR	STEFFEN SCHOLZ@SDR.DE	DE	102328343,5	2003021659A1	FR020204	30-aug-02	07-Identification	QUALITY ESTIMATION OF FINGERPRINT IMAGES	EP	2102347,7
FR020204	30-aug-02	07-Identification	QUALITY ESTIMATION OF FINGERPRINT IMAGES	STEFFEN HAHN 071623@PHILIPS.COM	DE	102328343,5	2003021659A1	FR020205	30-aug-02	07-Identification	METHOD FOR BINARIZATION OF FINGERPRINT IMAGES	EP	2102261,58
FR020205	30-aug-02	07-Identification	METHOD FOR BINARIZATION OF FINGERPRINT IMAGES	JAN-WILLEM VAN DE WAERDT@PHILIPS.COM	EP	2102261,58	2003021659A1	FR020206	30-aug-02	04-Audio/Video	COMMON ACCESS DELAY LINE	FR	21045,2
FR020206	30-aug-02	04-Audio/Video	COMMON ACCESS DELAY LINE	EMMANUEL ARSCHVILI@PHILIPS.COM	FR	21045,2	2003021659A1	FR020207	30-aug-02	01-CMOS and embedded pr	ENTROPY ESTIMATION AND DECIMATION FOR IMPROVING THE ...	US	10232489,2
FR020207	30-aug-02	01-CMOS and embedded pr	ENTROPY ESTIMATION AND DECIMATION FOR IMPROVING THE ...	LASZLO HARAS 23011@PF.UNKNOW.NL	WO	10232489,2	2003021659A1	FR020208	30-aug-02	04-Audio/Video	FERROELECTRIC-SEMICONDUCTOR DEVICE	US	10232489,2
FR020208	30-aug-02	04-Audio/Video	FERROELECTRIC-SEMICONDUCTOR DEVICE	PAUL VAN DER SLUIS@PHILIPS.COM	EP	2078419,5	2003021659A1	FR020209	30-aug-02	05-Mobile Communication	OPERATIONAL AMPLIFIER	EP	2078381,7
FR020209	30-aug-02	05-Mobile Communication	OPERATIONAL AMPLIFIER	ZHENHUA WANG@PHILIPS.COM	EP	2078381,7	2003021659A1	FR020210	30-aug-02	05-Mobile Communication	HIGH POWER DOHERTY AMPLIFIER	EP	2078366,8
FR020210	30-aug-02	05-Mobile Communication	HIGH POWER DOHERTY AMPLIFIER	IGOR BLEDNOW@PHILIPS.COM	EP	2078366,8	2003021659A1	FR020211	30-aug-02	07-Identification	VERFAHREN ZUM ZWISCHENSPEICHERN VON REGISTERINHALTE/DE	EP	2078421,1
FR020211	30-aug-02	07-Identification	VERFAHREN ZUM ZWISCHENSPEICHERN VON REGISTERINHALTE/DE	WOLFRAM DRESCHER@PHILIPS.COM	EP	2078421,1	2003021659A1	FR020212	30-aug-02	04-Audio/Video	TEST CONTROLLER WITH STATE HOLDING LOGIC	EP	2078378,6
FR020212	30-aug-02	04-Audio/Video	TEST CONTROLLER WITH STATE HOLDING LOGIC	ARNIE REESSELS@PHILIPS.COM	EP	2078378,6	2003021659A1	FR020213	30-aug-02	07-Identification	VECTORIAL REPRESENTATION OF A FINGERPRINT IMAGE	EP	2078289,4
FR020213	30-aug-02	07-Identification	VECTORIAL REPRESENTATION OF A FINGERPRINT IMAGE	TOM WAAYERS@PHILIPS.COM	EP	2078289,4	2003021659A1	FR020214	30-aug-02	05-Mobile Communication	INSTRUCTION CACHE WAY PREDICTION FOR JUMP TARGETS	US	10219074
FR020214	30-aug-02	05-Mobile Communication	INSTRUCTION CACHE WAY PREDICTION FOR JUMP TARGETS	JAN-WILLEM VAN DE WAERDT@PHILIPS.COM	US	10219074	2003021659A1	FR020215	30-aug-02	05-Mobile Communication	RADIO FREQUENCY VARIABLE AMPLIFIER WITH IMPROVED CONT	US	60403293
FR020215	30-aug-02	05-Mobile Communication	RADIO FREQUENCY VARIABLE AMPLIFIER WITH IMPROVED CONT	BILL REDMAN-WHITE@PHILIPS.COM	EP	2071948,5	2003021659A1	FR020216	30-aug-02	02-Packaging & Testing	HIGH-DATA RATE WITH IP/SPAS CONNECTION	EP	2071948,5
FR020216	30-aug-02	02-Packaging & Testing	HIGH-DATA RATE WITH IP/SPAS CONNECTION	HAROLD FISCHER 23759@PF.UNKNOW.NL	EP	2071948,5	2003021659A1	FR020217	30-aug-02	05-Mobile Communication	DEVICE WITH ADAPTIVE THRESHOLD CONTROL	EP	2071948,5
FR020217	30-aug-02	05-Mobile Communication	DEVICE WITH ADAPTIVE THRESHOLD CONTROL	JOSE PINEDA DE GYZEZ@PHILIPS.COM	EP	2071948,5	2003021659A1	FR020218	30-aug-02	05-Mobile Communication	TWO SPURIOUS-REJECT MIXERS WITH LOS IN QUADRATURE	EP	2071948,5
FR020218	30-aug-02	05-Mobile Communication	TWO SPURIOUS-REJECT MIXERS WITH LOS IN QUADRATURE	ZHENHUA WANG@PHILIPS.COM	EP	2071948,5	2003021659A1	FR020219	30-aug-02	02-Packaging & Testing	LOW ENAC FIRDAC	EP	10235338,5
FR020219	30-aug-02	02-Packaging & Testing	LOW ENAC FIRDAC	MATTHIAS LOCHER@PHILIPS.COM	EP	10235338,5	2003021659A1	FR020220	30-aug-02	05-Mobile Communication	VOLTAGE CONTROLLED OSCILLATOR	EP	2078287,6
FR020220	30-aug-02	05-Mobile Communication	VOLTAGE CONTROLLED OSCILLATOR	EDMUND D. HEIDEN@PHILIPS.COM	EP	2078287,6	2003021659A1	FR020221	30-aug-02	02-Packaging & Testing	IMPROVED SAWING PROCESS	EP	2078215,7
FR020221	30-aug-02	02-Packaging & Testing	IMPROVED SAWING PROCESS	F.H.M. T.VELD@PHILIPS.COM	EP	2078215,7	2003021659A1	FR020222	30-aug-02	05-Mobile Communication	SELECTIVE INSTRUCTION FETCH	EP	2078215,7
FR020222	30-aug-02	05-Mobile Communication	SELECTIVE INSTRUCTION FETCH	KAROLIN DE BAERE@PHILIPS.COM	EP	2078215,7	2003021659A1	FR020223	30-aug-02	05-Mobile Communication	HIGH IMPEDANCE SAW/SAW FILTERED BAND CONNECTIONS	GB	2017272,2
FR020223	30-aug-02	05-Mobile Communication	HIGH IMPEDANCE SAW/SAW FILTERED BAND CONNECTIONS	KEVIN BOYLE@PHILIPS.COM	EP	2017272,2	2003021659A1	FR020224	30-aug-02	04-Audio/Video	MODIFIED OSCILLATOR TANK CIRCUIT FOR SWITCHING BETWEEN PHASE DETECTOR	EP	2078157,1
FR020224	30-aug-02	04-Audio/Video	MODIFIED OSCILLATOR TANK CIRCUIT FOR SWITCHING BETWEEN PHASE DETECTOR	ALAN YEO CHIN LEONG@PHILIPS.COM	EP	2078157,1	2003021659A1	FR020225	30-aug-02	05-Mobile Communication	FRONT-END DIVERSITY	FR	20974,4
FR020225	30-aug-02	05-Mobile Communication	FRONT-END DIVERSITY	MIHAL SANDULEANU@PHILIPS.COM	FR	20974,4	2003021659A1	FR020226	30-aug-02	06-Correctivity	VLM FUNCTIONAL UNIT(FU)	FR	20974,4
FR020226	30-aug-02	06-Correctivity	VLM FUNCTIONAL UNIT(FU)	ALEXANDRE HUMBERT@PHILIPS.COM	FR	20974,4	2003021659A1	FR020227	30-aug-02	06-Correctivity	LOUDSPEAKER WITH A SUBSEQUENTLY ATTACHED AMPLIFIER	EP	2102077,1
FR020227	30-aug-02	06-Correctivity	LOUDSPEAKER WITH A SUBSEQUENTLY ATTACHED AMPLIFIER	MARC DURANTON@PHILIPS.COM	EP	2102077,1	2003021659A1	FR020228	30-aug-02	09-Discretes and Multimeters	INL ERROR COMPENSTION IN DAC	FR	20974,8
FR020228	30-aug-02	09-Discretes and Multimeters	INL ERROR COMPENSTION IN DAC	ERICH KLEIN@PHILIPS.COM	FR	20974,8	2003021659A1	FR020229	30-aug-02	07-Identification	TAG-IC WITH TWO DIFFERENT SUPPLY VOLTAGES	EP	2102073,3
FR020229	30-aug-02	07-Identification	TAG-IC WITH TWO DIFFERENT SUPPLY VOLTAGES	ROELAND HELINA@PHILIPS.COM	EP	2102073,3	2003021659A1	FR020230	30-aug-02	04-Audio/Video	POWER-ON-RESET DEPENDS ON READ/WRITE MODES	EP	2078074,8
FR020230	30-aug-02	04-Audio/Video	POWER-ON-RESET DEPENDS ON READ/WRITE MODES	BENOIT SUYOT@PHILIPS.COM	EP	2078074,8	2003021659A1	FR020231	30-aug-02	05-Mobile Communication	VIDEO DATA FILTERING ARRANGEMENT AND METHOD	US	10240523,1
FR020231	30-aug-02	05-Mobile Communication	VIDEO DATA FILTERING ARRANGEMENT AND METHOD	ALAN TERRY@PHILIPS.COM	US	10240523,1	2003021659A1	FR020232	30-aug-02	04-Audio/Video	FLEXIBLE VECTOR MEMORY	EP	2078074,8
FR020232	30-aug-02	04-Audio/Video	FLEXIBLE VECTOR MEMORY	ROBERT KOFLER@PHILIPS.COM	EP	2078074,8	2003021659A1	FR020233	30-aug-02	05-Mobile Communication	PLANAR INDUCTANCE WITH REDUCED MAGNETIC FIELD	DE	10233980,5
FR020233	30-aug-02	05-Mobile Communication	PLANAR INDUCTANCE WITH REDUCED MAGNETIC FIELD	TIM LANGE@PHILIPS.COM	EP	10233980,5	2003021659A1	FR020234	30-aug-02	07-Identification	ANTICOLLISION METHOD THAT MARKS THE TRAE SLOTS	EP	2102057,3
FR020234	30-aug-02	07-Identification	ANTICOLLISION METHOD THAT MARKS THE TRAE SLOTS	JOSEF EINZINGER@PHILIPS.COM	EP	2102057,3	2003021659A1	FR020235	30-aug-02	04-Audio/Video	VIDEO DATA FILTERING ARRANGEMENT AND METHOD	US	10240523,1
FR020235	30-aug-02	04-Audio/Video	VIDEO DATA FILTERING ARRANGEMENT AND METHOD	CHRISTIAN SCHERBON@PHILIPS.COM	US	10240523,1	2003021659A1	FR020236	30-aug-02	05-Mobile Communication	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS	FR	2093,61
FR020236	30-aug-02	05-Mobile Communication	OFFSET CORRECTION FOR DOWN-CONVERSION MIXERS	LASZLO HARAS 23011@PF.UNKNOW.NL	US	10240523,1	2003021659A1	FR020237	30-aug-02	05-Mobile Communication	IMPROVED INTER-PROCESSOR COMMUNICATION SYSTEM	EP	2077894,7
FR020237	30-aug-02	05-Mobile Communication	IMPROVED INTER-PROCESSOR COMMUNICATION SYSTEM	IVAN MIRO PANADES 29342@PF.PHILIPS.COM	EP	2077894,7	2003021659A1	FR020238	30-aug-02	05-Mobile Communication	COMPACT INDUCTOR FOR MULTIPLE RESONATORS IN MULTIBAND EP	GB	216897,9
FR020238	30-aug-02	05-Mobile Communication	COMPACT INDUCTOR FOR MULTIPLE RESONATORS IN MULTIBAND EP	BILL REDMAN-WHITE@PHILIPS.COM	EP	216897,9	2003021659A1	FR020239	30-aug-02	05-Mobile Communication	SWITCHED-CURRENT PIPELINE ADC	EP	2077930,2
FR020239	30-aug-02	05-Mobile Communication	SWITCHED-CURRENT PIPELINE ADC	STEPHAN M.KOCH@PHILIPS.COM	EP	2077930,2	2003021659A1	FR020240	30-aug-02	04-Audio/Video	CAMERA MODULE VG	EP	2284190,1
FR020240	30-aug-02	04-Audio/Video	CAMERA MODULE VG	GUIDO DOLMANS@PHILIPS.COM	EP	2284190,1	2003021659A1	FR020241	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	EP	2077858,9
FR020241	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	JOHN HUGHES@PHILIPS.COM	EP	2077858,9	2003021659A1	FR020242	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	EP	2077858,9
FR020242	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	LEO VAN GEMERT@PHILIPS.COM	EP	2077858,9	2003021659A1	FR020243	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	EP	2077858,9
FR020243	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	ARNAUD BURGE@PHILIPS.COM	EP	2077858,9	2003021659A1	FR020244	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	EP	2077858,9
FR020244	30-aug-02	05-Mobile Communication	RE-ORGANIZED BITSTREAM	GUILLAUME DE CREMOLUX@PHILIPS.COM	EP	2077858,9	2003021659A1						

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Table with columns for patent number, classification, title, inventor, applicant, filing date, and priority date. Includes entries like 'US020270', 'NL020647', 'DE020165', etc.

NL020472	5-jun-02	03-RF Devices	MARC VAN DELDEN@PHILIPS.COM	MEM-SWITCH AND TUNABLE INDUCTOR	2077202.6	2003050114-A1	US042203
DE020261	5-jun-02	06-Connectivity	HELGE BETZINGER@PHILIPS.COM	VERFAHREN ZUM CODIEREN/DECODIEREN VON VLIW-CACHED-BE	10225009.5	2003050921-A2	
FR020052	4-jun-02	05-Mobile Communication	FLORENT SELVES 26104@PF.PHILIPS.COM	DE SURCHERCHER FLASH WITH CMOS SENSOR OPERATIONS IN ROLLING	206858	2003050927-A1	
NL020451	4-jun-02	05-Mobile Communication	SIGI HALBERSTADT@PHILIPS.COM	INDUCTOR CURRENT EMULATION IN BUCK CONVERTER	2077196	2003051012-A1	
AT020049	4-jun-02	07-Identification	HANS KALDOR@PHILIPS.COM	ROLL-BACK METHOD FOR A SMART CARD	20100684.8	2003051026-A1	
DE020137	4-jun-02	08-Discretes and Multimedia	SIGI ARNOOLD@PHILIPS.COM	SOLIC WITH TWO FIELD PLATES	18924615.7	2003051031-A2	
NL020432	4-jun-02	09-Discretes and Multimedia	WOLFGANG SCHNITT@PHILIPS.COM	DC-DC WITH ACCURATE LOAD POSITIONING BASED ON AVERAGE	20770662	2003051031-A1	
NL020458	31-mei-02	01-CMOS and embedded p	GRIEL HORR 14328@PF.UNKNOW.N.ORG	DENSE ARRAY STRUCTURE FOR NON-VOLATILE SEMICONDUCTOR	2077155.6	2003051031-A1	
DE020136	31-mei-02	05-Mobile Communication	ROLF BECKER@PHILIPS.COM	DIGITAL OUTPUT STAGE COMPLIANT WITH HIGH VOLTAGE SWING	2013059.8	2003051031-A1	
GB020082	31-mei-02	09-Discretes and Multimedia	MICHAEL STEVEN PEAKE@PHILIPS.COM	NEW BREED OF TRENCHFET WITH NEAR-VBD BIASED BOTTOM	212564.9	2003051031-A1	
GB020021	31-mei-02	09-Discretes and Multimedia	STEVEN PEAKE@PHILIPS.COM	TRENCHFET WITH TRENCHFET FIELD PLATE BELOW GATE	228909	2003051031-A1	
NL020440	29-mei-02	01-CMOS and embedded p	PETER MAGREME@PHILIPS.COM	IMPROVED RSI TRENCHFET STRUCTURE AND PROCESS	303162.2	2003051031-A1	
NL020409	28-mei-02	08-Automotive	HAN SCHUURMANS@PHILIPS.COM	METHOD TO FABRICATE SISE HETEROJUNCTION BIPOLAR TRANS	2077119.7	2003051031-A1	
GB020082	28-mei-02	09-Discretes and Multimedia	JOHN HUGHES@PHILIPS.COM	SIGMA DELTA A/D CONVERTER WITH PSEUDO FLASH CONVERTER	2077109.3	2003051031-A1	
NL020414	28-mei-02	04-Audio/Video	VICTOR KAL@PHILIPS.COM	SWITCHED-CURRENT COMPARATOR WITHOUT KICKBACK	212327.1	2003051031-A1	
FR020048	28-mei-02	04-Audio/Video	HANS BREKELMANS@PHILIPS.COM	2T MEMORY CELL	2077100.2	2003051031-A1	
US020167	28-mei-02	05-Mobile Communication	LAURENT HERRMANN@PHILIPS.COM	VIRTUAL REMOTE CONTROL IN MPEG-A SCENE	208502	2003051031-A1	
DE020350	28-mei-02	06-Connectivity	ALOK GOVIL@PHILIPS.COM	FAST CASCADDED CLASS AB MOS OUTPUT STAGE	181569.48	2003051031-A1	
FR020050	28-mei-02	06-Connectivity	GUNWANT SICHE@PHILIPS.COM	VERFAHREN UND ANORDNUNG ZUR DEKODIERUNG VON CCK KOI DE	10223808.1	2003051031-A1	
GB020076	25-mei-02	04-Audio/Video	FRANZ AMTHANN@PHILIPS.COM	PHASE LOCKED LOOP	208503	2003051031-A1	
DE020098	24-mei-02	03-RF Devices	JOHN HAROLD BARRY@PHILIPS.COM	DATA CARRIER FOR PROCESSING A SELF-TAKING SIGNAL	2104553.2	2003051031-A1	
US020148	24-mei-02	04-Audio/Video	MARION MATTERS@PHILIPS.COM	METHOD OF REDUCING PEAK POWER IN A SYSTEM BY DIVIDING	10223285.4	2003051031-A1	
NL020475	24-mei-02	05-Mobile Communication	KEES VAN BERKEL@PHILIPS.COM	ACCESS CONTROL BUS SYSTEM	18156285	2003051031-A1	
NL020476	24-mei-02	05-Mobile Communication	KEES VAN BERKEL@PHILIPS.COM	COMBINED RECONFIGURABLE VECTOR AND SCALAR PIPELINE	2077034.3	2003051031-A1	
NL020477	24-mei-02	05-Mobile Communication	KEES VAN BERKEL@PHILIPS.COM	COMBINED VECTOR AND SCALAR MEMORY	2076618.2	2003051031-A1	
NL020461	23-mei-02	01-CMOS and embedded p	BERNARDO KASTRUP@PHILIPS.COM	VECTORIZATION OF ACU CONFIGURATION	2076819	2003051031-A1	
DE020130	23-mei-02	05-Mobile Communication	FRANK HEINLE@PHILIPS.COM	IC DESIGN METHOD	2076885.6	2003051031-A1	
NL020452	23-mei-02	07-Identification	GERARD HUBERT 35892@PF.UNKNOW.N	POWER EFFICIENT OFF-LINE PROCESSING FOR UMITS IDLE MODE	10222670.8	2003051031-A1	
NL020416	22-mei-02	01-CMOS and embedded p	TON DITTEWIG@PHILIPS.COM	DPA RESISTIVE BLOCK CIPHER	311612.3	2003051031-A1	
FR020047	22-mei-02	02-Packaging & Testing	TON DITTEWIG@PHILIPS.COM	A MRAM-MEMORY CELL SUITABLE FOR A READ-WHILE-WRITE	2077600.4	2003051031-A1	
US020152	21-mei-02	05-Mobile Communication	NIELS PRAMER@PHILIPS.COM	MRAM-CELL AND ARRAY-ARCHITECTURE WITH MAXIMUM READ	209277	2003051031-A1	
DE020126	20-mei-02	03-RF Devices	BOB MILSON@PHILIPS.COM	TESTABLE LOGIC CELL	2075887.2	2003051031-A1	
FR020044	17-mei-02	05-Mobile Communication	HANS PETER LOEBL@PHILIPS.COM	RF POWER AMPLIFIER	101152235	2003051031-A1	
NL020368	16-mei-02	02-Packaging & Testing	LUUK TIEMEIJER@PHILIPS.COM	SOLID-STATE DEVICE WITH INTEGRAL VOLTAGE SENSE	2253544.7	2003051031-A1	
FR020044	17-mei-02	05-Mobile Communication	HANS PETER LOEBL@PHILIPS.COM	EXTENDED-BANDWIDTH MAXIMALLY-FLAT THIN-FILM BAW RF	209079	2003051031-A1	
NL020401	16-mei-02	07-Identification	HEIMO BERGLER@PHILIPS.COM	SAW FILTER WITH IMPROVED ELECTRODE DESIGN	2076913.3	2003051031-A1	
DE020122	14-mei-02	08-Connectivity	STEFF ELDREWS@PHILIPS.COM	EXTRINSIC INFORMATION MEMORY FOR TURBO DECODER	10021451.4	2003051031-A1	
DE020348	14-mei-02	05-Connectivity	WOLFRAM DRESCHER@PHILIPS.COM	METHOD FOR CALIBRATING AND DE-EMBEDDING, AND SET OF	2076911.3	2003051031-A1	
DE020347	13-mei-02	05-Connectivity	LEWE FORST@PHILIPS.COM	ZERO-DEAD TIME SWITCHING	2076913.5	2003051031-A1	
NL020355	10-mei-02	05-Mobile Communication	HENNING MOELLER@PHILIPS.COM	TRANSMISSION ERROR RESISTANT READER STATION	10021451.4	2003051031-A1	
AT020024	10-mei-02	07-Identification	KIM JASPERS@HETNET.NL	DIRECT CONTACTLESS COMMUNICATION AMONG TRANSPONDER DE	10221529.4	2003051031-A1	
GB020056	9-mei-02	03-RF Devices	CHRISTIAN LACKNER@PHILIPS.COM	METHOD AND ARRANGEMENT FOR POWER EFFICIENT CONTROL	10221520.8	2003051031-A1	
NL020397	8-mei-02	01-CMOS and embedded p	KEVIN BOYLE@PHILIPS.COM	BURST ZUGRIFFSVERFAHREN AUF CO-PROZESSOREN	10231206.6	2003051031-A1	
NL020342	7-mei-02	04-Audio/Video	JOOP VAN LAUREN@PHILIPS.COM	IMAGE ENHANCING	2076861.1	2003051031-A1	
NL020338	7-mei-02	04-Audio/Video	ROB VAN SCHALK@PHILIPS.COM	METHOD TO PAY WITH A SMART CARD	2100474.2	2003051031-A1	
AT020131	7-mei-02	04-Audio/Video	LIN YUE 22455@PF.UNKNOW.N.ORG	MODULAR NOTCH DIVERSITY	210601.1	2003051031-A1	
US020125	1-mei-02	03-RF Devices	ANDY NEGOTI@PHILIPS.COM	METHOD TO INCREASE THE COUPLING COEFFICIENT OF FLOATIN	2076771.1	2003051031-A1	
GB020052	30-apr-02	03-RF Devices	KEVIN BOYLE@PHILIPS.COM	DIVISOR WITH INTERLACED VIDEO + DE-INTERLACED TEXT	2076814.9	2003051031-A1	
CH020006	30-apr-02	05-Mobile Communication	THOMAS PAUSER@PHILIPS.COM	METHOD OF MAKING A SEMICONDUCTOR DEVICE WITH NVM	2076830.5	2003051031-A1	
FR020005	30-apr-02	05-Mobile Communication	GIJS VAN STEENWIJK@PHILIPS.COM	CHARGE PUMP	10140319	2003051031-A1	
FR020043	30-apr-02	05-Mobile Communication	FABRIZIO CAMPANALE@PHILIPS.COM	EFFICIENT IMPLEMENTATION OF JOINT PHASE AND FREQUENCY	2089864.6	2003051031-A1	
AT020023	30-apr-02	07-Identification	HERVE JACOB@PHILIPS.COM	IMPROVED ARCHITECTURE OF A POLYLED DRIVING IC FOR...	208956.6	2003051031-A1	
NL020344	29-apr-02	09-Discretes and Multimedia	FRANZ AMTHANN@PHILIPS.COM	DUAL FED PIFA WITH PIN SWITCHING AND NO DIPLEXER	10136732	2003051031-A1	
NL020317	25-apr-02	09-Discretes and Multimedia	JAN WESTRA 18847@PF.UNKNOW.N.ORG	MULTI-BAND INDUCTIVELY TUNED PIFADS-PIFA	208818.4	2003051031-A1	

Patent No.	IPC Class.	Inventor	Assignee	Title	Pub No.	Pub Date	Pub Type
AT020020	25-apr-02	FRANZ AMTWMANN@PHILIPS.COM	PHILIPS.COM	INVENTORIZED TAGS RESPOND IN A FIXED TIME SLOT	2100399.1	20030919	EP
NL020332	25-apr-02	E.VAR.TUJIL@PHILIPS.COM	PHILIPS.COM	COMPENSATION OF RIGHT-HALFPLANE ZERO IN A GM BASED INTLEP	2076619.2	20030919	EP
US020007	22-apr-02	GORNELIU.TOBESCU@PHILIPS.COM	PHILIPS.COM	DIRECT RAM ACCESSING WITHOUT HANDSHAKING, USING A SING EP	2009493.2	20030923	US
US020123	19-apr-02	LASZLO HARS.22011@PF.UNKNOW.NL	PHILIPS.COM	GAP HISTOGRAM ON-LINE RANDOMNESS TEST	1014275.6	20030923	US
CH020006	19-apr-02	ANDY.NECCI@PHILIPS.COM	PHILIPS.COM	SOFTWARE RE-CONFIGURABLE CHARGE PUMP WITH IMPROVED I EP	2009829	20030923	EP
NL020324	19-apr-02	HENK JANI.TEN.DOLLE@PHILIPS.COM	PHILIPS.COM	COMMON MODE DISTORTION SUPPRESSION FOR PUSH/PULL CAS EP	2076546.7	20030923	EP
GB020047	18-apr-02	AROUJG.JSIRA@PHILIPS.COM	PHILIPS.COM	SHARED FIFO ARRANGEMENT IN MULTI-TRACK IC ARCHITECTURE GB	208834.2	20030923	GB
NL020321	18-apr-02	JEROEN.LEIJTEN@PHILIPS.COM	PHILIPS.COM	BALANCING PERFORMANCE, POWER DISSIPATION AND STATE SA EP	2076525.1	20030923	EP
DE020097	18-apr-02	MATTHIAS.MUTH@PHILIPS.COM	PHILIPS.COM	DUAL VOLTAGE SUPPLY CONCEPT FOR AUTOMOTIVE SYSTEM DE	1021723.8	20030923	DE
GB020046	18-apr-02	STEVEN.PEAK@PHILIPS.COM	PHILIPS.COM	TRANSVERSE HIGHER-DOPED RUGGEDNESS STRIPE MIN-PITCH I GB	208833.4	20030923	GB
US020121	18-apr-02	JIM.SPEARE@PHILIPS.COM	PHILIPS.COM	DIFFERENTIAL OUTPUT STRUCTURE W/REDUCED SKEW FOR A SI US	1812596.3	20030923	US
US020118	17-apr-02	LASZLO.HARS.22011@PF.UNKNOW.NL	PHILIPS.COM	HADAMARD-TRANSFORM ON-LINE RANDOMNESS TEST	1812480.4	20030923	US
AT020019	16-apr-02	RAINER.MOLL.22018@PF.PHILIPS.COM	PHILIPS.COM	DIMPLED APERTS FOR BUMP CONNECTION	2100395.5	20030923	EP
DE020100	16-apr-02	PETER.FUHRMANN@PHILIPS.COM	PHILIPS.COM	METHOD OF SETTLING THE COMMUNICATION SCHEDULE IN ... DE	10216894.6	20030923	DE
DE020333	16-apr-02	PETER.FUHRMANN@PHILIPS.COM	PHILIPS.COM	FLEXRAY COMMUNICATION PROTOCOL	2008171.7	20030923	EP
DE030304	16-apr-02	TOM.FOREST.27361@PF.UNKNOW.NL	PHILIPS.COM	METHOD FOR SYNCHRONIZING CLOCKS IN A DISTRIBUTED COMM EP	3008739.9	20030923	EP
DE030306	16-apr-02	TOM.FOREST.27361@PF.UNKNOW.NL	PHILIPS.COM	METHOD FOR SYNCHRONIZING CLOCKS IN A DISTRIBUTED COMM EP	3008739.9	20030923	EP
NL020327	11-apr-02	HENK.KLOEN@PHILIPS.COM	PHILIPS.COM	MECHANICAL ANCHORING OF ENCAPSULATION (IN LEAD FRAME) EP	2076426.2	20030923	EP
NL021100	11-apr-02	ROELF.GROENHUIJS@PHILIPS.COM	PHILIPS.COM	PACKAGE WITH LEADFRAME COMPRISING ETCH MASKS	2079544.9	20030923	EP
NL020277	11-apr-02	EDUARD.STIKVOORT@PHILIPS.COM	PHILIPS.COM	POLYPHASE FILTER	2078439.7	20030923	EP
FR020053	11-apr-02	DRIES.VAN.WASENINGEN@PHILIPS.COM	PHILIPS.COM	FRAME ALIGNMENT FOR BUFFERLESS MATRIX IN PACKET FR	2004070.1	20030923	FR
DE020090	10-apr-02	MARC.DURANTON@PHILIPS.COM	PHILIPS.COM	MSB/LSB/LS BASED MEMORY STRUCTURE	204462	20030923	DE
FR020028	9-apr-02	HARTMUT.ZABEN@PHILIPS.COM	PHILIPS.COM	FAST SEARCH ALGORITHM FOR DATA SORTING IN CHAINED EP	2076399.2	20030923	EP
US020036	9-apr-02	PETRA.JORIGH.21855@PF.UNKNOW.NL	PHILIPS.COM	DUAL DS-SS/PSA FOR UNITS WITH DUAL FILTERED FEEDS GB	208130.5	20030923	GB
NL020265	8-apr-02	KEVIN.BOYLE@PHILIPS.COM	PHILIPS.COM	DUAL DS-SS/PSA FOR UNITS WITH DUAL FILTERED FEEDS FR	2076493.6	20030923	FR
CH020008	2-apr-02	JACCO.DERKERS@PHILIPS.COM	PHILIPS.COM	A DIFFERENTIAL AMPLIFIER	2044013	20030923	EP
FR020028	2-apr-02	GUIDO.PLANGGER@PHILIPS.COM	PHILIPS.COM	DESIGN FOR TEST OF HIGH VOLTAGE CIRCUITRY, MADE OF TRAN EP	1830021000993	20030923	EP
NL020278	26-mrt-02	EMERIC.USUENG@PHILIPS.COM	PHILIPS.COM	SCALABLE CLUSTERED ARCHITECTURE FOR VLIW DE	10213423.5	20030923	DE
NL020238	26-mrt-02	MARCO.BEKOUJ@PHILIPS.COM	PHILIPS.COM	CIRCUITS FOR PHASE SHIFTING & MIRROR SUPPRESSION EP	2076188.0	20030923	EP
US020099	26-mrt-02	BURTHARD.DICK@PHILIPS.COM	PHILIPS.COM	A HIGH FREQUENCY TUNER	18107000	20030923	US
US020062	26-mrt-02	LASZLO.HARS.22011@PF.UNKNOW.NL	PHILIPS.COM	GAP AVERAGE ON-LINE RANDOMNESS TEST	10106946	20030923	US
US020093	26-mrt-02	LASZLO.HARS.22011@PF.UNKNOW.NL	PHILIPS.COM	MONOBI-TURN FREQUENCY ON-LINE RANDOMNESS TEST	10106950	20030923	US
DE020071	23-mrt-02	DANIEL.STABENAU@PHILIPS.COM	PHILIPS.COM	NAVIGATION FOR SERVICES WITH FIXED POINTS PER WAY DE	10213180.3	20030923	DE
FR020021	22-mrt-02	ERIC.CALMELS@PHILIPS.COM	PHILIPS.COM	SET TOP BOX WITH SOFTWARE DOWNLOAD FR	2038875	20030923	FR
FR020022	22-mrt-02	ERIC.CALMELS@PHILIPS.COM	PHILIPS.COM	MODULAR SET TOP BOX	2290736	20030923	FR
NL020230	21-mrt-02	ROB.WOLTERS@PHILIPS.COM	PHILIPS.COM	SOA FOR IC'S WITH SECURITY COATING EP	2076112.8	20030923	EP
DE020077	20-mrt-02	IGOR.BLEDNOV@PHILIPS.COM	PHILIPS.COM	QUALITY ENHANCEMENT OF FINGERPRINT IMAGES EP	2076105.2	20030923	EP
FR020319	19-mrt-02	JEAN-CLAUDE.SIX@PHILIPS.COM	PHILIPS.COM	ISOLATED CAVITY CREATION FR	2033770	20030923	FR
FR020320	19-mrt-02	ERIC.DESNIGHT@PHILIPS.COM	PHILIPS.COM	LOW COST CONDITIONAL ACCESS SYSTEM FR	2033771	20030923	FR
NL020218	18-mrt-02	PIERRE.DE.GREEF@PHILIPS.COM	PHILIPS.COM	SUBFIELD DRIVEN MOBILE DISPLAYS EP	2076071.8	20030923	EP
NL020212	18-mrt-02	KASJA.LEIJTEN.NOWAK@PHILIPS.COM	PHILIPS.COM	CL. COMMON DECODER FOR CONFIGURATION MEMORY AND LUT EP	2076046.4	20030923	EP
NL020211	18-mrt-02	KASJA.LEIJTEN.NOWAK@PHILIPS.COM	PHILIPS.COM	CL. EFFICIENT IMPLEMENTATION OF MULTIPLEXERS EP	2076049.2	20030923	EP
AT020016	18-mrt-02	FRANZ.AMTMANN@PHILIPS.COM	PHILIPS.COM	AUTHORIZATION CARD FOR COMMUNICATION ALLOWANCE EP	2100270.4	20030923	EP
US020014	14-mrt-02	HOLGER.KUNKAT@PHILIPS.COM	PHILIPS.COM	IC CIRCUIT WITH MIXED SUPPLY VOLTAGE CAPABILITY US	1010493.7	20030923	US
US020018	14-mrt-02	KWONG.KAM.CHON@PHILIPS.COM	PHILIPS.COM	FRONT STAGE AMPLIFIER WO	9320020000042	20030923	WO
SG020006	13-mrt-02	HOLGER.KUNKAT@PHILIPS.COM	PHILIPS.COM	COMMUNICATION OF READERS BY A SEPARATE PROTOCOL EP	2100244.9	20030923	EP
AT020012	13-mrt-02	WOLTER.TURRY@PHILIPS.COM	PHILIPS.COM	FIXED CLOCK DECODING EP	2076981.7	20030923	EP
NL020204	12-mrt-02	JERGEN.VAN.AG@PHILIPS.COM	PHILIPS.COM	MAINTAINING SELECTION WHEN SWITCHING TO ANOTHER SERV EP	6073537.16	20030923	EP
NL020230	11-mrt-02	STEFAN.BUTZMANN@WEB.DE	PHILIPS.COM	RF AND BB SUBSYSTEMS DIGITAL INTERFACE US	10210184.1	20030923	US
FR020018	9-mrt-02	WOL.FRANG.SCHNITT@PHILIPS.COM	PHILIPS.COM	RF SENSOR SUPPRESSING EXTERNAL MAGNETIC FIELDS DE	2075944.7	20030923	DE
FR020052	7-mrt-02	WOL.FRANG.SCHNITT@PHILIPS.COM	PHILIPS.COM	IC-MISMATCH ESTIMATION AND CORRECTION EP	2075607.4	20030923	EP
FR020070	5-mrt-02	GUNWAR.WEITZGER@PHILIPS.COM	PHILIPS.COM	A TIME-ON-DEMAND VOLTAGE COMPARTOR EP	1810860.6	20030923	EP
NL020169	5-mrt-02	GULLAUME.DE.CREMOUX@PHILIPS.COM	PHILIPS.COM	EXTENDED DETERMINISTIC LOGIC BUILD-IN-SELF-TESTS DE	10209070.5	20030923	DE
DE020045	1-mrt-02	FRIEDRICH.HAPKE@PHILIPS.COM	PHILIPS.COM	CONTROLLING MODE OF APPARATUS EP	2075944.7	20030923	EP
NL020181	27-feb-02	VISHAL.ANAND.21108@PF.PHILIPS.COM	PHILIPS.COM	SHARED QUEUE FOR MULTIPLE INPUT-STREAMS US	1810860.6	20030923	US
FR020011	26-feb-02	GUODAO.TAO@PHILIPS.COM	PHILIPS.COM	A FAST WATER LEVEL RELIABILITY EVALUATION STRUCTURE EP	2076761.3	20030923	EP
FR020013	26-feb-02	MARC.DURANTON@PHILIPS.COM	PHILIPS.COM	TIME DRIVEN CONTROL PROCESSOR FR	2023366	20030923	FR
NL020145	25-feb-02	HERMAN.UITTENBOGAARD@PHILIPS.COM	PHILIPS.COM	ARBITRATION FOR NON-CRITICAL IP CHANNELS FR	2075793.0	20030923	FR
DE020043	22-feb-02	CORD-HEINRICH.KOHSIEN@PHILIPS.COM	PHILIPS.COM	CORD-CONTROLLED LINEAR RESISTANCE DE	10207676.8	20030923	DE

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Patent No.	IPC Class.	Inventor	Assignee	Title	Pub. No.	Pub. Date
DE020044	04-Audio/Video	CORD-HEINRICH KOHSEK	PHILIPS.COM	CONTROLLABLE LINEAR INTEGRATED RESISTOR	10207675.8	10/20/2002
G020001C	05-Mobile Communication	ADAM FUKS	PHILIPS.COM	LOW POWER FAST ACCESS BUS PROTOCOL	2004144	02/01/02
FR020010	01-CMOS and embedded p	WILLEM TOREN	16664@FF.UNIKWONV.NL	ADVANCED LITHO FOR PRINTING ULTRA DENSE FEAs	2004030.4	01/16/02
DE020042	04-Audio/Video	JOOP VAN HAMMEREN	PHILIPS.COM	TUNER CONTACT TO CUSTOMER BOARD (PINK STRIPE)	1020772.9	01/16/02
NL020143	04-Audio/Video	LASZLO HARS	22011@PF.UNIKWONV.NL	FLIPFLOP LATCHES CLOCKED BY NON-OVERLAPPING CLOCKS	2004021801A1	01/16/02
US020046	07-Identification	LASZLO HARS	22011@PF.UNIKWONV.NL	ON-LINE RANDOMNESS TEST FOR DETECTING IRREGULAR	200402181A.2	01/16/02
US020061	07-Identification	LASZLO HARS	22011@PF.UNIKWONV.NL	ON-LINE RANDOMNESS TEST UTILIZING AUTO-CORRELATION	200402181A.2	01/16/02
DE020036	05-Mobile Communication	STEFAN KLEINBERG	24148@PF.PHILIPS.COM	ARRANG AND METHOD TO MEASURE THE AVERAGE CURRENT	10207062.8	01/16/02
A0200110	07-Identification	CHRISTIAN BRUGGER	PHILIPS.COM	ENDLESS TRANSMITTING-MEANS PRIMARY PRODUCT	210146.2	01/16/02
US020018	09-Discretes and Multimar	JEFF D. SEN	PHILIPS.COM	N-PHASE INTEGRATED BLOCK CONVERTER	10078701	01/16/02
DE020048	08-Connectivity	WOLFRAM DRESCHER	PHILIPS.COM	VERFAHREN UND ANORDNUNG ZUM ZUSAMMENFUERN VON D	10206830.5	01/16/02
NL020157	08-Automotive	MANFRED ZINKE	PHILIPS.COM	COMMUNICATION SYSTEM WITH AUTONOMOUS MEDIA ACCESS	10206875.5	01/16/02
DE020035	05-Discretes and Multimar	WINSTON LANGESLAG	PHILIPS.COM	NO SOUND IN POWER CONVERTER	2002917.9	01/16/02
US020009	04-Audio/Video	HANS-JUERGEN KUEHN	PHILIPS.COM	GATED DIGITAL AGC CIRCUIT FOR ANALOG-TV RECEIVERS	60355203	01/16/02
DE020045	05-Connectivity	DAVID ZIGER	PHILIPS.COM	CALIBRATION WAFER FOR A STEPPER	10205593.0	01/16/02
DE020002	04-Audio/Video	PAUL SCHWANING	PHILIPS.COM	METHOD FOR THE PROVISION OF A DESIGN, TEST AND ...	10205944.9	01/16/02
CH020003	05-Mobile Communication	HANS-JUERGEN KUEHN	PHILIPS.COM	DIGITAL PLL FOR CARRIER RECOVERY	2002682.9	01/16/02
NL020087	05-Mobile Communication	JOAN DRENTHE	PHILIPS.COM	REDUCING OF LEAKAGE CURRENTS IN SUBMICRON PROCESS	2002681.1	01/16/02
NL020118	06-Connectivity	ROLF BECKER	PHILIPS.COM	DIGITAL INPUT STAGE COMPLIANT WITH HIGH VOLTAGE SWING...	2075489.4	01/16/02
FR020006	08-Discretes and Multimar	OSWALD MCCONEN	PHILIPS.COM	A MULTI-BAND RECEIVER AND TRANSMITTER	2075485.8	01/16/02
GB020005	05-Mobile Communication	EVERETT SEEVICK	02842@PF.UNIKWONV.NL	FAST DATA PATH FOR LOW-VOLTAGE, SINGLE BITLINE MEMORIES	201296	01/16/02
NL020084	04-Audio/Video	FRANCK THEBALLT	PHILIPS.COM	EXTENDED BCH AND S-MISC RADIO CONFLICT	10204294.2	01/16/02
SG020001	01-CMOS and embedded p	RALF BEIER	PHILIPS.COM	VOLTAGE REGULATOR FOR A PULSE CREATION CIRCUIT	2075425.5	01/16/02
NL020102	04-Audio/Video	STEVEN PEAKE	PHILIPS.COM	SOCKET & BESTFIT INTEGRATED SCHOTTKIES	10204344.2	01/16/02
DE020043	05-Mobile Communication	TOH YEW TENG	PHILIPS.COM	SPACER IN MEMORY CELL	10204345	01/16/02
DE020044	05-Mobile Communication	IGOR BLEDOV	PHILIPS.COM	TELEVISION TUNER AND PRINTED CIRCUIT USED BOARD THEREIN	2075419.8	01/16/02
NL020071	06-Connectivity	HELGE BETZINGER	PHILIPS.COM	POWER TRANSDUCER HARMONIC TERMINATION	10204346	01/16/02
NL020083	06-Connectivity	ELUNAR WETZGER	PHILIPS.COM	KONFIGURIERBARES BUSSYSTEM	2075424.8	01/16/02
AT020004	01-Identification	B. NAUTA	@EL.UUTWENTE.NL	METHOD FOR PROCESSING INSTRUCTIONS	2100093	01/16/02
DE020025	03-RF Devices	HELMUT KRANAUERT	PHILIPS.COM	ADAPTIVE DC COMPONENT DETECTION IN A BURST SIGNAL	607354052	01/16/02
FR020004	04-Audio/Video	A. J. M. DE GRAAUV	PHILIPS.COM	PLL WITH REDUCED JITTER	20754085	01/16/02
FR020005	04-Audio/Video	MARINEE KLEEG	PHILIPS.COM	ADAPTING COIL VOLTAGE OF A TAG TO FIELD STRENGTH	20754085	01/16/02
NL020106	05-Mobile Communication	THEIRY LAFAGE	23689@PF.PHILIPS.COM	ELECTRONIC DEVICE	20754085	01/16/02
NL020107	08-Discretes and Multimar	PHILIPPE GEVTRIC	PHILIPS.COM	MULTIMEDIA STREAMING OVER VARIABLE NETWORKS	20754085	01/16/02
NL020108	08-Discretes and Multimar	PETER SCHOLTEMS	PHILIPS.COM	AVERAGING AMPLIFIER ARRAY	20754085	01/16/02
DE020042	09-Discretes and Multimar	ROBERT VAN VELDHOVEN	PHILIPS.COM	CIRCUIT WITH A DIGITAL TO ANALOG CONVERTER	2075389.3	01/16/02
NL020096	02-Packaging & Testing	JOERG FISCHER	PHILIPS.COM	ELECTRONIC CIRCUIT WITH A SIGMA DELTA A/D CONVERTER	10203381.5	01/16/02
NL020093	04-Audio/Video	ROBERT VAN VELDHOVEN	PHILIPS.COM	TESTING OF CIRCUIT WITH PLURAL CLOCK DOMAINS	10203367.9	01/16/02
NL020094	04-Audio/Video	BART VERMELLEN	PHILIPS.COM	REMOVING QUANTIZATION NOISE FROM PCM CODED WAVEFORM	2075303.4	01/16/02
NL020095	04-Audio/Video	ROBERT TOLKIEHN	PHILIPS.COM	CURRENT BASED INTERFACE	2075302.9	01/16/02
NL020096	04-Audio/Video	ERGAN GIGI	PHILIPS.COM	IN-SHIN DECOUPLING AND POWER SUPPLY	2075274.7	01/16/02
NL020097	04-Audio/Video	FRANK VAN RENS	PHILIPS.COM	TRANSMITTER STAGE FOR DIFFERENTIAL BUS SYSTEMS	10203032.5	01/16/02
NL020098	04-Audio/Video	HARRY VEENDRICK	PHILIPS.COM	CONFIGURABLE SYNCHRONOUS OR ASYNCHRONOUS BUS INTER	101652276	01/16/02
NL020099	04-Audio/Video	BERNARD ELEN	PHILIPS.COM	IMPROVED CMOS INVERTER	101652277	01/16/02
NL020100	04-Audio/Video	TIM POKTUS	PHILIPS.COM	DUAL SLOPE LEVEL SHIFTER	10200817.1	01/16/02
NL020101	04-Audio/Video	HANS BREKELMANS	PHILIPS.COM	METHOD TO IMPROVE THE POWER SUPPLY REJECTION	2002589.7	01/16/02
NL020102	04-Audio/Video	ZHENHUA WANG	PHILIPS.COM	BONDPAD ON ACTIVE AREA FOR MR-SENSORS	200305650A1	01/16/02
NL020103	04-Audio/Video	MICHAEL DOESCHER	PHILIPS.COM	COMPLEX SWITCHED-CURRENT INTEGRATOR WITH DYNAMIC ELF	200305650A1	01/16/02
NL020104	04-Audio/Video	OLAF HIRSCH	PHILIPS.COM	RAKE RECEIVER WITH FINGER COMPENSATORS	200305650A1	01/16/02
NL020105	04-Audio/Video	JOHN HUGHES	PHILIPS.COM	BUFFER/INVERTER LIBRARY CELLS FOR CLOCK TREES	200305650A1	01/16/02
NL020106	04-Audio/Video	XULIUZHOU	DSP.UFL.EDU	FAST MULTIPLEXING IN GF (2 POWER N)	10164416.7	01/16/02
NL020107	04-Audio/Video	WOLFGANG BLURH	PHILIPS.COM	TIMING OPTIMIZED PROGRAMMING OF NV MEMORIES	10164415.9	01/16/02
NL020108	04-Audio/Video	WOLFGANG BLURH	PHILIPS.COM	INSTRUCTION DRIVEN WRITING OF NV MEMORIES	1403378.9	01/16/02
NL020109	04-Audio/Video	SERASTIEN CHARPENTIER	PHILIPS.COM	UNIVERSAL TURBOCODER USING BLINDING WINDOWS	10163788.5	01/16/02
NL020110	04-Audio/Video	HENK VAN DER WUJST	PHILIPS.COM	UNIVERSAL TURBOCODER USING BLINDING WINDOWS	10163788.5	01/16/02

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GB010222	22-36c-01	SAUL DOOLEY@PHILIPS.COM	FAST ACQUISITION, HIGH SENSITIVITY GPS RECEIVER	22-36c-01	1306044.8	GB	20030929555-41	2003/09/29
DE010351	21-48c-01	JOCHEN RUDOLPH@PHILIPS.COM	HIGH VOLTAGE RANGE CMOS CURRENT MIRROR	21-48c-01	1016336.34.4	DE	20030929555-41	2003/09/29
DE010378	21-dec-01	GUNAR NITSCHKE@PHILIPS.COM	METHOD FOR THE DETERMINATION OF A MAXIMUM OR A MINIMUM OF A COMMUNICATION BUS SYSTEM	21-dec-01	1285134.8	EP	20030929555-41	2003/09/29
NL010959	21-48c-01	PATRICK HELMETS@PHILIPS.COM	COUPLE WITH RESONANT STRUCTURE	21-48c-01	1285134.8	EP	20030929555-41	2003/09/29
DE010336	20-48c-01	MARION MATTERS@PHILIPS.COM	BIAS LEVEL CALIBRATION FOR GREYS-SCALE CHIPS	20-48c-01	1205039.1	EP	20030929555-41	2003/09/29
CH010036	20-48c-01	WOLFGANG FALLOT-BURGHARDT@PHILIPS.COM	ARRANGEMENT TO CREATE THE COLUMN VOLTAGE IN LCD DRIVES	20-48c-01	10162766.1	DE	1306044.8	2003/09/29
FR010183	20-48c-01	CHRIS SPEIRS@PHILIPS.COM	MILLIENIUM WATERMARKING IMPLEMENTATION	20-48c-01	116575	FR	1306044.8	2003/09/29
FR010177	20-48c-01	HUGUES DE PERTHUIS@PHILIPS.COM	HIGH PERFORMANCE BANDGAP GENERATOR	20-48c-01	116575	FR	1306044.8	2003/09/29
FR010184	20-48c-01	HERVE MARIE@PHILIPS.COM	ACCURATE GIN-C FILTER	20-48c-01	116575	FR	1306044.8	2003/09/29
FR010178	20-48c-01	STEPHANE BOUNVIER@PHILIPS.COM	AGC CIRCUIT FOR OPTICAL FIBRE NOTABLY	20-48c-01	116575	FR	1306044.8	2003/09/29
DE010330	20-48c-01	MARC DURANTON@PHILIPS.COM	ENTRY CODING OF THE HEADER OF PACKETS	20-48c-01	10162761.3	DE	20030929555-41	2003/09/29
DE010340	20-48c-01	CORD-HEINRICH KGH-SIEK@PHILIPS.COM	MOS TRANSISTOR ARRANGEMENT FOR SOFT SWITCHING	20-48c-01	1302504.4	EP	20030929555-41	2003/09/29
NL010982	20-48c-01	MICHAEL DOESCHER@PHILIPS.COM	HOMOGENIZING STRIPES FOR XMR SENSORS	20-48c-01	1205015.9	EP	20030929555-41	2003/09/29
NL010972	19-48c-01	PETER SCHOLTENS@PHILIPS.COM	A MORE COMPACT CODE FOR AD-CONVERTERS	19-48c-01	1302504.4	EP	20030929555-41	2003/09/29
GB010216	19-48c-01	ROB WOLTERS@PHILIPS.COM	IN-SITU BARRIER IMPROVEMENT FOR CU	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
DE010337	19-48c-01	ANDY YULE@PHILIPS.COM	DYNAMIC TESTROM SELF-TEST PROTECTION	19-48c-01	1162291	DE	1306044.8	2003/09/29
DE010341	19-48c-01	DETLEF MUELLER_2@PHILIPS.COM	SECURE CHECK-AUTOMATION TEST DURING RESET PHASE	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
DE010342	19-48c-01	DETLEF MUELLER_7@PHILIPS.COM	DEVICE SPECIFIC ROM CODE	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
DE010344	19-48c-01	DETLEF MUELLER_2@PHILIPS.COM	EEPROM WRITE PROTECTION	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
DE010345	19-48c-01	MARKUS FEUSER@PHILIPS.COM	CODED STATUS INFORMATION IN SMART CARD CONTROLLER	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
NL010925	19-48c-01	AD-PEETERS@PHILIPS.COM	COUNTERMEASURE AGAINST DPA	19-48c-01	10162300.8	DE	1306044.8	2003/09/29
US010924	18-48c-01	KEN PIERICK.0227@PHILIPS.COM	DELAY CONTROLLED LOOP CONTROLS SPEED OF CMOS LOGIC	18-48c-01	1234854	EP	20030929555-41	2003/09/29
A1010078	17-48c-01	HENG HE.2340@PHILIPS.COM	POLARITY REVERSAL TOLERANT ELECTRICAL CIRCUIT FOR ANTI-COLLISION PROCEDURE USING UID OR USER DATA	17-48c-01	1000768.7	US	20030929555-41	2003/09/29
CH010022	15-48c-01	FRANZ AMTMANN@PHILIPS.COM	CARBON-DETERMINED LIFETIME PROFILE IN POWER SEMICONDUCTOR	15-48c-01	130018.5	GB	20030929555-41	2003/09/29
CH010048	14-dec-01	MIRON DRIBNIS@PHILIPS.COM	FREELY PROGRAMMABLE SELECTION OF THE ROW FUNCTION	14-dec-01	1129872.8	EP	20030929555-41	2003/09/29
NL010950	14-dec-01	DOMINIK ZEITZ.2265@PHILIPS.COM	PROCESSOR WITH COMMUNICATION UNIT	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
NL010951	14-dec-01	JOS VAN EUNDOHOVEN@PHILIPS.COM	MULTI-PROCESSOR DATAFLOW SYNC WITH DISTRIBUTED ADMINISTRATION	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
NL010952	14-dec-01	JOS VAN EUNDOHOVEN@PHILIPS.COM	MULTI-PROCESSOR CACHE COHERENCY BY DATA FLOW SYNC	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
NL010953	14-dec-01	JOS VAN EUNDOHOVEN@PHILIPS.COM	MULTI-PROCESSOR CACHE COHERENCY BY DATA FLOW SYNC	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
US010920	14-dec-01	JOS VAN EUNDOHOVEN@PHILIPS.COM	PROCESSOR WITH COMMUNICATION UNIT	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
BE010020	14-dec-01	LIN YUE.ZI45@PHILIPS.COM	RELIABLE DECODING OF QUICK PAGING CHANNEL IN PIPELINED CODE IN MONTGOMERY MULTIPLIER	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
NL010950	14-dec-01	ZONGLIANG WU.1588@PHILIPS.COM	PIPELINED CODE IN MONTGOMERY MULTIPLIER	14-dec-01	1204883.1	EP	20030929555-41	2003/09/29
US010918	14-dec-01	GERARD HUBERT.659@PHILIPS.COM	A SYSTEM FOR ENHANCING FAULT TOLERANCE AND READ METHOD FOR FLASH EEPROM DEVICE	14-dec-01	1001768.7	US	20030929555-41	2003/09/29
NL010947	13-dec-01	JURDI LEHMAN@PHILIPS.COM	MATRIX SWITCH WITH COMMON VOLTAGE REFERENCE LINE	13-dec-01	1204846.8	EP	20030929555-41	2003/09/29
FR010172	11-dec-01	PHILIPPE BARRE@PHILIPS.COM	SYSTEM WITH A Clocked INTERFACE	11-dec-01	115975	FR	1306044.8	2003/09/29
CH010025	11-dec-01	PHILIPPE BARRE@PHILIPS.COM	DUAL GATE OXIDE HIGH-VOLTAGE SEMICONDUCTOR DEVICE	11-dec-01	1204834.4	EP	20030929555-41	2003/09/29
US010610	10-dec-01	STEPHANIA M KOCH@PHILIPS.COM	LATERAL HIGH VOLTAGE SEMICONDUCTOR DEVICE HAVING NOISE REDUCTION ARCHITECTURE FOR LOW DROPOUT	10-dec-01	1001584.7	US	20030929555-41	2003/09/29
US010648	10-dec-01	TED LETAVIC@PHILIPS.COM	LATERAL HIGH VOLTAGE SEMICONDUCTOR DEVICE HAVING MR SENSOR FOR SPEED AND DIRECTION DETECTION	10-dec-01	1001584.7	US	20030929555-41	2003/09/29
US018185	10-dec-01	TED LETAVIC@PHILIPS.COM	MRS SENSOR FOR SPEED AND DIRECTION DETECTION	10-dec-01	1001318.1	US	20030929555-41	2003/09/29
DE010328	8-dec-01	ANTON BAKKER@PHILIPS.COM	GAS VACUUM GAP IN DEEP TRENCH FIELD-EFFECT DRIVER COLUMN ARCHITECTURE FOR LOW POWER AMPLIFIER	8-dec-01	10160450.5	DE	20030929555-41	2003/09/29
NL010958	8-dec-01	REINHARD BUCHHOLD@PHILIPS.COM	ANTENNA DIVERSITY OF AN ELECTRONIC DEVICE	8-dec-01	129450.3	GB	20030929555-41	2003/09/29
CH010027	7-dec-01	ERWIN HUIZEN@PHILIPS.COM	A MINIATURE FILTER BALL UNIFORMITY DESIGN	7-dec-01	129450.3	GB	20030929555-41	2003/09/29
NL010984	6-dec-01	CHRIS SPEIRS@PHILIPS.COM	DIRECT PLACING OF COMPONENT IN PAPER TAPE	6-dec-01	1204723.9	EP	20030929555-41	2003/09/29
NL010983	6-dec-01	JOS BERGVERVOET@PHILIPS.COM	MULTI-RESURF & POLYDOX-ENHANCED BIGE 600V DIODES	6-dec-01	1204723.9	EP	20030929555-41	2003/09/29
NL010995	5-dec-01	THEO RIJNS@PHILIPS.COM	ESD PROTECTION CIRCUIT FOR USE IN RF CMOS IC	5-dec-01	10017986	US	20030929555-41	2003/09/29
US010200	4-dec-01	ED HUANG@PHILIPS.COM	VERFAHREN UND ANORDNUNG ZUR ADDRESS-GENERIERUNG IN EIDE INTEGRATED CIRCUIT WITH AMPLIFIER	4-dec-01	1204585.2	EP	20030929555-41	2003/09/29
US010581	4-dec-01	WOLFRAM DRESCHER@PHILIPS.COM	PULSED-LASER AMORPHIZATION FOR SHALLOW JUNCTIONS TEST CIRCUIT TOPOLOGY RECONFIGURATION AND MULTISENSOR ENVIRONMENT TESTING WITH A RESPONDER	4-dec-01	1204585.2	EP	20030929555-41	2003/09/29
US010987	4-dec-01	RAINIER BREUNINGER@PHILIPS.COM	VERFAHREN UND ANORDNUNG ZUR DENODIERUNG SPEZIFISCHER REMOTE CONTROL FOR PRODUCING AN ADJUSTABLE MODULATE EP	4-dec-01	1000567.2	EP	20030929555-41	2003/09/29
US010209	3-dec-01	ZONGLIANG WU.1588@PHILIPS.COM	HIGH-SPEED INTERCHIP INTERFACE PROTOCOL	3-dec-01	091997764	US	20030929555-41	2003/09/29
US010976	30-nov-01	PETER STOLK@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010977	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010978	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010979	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010980	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010981	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010982	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010983	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010984	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010985	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010986	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010987	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010988	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010989	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010990	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010991	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010992	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010993	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010994	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010995	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010996	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010997	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010998	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US010999	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29
US011000	30-nov-01	LONNIE GORFF.21237@PHILIPS.COM	PHILIPS LABELS TRANSMIT EAS-BIT IN TWO TIME SLOTS	30-nov-01	091997764	US	20030929555-41	2003/09/29



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US018153	08-Discretes and Multitasks	JERRY LEBLER 23051@PFF.PHILIPS.COM	FOR NEAR LOSSLESSLY MEASURING INDUCTOR CURRENT	US	09/981471	2003/03/27/01A1	US064653
NL010713	08-Connectivity	DOMINE LEEAERTS@PHILIPS.COM	A RECEIVER	EP	1203585.4	2003/03/27/01A1	
US018187	01-CMOS and embedded p	MIKE OLEWINE 24143@PFF.PHILIPS.COM	METAL-INSULATOR-METAL (MIM) CAPACITOR STRUCTURE	US	09/9173840	2003/03/26/01A1	US0272169
DE010278	02-Packaging & Testing	MICHAEL DROESCHER@PHILIPS.COM	CHIP-SCALE PACKAGING WITH STEP BY STEP ENCAPSULATION	O DE	10149689.3	2003/03/26/01A1	US0262958
NL010677	02-Packaging & Testing	JACOB SMANIG@PHILIPS.COM	MARKING CHIPS THROUGH A HANDLING FOIL	EP	12033817	2003/03/26/01A1	US0262958
FR010149	09-Discretes and Multitasks	EMERIC UGUEN@PHILIPS.COM	EXTERNAL CLOCK REPLACING IN AN IC	EP	12033817	2003/03/26/01A1	US0262958
FR010277	10-Cipher	EMERIC UGUEN@PHILIPS.COM	DIGITAL SWITCHABLE CURRENT SOURCE	FR	112381	2003/03/26/01A1	US0647206
AT010059	05-Mobile Communication	ERNST BRETSCHNEIDER@PHILIPS.COM	TRANSDUCER WITH HOLDING CLAMS FOR COIL LEADS	DE	10149789.5	2003/03/26/01A1	US0219062
AT010058	07-Identification	ERWAL FRASL@PHILIPS.COM	TRANSPONDER WITH A CHANGEABLE CIRCUIT CONFIGURATION	EP	1890285.8	2003/03/26/01A1	US0219062
AT010056	07-Identification	BERNHARD SPIESS@PHILIPS.COM	TRANSPONDER DETECTS AN INTERRUPTION OF A COLD CHAIN	EP	1890286.1	2003/03/26/01A1	US0219062
DE010273	07-Identification	MARTIN BUEHRELE@PHILIPS.COM	CONTACTLESS LOCALIZATION OF MOVED OBJECTS	DE	10149819.9	2003/03/26/01A1	US0219062
DE010272	04-08-01	DIRK WENZEL@PHILIPS.COM	AUTOMATIC OFFSET CALIBRATION ON SENSORS	DE	10149819.9	2003/03/26/01A1	US0219062
NL010576	09-Discretes and Multitasks	MICHAEL MUTH@PHILIPS.COM	REDUCED SAMPLE-RATE SIGMA-DELTA MODULATION	EP	1203770.1	2003/03/26/01A1	US0600733
NL010712	07-Identification	ENDER ROZA@PHILIPS.COM	HIGH SPEED HIGH RESOLUTION ADC ARCHITECTURE	EP	1203770.4	2003/03/26/01A1	
NL010679	09-Discretes and Multitasks	RAF ROOYERS@PHILIPS.COM	GATE DRIVER APPARATUS HAVING AN ENERGY RECOVERING	EP	1203774.7	2003/03/26/01A1	
US010485	05-Mobile Communication	FAYE LI@PHILIPS.COM	A THIN-LAYER SILICON-ON-INSULATOR (SOI)	US	09/9480093	2003/03/26/01A1	US0600169
US010486	09-Discretes and Multitasks	TED LETAVIC@PHILIPS.COM	A SELF-ALIGNED DUAL-OXIDE UMOSFET DEVICE AND A	US	09/968121	2003/03/26/01A1	US0611365
NL010665	02-Packaging & Testing	AVAN ARENDONK@PHILIPS.COM	TEST TIME REDUCTION OF AN INTEGRATED CIRCUIT	US	09/968142	2003/03/26/01A1	US0611365
FR010135	05-Mobile Communication	LAURENT MONGE@PHILIPS.COM	BUS SYSTEM AND BUS INTERFACE FOR CONNECTION TO A BUS	FR	112389	2003/03/26/01A1	US0620499
SG010017	08-Connectivity	ALAN CHANG@PHILIPS.COM	PARALLEL DATA COMMUNICATION REALIGNMENT OF DATA	SG	2001000196	2003/03/26/01A1	US0646111
US010593	06-Connectivity	GREG EHMAN@PHILIPS.COM	DYNAMIC SAMPLING	US	09/965297	2003/03/26/01A1	
US018164	09-Discretes and Multitasks	ERWIN BELLEERS@PHILIPS.COM	MULTIPATH NESTED GM MILLER COMPENSATION	US	09/9656038	2003/03/26/01A1	
SG010011	08-Connectivity	ALAN CHANG@PHILIPS.COM	BUS SYSTEM AND BUS INTERFACE FOR CONNECTION TO A BUS	US	09/967418	2003/03/26/01A1	
FR010131	05-Mobile Communication	NGUYEN TRIU LUAN@PHILIPS.COM	BUS SYSTEM AND BUS INTERFACE FOR CONNECTION TO A BUS	FR	112323	2003/03/26/01A1	
US010455	09-Discretes and Multitasks	DEV ALOK 18377@PFF.UNKNOWN.ORG	SILICON CARBIDE LATERAL METAL-OXIDE SEMICONDUCTOR	US	09/961982	2003/03/26/01A1	US0617021
DE010268	07-Identification	KARL-RAGMAR RIEMSCHNEIDER@PHILIPS.COM	BIMODAL DISPLAY CONTROL FOR CHIP CARDS	DE	10146884	2003/03/26/01A1	US0702672
US018156	04-Audio/Video	GREG ROELOFS@PHILIPS.COM	RF SIGNAL-MEDIATED RASTERIZATION	US	09/961982	2003/03/26/01A1	US0702672
US018157	04-Audio/Video	CIARAN O'DONNELL 18329@PFF.PHILIPS.COM	SCALABLE HOME CONTROL PLATFORM AND ARCHITECTURE	US	09/966064	2003/03/26/01A1	US0639627
NL020421	01-CMOS and embedded p	LEON WINTERS@PHILIPS.COM	LIQUID FLOW CALIBRATION METHOD/APP. WITH CONDUCTIVITY	US	09/961126	2003/03/26/01A1	
NL010667	02-Packaging & Testing	BART VERMEULEN@PHILIPS.COM	ELECTRONIC DEVICE WITH MULTIPLE TEST INTERFACES	EP	2077111.9	2003/03/26/01A1	
US018152	05-Mobile Communication	LIN YUE 22456@PFF.UNKNOWN.ORG	FRAME ERROR RATE ESTIMATION IN A RECEIVER	EP	1203565.5	2003/03/26/01A1	US0688233
US018158	18 sep-01	GREG EHMAN@PHILIPS.COM	DATA COMMUNICATION BUS TRAFFIC GENERATOR	US	09/957890	2003/03/26/01A1	US0702672
NL010648	02-Packaging & Testing	CARRY EMONS@PHILIPS.COM	METHOD OF EXAM. SEMIC. WAFERS WITH X-RAYS	US	09/955704	2003/03/26/01A1	
NL010656	18 sep-01	PIM JEDELOO@PHILIPS.COM	RF SIGNAL SWITCH CIRCUIT	EP	1203536.7	2003/03/26/01A1	US0713360
NL020252	17 sep-01	VLADIMIR FIGARSKY 21878@PFF.UNKNOWN.ORG	CALCULATION OF IDENTIFIER CODES DISTRIBUTED AMONG	EP	1203528.3	2003/03/26/01A1	
GB010148	13 sep-01	RUUD BALKMENDI@PHILIPS.COM	IC WITH LOW-K MESOPOROUS SILICA LAYER	US	09/9656923	2003/03/26/01A1	
DE010267	15 sep-01	ARIAN STRUKER@PHILIPS.COM	FLYBACK CONVERTER WITH F-CONTROL VALLEY SWITCHING	EP	2075982.1	2003/03/26/01A1	
DE010266	15 sep-01	WALTER EINFELDT@PHILIPS.COM	COMPARATOR WITH PROGRAMMABLE HYSTERESIS	EP	1203508.5	2003/03/26/01A1	
GB010151	13 sep-01	REINHARD BUCHHOLD@PHILIPS.COM	CURRENT SENSOR	DE	10145655.5	2003/03/26/01A1	US0688233
GB010149	13 sep-01	KEVIN BOYLE@PHILIPS.COM	LOW SAR CAPACITIVELY BACK-COUPLED HANDSET	DE	10145655.7	2003/03/26/01A1	US0713360
GB010146	13 sep-01	ANNE CASWELL@PHILIPS.COM	ENVIRONMENT PROFILE BASED GPS ACQUISITION TECHNIQUE	GB	122228.4	2003/03/26/01A1	
NL010663	13 sep-01	JACOB NAM@PHILIPS.COM	ACTIVE TUNABLE FILTER FOR MULTI-BAND MOBILE COMMUNICA	GB	123227.2	2003/03/26/01A1	
GB010145	13 sep-01	JACOB NAM@PHILIPS.COM	ASCOT MOSFET GATE BUSBAR AND TERMINATION	GB	122122.8	2003/03/26/01A1	US0600000
NL010664	13 sep-01	STEVEN PEAKE@PHILIPS.COM	EDGE TERMINATION FOR RESURF TRENCH-GATE MOSFETS	GB	122120.9	2003/03/26/01A1	US0600000
GB010147	12 sep-01	RAY HUETING@PHILIPS.COM	A LOW IF, DOUBLE CONVERSION SUPERHET RECEIVER USING IM	GB	1232121.7	2003/03/26/01A1	US0600000
NL010635	11 sep-01	ALAN DAVIE@PHILIPS.COM	ELECTRONIC DEVICE WITH DATA COMMUNICATION BUS	EP	121899.3	2003/03/26/01A1	US0600000
DE010256	10 sep-01	ANDRE NIEUWLAND@PHILIPS.COM	DEVICE FOR STORING A COUNTER-VALUE IN AN EEPROM	DE	10144503.2	2003/03/26/01A1	US0600000
DE010255	7 sep-01	FRANK BOEHM@PHILIPS.COM	SDRAM WITH INCREASED DATA THROUGHPUT	DE	10144503.2	2003/03/26/01A1	US0600000
DE010254	7 sep-01	ROELAND HELINA@PHILIPS.COM	A MINIMUM DETECTOR ARRANGEMENT	EP	1203384.3	2003/03/26/01A1	US0600000
US010419	05-Automotive	FRANK BOEHM@PHILIPS.COM	BINARY COUNTER	DE	101501125	2003/03/26/01A1	US0600000
US010418	05-Discretes and Multitasks	PETER FURSMANN@PHILIPS.COM	COMMUNICATION SYSTEM WITH MEDIA ACCESS CONTROL	EP	1203384.3	2003/03/26/01A1	US0600000
DE010248	4 sep-01	JACOB NAM@PHILIPS.COM	VOLTAGE REGULATOR MODULE FOR HIGH INPUT SUPPLY	DE	10144070.7	2003/03/26/01A1	US0600000
US010415	05-Mobile Communication	JACOB NAM@PHILIPS.COM	POWER AMPLIFIER WITH VARIABLE OUTPUT MATCHING NETWORK	US	09/9468004	2003/03/26/01A1	US0600000
DE010246	4 sep-01	PETER HANK@PHILIPS.COM	COMMON HARDWARE ACCEPTANCE FILTER FOR CAN-BUS	DE	121390.9	2003/03/26/01A1	US0600000
US018135	31 aug-01	MARK GAJDA@PHILIPS.COM	EDGE TERMINATION BELOW CONNECTION FINGERS IN MOSFET	US	10145336.5	2003/03/26/01A1	US0600000
US018137	31 aug-01	ANDREW J. BLACK@PHILIPS.COM	SHORT CMP POLISH METHOD	US	09/9493719	2003/03/26/01A1	US0600000
FR010124	31 aug-01	STACY HALL@PHILIPS.COM	CONSTANT PH POLISH AND SCRUB	FR	111321	2003/03/26/01A1	US0600000
FR010123	29 aug-01	HUGUES DE PETHIUS@PHILIPS.COM	ARBITRATION OF MEMORY ACCESS	FR	111321	2003/03/26/01A1	US0600000
US018133	29 aug-01	JOACHIM REINER 18380@PFF.UNKNOWN.ORG	BUMP BRIDGE PROVIDING INTERNAL CONNECTIONS	EP	1120555.6	2003/03/26/01A1	US0600000
CH010016	29 aug-01	JENS ROEVER@PHILIPS.COM	SYNCHRONIZATION OF NON-SEQUENTIAL MOVING	US	09/941478	2003/03/26/01A1	US0600000
CH010022	29 aug-01	ZHENHUA WANG@PHILIPS.COM	TRANSMITTER WITHOUT A MODULATOR	EP	1120554.9	2003/03/26/01A1	US0600000





Table with columns for Patent ID, Date, Classification, Title, Applicant, and Inventor. Patent titles include 'FLASH MEMORY WAIT STATE CONTROLLER', 'GAAS HIT PA MODULE DESIGN FOR DCMA HANDSETS', 'METHOD AND APPARATUS FOR SEARCHING FOR A METHOD AND APPARATUS FOR EFFICIENT CALCULATING', 'ABSOLUTE TEMPERATURE DETECTION FOR AN ANALOGIC ELECTRICAL ISOLATION OF MULTIPLE INTEGRATED', 'LOW COMPLEXITY ADAPTIVE FILTER', 'A COMMUNICATION SYSTEM AND AN ARRANGEMENT APPARATUS WITH A TEST INTERFACE', 'CLUSTER WITH MUTUALLY DIFFERENT PROCESSORS + SINGLE C', 'DC FREE PSEUDO RANDOM GENERATOR', 'OFFSET DESENSITISATION AND SQUELCH REDUCER FOR EXPLAN', 'MAKING MOS-TRANSISTORS WITH DIFFERENT GATE OXIDE THICK', 'METHOD OF MAKING SEMICONDUCTOR DEVICE WITH NVM', 'ELECTRICAL COMPONENT BATCH TESTING', 'MAGNETIC SENSOR', 'TRANSCEIVER ARRANGEMENT', 'I.C. CELL AND LIBRARY IDENTIFICATION', 'NEW DECODER TEST FOR NONVOLATILE MEMORIES', 'CIRCUIT FOR GENERATING A DECODER-CLOCK-SIGNAL', 'DISTRIBUTED FIFO CHANNEL ADMINISTRATION', 'DYNAMICALLY CONTROLLING EXECUTION OF OPERATIONS WITH', 'CHANGE OF FLOW INSTRUCTIONS HAVING EXECUTION INFORMAT', 'UNKNN PROCESSOR WITH MEMORY ADDRESS BY VECTORIZED ADD', 'ROUNDING OPERATIONS IN COMPUTER PROCESSOR', 'ACCESSING INSTRUCTION INFORMATION IN A PROCESSOR', 'CONTROLLING A REPLACEMENT POLICY OF A CACHE', 'CACHING OPERATIONS IN A VLUX PROCESSOR', 'COMBINED COLOR SPACE MATRIX TRANSFORMATION AND', 'COLOR KEY PRESERVATION DURING SAMPLE RATE', 'PRIMARY CURRENT EMULATION', 'MULTIPLE INTERLEAVED INTEGRATED CIRCUIT', 'RECONFIGURABLE ANALOG CELL', 'GENERALIZED 12C SLAVE TRANSMITTER/RECEIVER STATE', 'LDMOS DEVICE WITH INTEGRATED DIODE TO IMPROVE RELIAB', 'LEVEL SHIFTER WITH INDEPENDENT GROUNDS AND EMI-ISOLAT', 'SILICON CARBIDE SCHOTTKY BARRIER DIODE AND METHOD OF', 'ACHIEVING HIGH INVERSION LAYER MOBILITY IN SiC METAL-O', 'ISOLATED P5TH LINE INTERFACE USING 3 CAPACITORS', 'LABEL WITH A DUAL PURPOSE ADHESIVE LAYER', 'NEW STRUCTURE FOR A WIDE BAND VCC', 'CANCELLING FOR CHANGING CHANNELS', 'CHIP POWER DETECT US', 'RF POWER AMPLIFIER IN SUB-MICRON', 'ESTIMATING BER IN CDMA SYSTEM USING POWER CONTROL B', 'FAST AND ACCURATE CACHE WAY SELECTION METHOD', 'ERROR CORRECTED FAULTY ADDRESS INFORMATION', 'ALGORITHM FOR DIVISION OR INVERSION OF A NUMBER', 'IMPROVED INPUT PAD NOISE & OUTPUT CHARACTERISTICS', 'CHARACTERIZING AN ACTIVE TRACK AND LATCH SENSE-AMP', 'ULTRA THIN SEMICONDUCTOR PACKAGE', 'SEMICONDUCTOR MODULE HAVING MULTIPLE SEMICONDUCTOR', 'INCREASED PEAK DETECTION ACCURACY', 'METHOD FOR SEPARATION OF THIN OBJECTS', 'DEEP INSULATION FOR RF CIRCUITS', 'ASYMMETRICAL RESONANT HALFBRIDGE POWER CONVERSION', 'DISTRIBUTION OF VCC STATE INFORMATION IN A SWITCH', 'VERFAHREN ZUR VALIDIERUNG EINES MODELLS FUER EINE ...'

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ST Microelectronics

REEL: 043951 FRAME: 0208



Patent No.	IPC Class	Inventor(s)	Title	Pub No.	Pub Date	Pub Type	Pub Loc	Pub Desc	Pub Desc
NL010370	04-Audio/Video	88-Discretes and Multimedia	SELF-REGULATING PIPELINED HIGH BANDWIDTH SIGMA-DELTA M...	1202196	09/07/2006	US	US069627E		
US018078	01-CMOS and embedded	KAMALL KOSHY 22253@PF.PHILIPS.COM	FAST CMOS ADDRESS WITH NULL-CARRY LOOK-AHEAD	09/07/2006	09/07/2006	US	US069628E		
NL010395	09-Discretes and Multimedia	STEVEN YAN.DUJK@PHILIPS.COM	PROGRAMMABLE DELAY BY EXPLOITING CROSSTALK	1202178.8	09/07/2006	EP	US069629A1		
US018076	04-Audio/Video	GREG EHMAN@PHILIPS.COM	FIFO BUFFER THAT CAN READ AND/OR WRITE MULTIPLE DATA	09/07/2006	09/07/2006	US	US069630A1		
FR010076	06-Connectivity	FABIO BRAZ@PHILIPS.COM	IMPROVED CURRENT DRIVER	1073489	09/07/2006	FR	US069631A1		
A1010034	07-Identification	JOSEF PREISHUBER-PFLUEGL@PHILIPS.COM	PHILIPS STORING OF INTERMEDIATE OPERATION STATES IN A TAG	1073489	09/07/2006	FR	US069632A1		
DE010160	02-Packaging & Testing	WOLFGANG SCHNITZ@PHILIPS.COM	IC WITH A BORDER OF DUCTILE MATERIAL	10126955.2	09/07/2006	DE	US069633A1		
US018069	01-Other	RICHARD KLEINHORST@PHILIPS.COM	DYNAMIC DATA CONTROL CIRCUIT	09/07/2006	09/07/2006	US	US069634A1		
NL010378	01-CMOS and embedded	LANDON VINES@PHILIPS.COM	A DIGITAL SYSTEM AND A METHOD FOR ERROR DETECTION THERE	1202080.5	09/07/2006	US	US069635A1		
US018074	03-Packaging & Testing	GREG EHMAN@PHILIPS.COM	CMP POLISHER SUBSTRATE REMOVAL CONTROL MECHANISM	09/07/2006	09/07/2006	US	US069636A1		
US018081	31-mel-01	LONGRICH GOFF 21237@PF.PHILIPS.COM	PARALLEL DATA COMMUNICATION HAVING MULTIPLE SYNC CODES	09/07/2006	09/07/2006	US	US069637A1		
US018072	04-Audio/Video	SANTANU DUTTA@PHILIPS.COM	SELECTIVE ACCESS TO MULTIPLE REGISTERS HAVING	09/07/2006	09/07/2006	US	US069638A1		
US018086	04-Audio/Video	CHRISTOPHER STOBART.16201@PF.PHILIPS.COM	RECONFIGURABLE DIGITAL FILTER HAVING MULTIPLE FILTER MC	09/07/2006	09/07/2006	US	US069639A1		
DE010153	05-Mobile Communication	ALLEN HE 23801@PF.PHILIPS.COM	USE OF AN IDENTITY DETECTOR BLOCK IN DECT SYSTEMS	101269420.8	09/07/2006	DE	US069640A1		
US018082	31-mel-01	MIPAL SANDULLEANU@PHILIPS.COM	METHOD AND APPARATUS FOR A COMPLEMENTARY	09/07/2006	09/07/2006	US	US069641A1		
NL010334	06-Connectivity	WAN SYVESTKA@PHILIPS.COM	PARALLEL DATA COMMUNICATION CONSUMING LOW POWER	1202082.2	09/07/2006	EP	US069642A1		
US018055	31-mel-01	NEAL WINGEN@PHILIPS.COM	AUTOMATIC UPGRADABLE UART CIRCUIT ARRANGEMENT	09/07/2006	09/07/2006	US	US069643A1		
US018064	31-mel-01	NEAL WINGEN@PHILIPS.COM	AN INTEGRATED CIRCUIT ARRANGEMENT WITH FEATURE	09/07/2006	09/07/2006	US	US069644A1		
US018075	31-mel-01	NEAL WINGEN@PHILIPS.COM	A CIRCUIT ARRANGEMENT AND METHOD FOR IMPROVING	09/07/2006	09/07/2006	US	US069645A1		
US018077	31-mel-01	NEAL WINGEN@PHILIPS.COM	A POWER AND FREQUENCY ADJUSTABLE UART DEVICE	09/07/2006	09/07/2006	US	US069646A1		
US018081	06-Connectivity	DC SESSIONS@PHILIPS.COM	PARALLEL COMMUNICATION BASED ON BALANCED DATA-BIT	09/07/2006	09/07/2006	US	US069647A1		
FR010072	05-Mobile Communication	SUNDAR RAMAN.17394@PF.PHILIPS.COM	VARING EARLY-LATE SPACING IN A DELAY LOCKED	09/07/2006	09/07/2006	FR	US069648A1		
FR010077	02-Packaging & Testing	JACKY DEROUAILT@PHILIPS.COM	EMBEDDED APPLICATION TEST	1073000	09/07/2006	FR	US069649A1		
US010264	29-mel-01	DAGNACHIEW BURR@PHILIPS.COM	COMPENSATE FEEDBACK LOOP DELAY PERFORMANCE LOSS	09/06/2006	09/06/2006	US	US069650A1		
US018071	05-Mobile Communication	DAVID DUPERRAY@PHILIPS.COM	A TRANSMITTER WITH A SLIDING COMPRESSION POINT	09/06/2006	09/06/2006	US	US069651A1		
NL010396	09-Discretes and Multimedia	ERWIN HILZEN@PHILIPS.COM	GATE CONNECTION FOR GATEOX FIRST TRENCH MOS	113143.2	09/06/2006	GB	US069652A1		
SGO10002	28-mel-01	TOHYEOW TENG@PHILIPS.COM	SUPPRESSION OF SUPPLY LINE RIPPLE NOISE	1201981.7	09/06/2006	EP	US069653A1		
NL010324	04-Audio/Video	FRANS DE JONG@PHILIPS.COM	TEST CIRCUIT	1201973.3	09/06/2006	EP	US069654A1		
FR010067	02-Packaging & Testing	STEPHANE MUTZ@PHILIPS.COM	MEMORY ACCESS REORDERING	106748	09/06/2006	FR	US069655A1		
FR010069	04-Audio/Video	STEPHANE MUTZ@PHILIPS.COM	INSTRUCTION FOR FAST CPU MEMORY ACCESS	106748	09/06/2006	FR	US069656A1		
GB010074	19-mel-01	KEVIN BOYLE@PHILIPS.COM	SMALL DUAL BAND HANDSET ANTENNA	112266.4	09/06/2006	GB	US069657A1		
DE010375	18-mel-01	MICHAEL KOHLMANN.2174@PF.PHILIPS.COM	PHILIPS METHOD FOR ROBUST DETECTION OF UARTS P-SCH	10124223.9	09/06/2006	DE	US069658A1		
US018058	05-Mobile Communication	PETER VAN DEN HAMER@PHILIPS.COM	SELF-DESCRIPTIVE RF TAG	1201982.8	09/06/2006	EP	US069659A1		
CH010013	17-mel-01	DENNIS KOJTSURES@PHILIPS.COM	PROPAGATION DELAY INDEPENDENT SDRAM DATA CAPTURE	09/05/2006	09/05/2006	US	US069660A1		
A1010030	07-Identification	ZHEN HUA WANG@PHILIPS.COM	PROTECTION OF INTERMETALLIC CONNECTIONS BY A FOIL	1112125.8	09/05/2006	EP	US069661A1		
A1010031	07-Identification	RAINER MOLL.22618@PF.PHILIPS.COM	CRIMP PROCESS USING A DAMPPED AREA	1890147	09/05/2006	EP	US069662A1		
A1010032	07-Identification	RAINER MOLL.22618@PF.PHILIPS.COM	LEAD-FRAME TAPE WITH AUXILIARY TAPE	1890148.8	09/05/2006	EP	US069663A1		
DE010145	07-Identification	WOLFGANG BURR@PHILIPS.COM	PROGRAMMABLE MULTILEVEL SCRAMBLING OF NV-MEMORIES	10124139.9	09/05/2006	DE	US069664A1		
DE010146	04-Audio/Video	MATTHIAS PETERS@PHILIPS.COM	CIRCUIT FOR COMBINING VIDEO DATA STREAMS	1201840.4	09/05/2006	EP	US069665A1		
NL010292	16-mel-01	DOMINE LEENARTS@PHILIPS.COM	RECONFIGURABLE DEVICE USING INVERTING PROPERTY	1201833.9	09/05/2006	EP	US069666A1		
NL010358	16-mel-01	KASIA LEUTENHUYK@PHILIPS.COM	DVB RECEIVER	10122830.8	09/05/2006	DE	US069667A1		
DE010142	11-mel-01	JOACHIM BRILKA@PHILIPS.COM	INTEGRATED DVB-FILTER	10122831.7	09/05/2006	DE	US069668A1		
US018058	02-Packaging & Testing	AXEL KATTNER@PHILIPS.COM	METHOD FOR MEASURING FUSE RESISTANCE IN A FUSE	09/05/2006	09/05/2006	US	US069669A1		
DE010138	10-mel-01	ELIE KHOURY@PHILIPS.COM	ANTI DEMODULATOR	09/05/2006	09/05/2006	DE	US069670A1		
FR010058	10-mel-01	BURKHARD DICK@PHILIPS.COM	MICROPROCESSOR SUITED FOR MOBILES PHONES AND OTHER P	10122748.5	09/05/2006	FR	US069671A1		
US018061	05-Mobile Communication	ARNAUD ROSAY@PHILIPS.COM	A SECURE POLY FUSE WITH A POWER-ON OR ON-PRESET	10122481.4	09/05/2006	US	US069672A1		
US010336	07-Identification	ELIE KHOURY@PHILIPS.COM	SYSTEM CLOCK MULTIPLEXER	09/05/2006	09/05/2006	US	US069673A1		
US010366	06-Connectivity	CHRISTOPH LAMMERS@PHILIPS.COM	ADDRESS-GENERATING ARRANGEMENT	10122399.9	09/05/2006	DE	US069674A1		
US010367	10-Other	UMA PORST@PHILIPS.COM	VARIABLE AND FAIL-SAFE SYSTEM RESET	10121935	09/05/2006	US	US069675A1		
DE010126	5-mel-01	RAJEEV SETHI.21430@PF.PHILIPS.COM	A KEYPAD DEVICE SECURITY SYSTEM AND METHOD	10121745.5	09/05/2006	DE	US069676A1		
US010368	07-Identification	MATTHIAS NUTH@PHILIPS.COM	SWITCHED ANTENNAS FOR RE-IDENTIFICATION	10121745.5	09/05/2006	DE	US069677A1		
US010369	07-Identification	ELIE KHOURY@PHILIPS.COM	POWER SUPPLY ARRANGEMENT FOR MIXED-SIGNAL IC'S	116494.6	09/05/2006	GB	US069678A1		
NL010275	1-mel-01	JOHN HUGHES@PHILIPS.COM	IMPROVED BACK-END HOMOGENEITY IN METAL LAYER	10121748.5	09/05/2006	DE	US069679A1		
US010219	05-Mobile Communication	SIFEN LIU.17623@PF.UNKNOWN.ORG	APPARATUS AND METHOD FOR TUNING AN INTER-STAGE	1208980	09/05/2006	GB	US069680A1		
US010218	09-Discretes and Multimedia	STEVEN PEAKE@PHILIPS.COM	ASCOT MOSFET PLANARISED TEGS CAPPING	1208981.6	09/05/2006	GB	US069681A1		
GB010212	28-apr-01	STEVEN PEAKE@PHILIPS.COM	WET ETCHING OF SIN DR REGION AT T ABOVE 100 DEGREES C	1208987.6	09/05/2006	GB	US069682A1		
NL010233	27-apr-01	STEVEN PEAKE@PHILIPS.COM	REMOVAL IN RC WITH HF SOLUTION	10120200141.1	09/05/2006	WO	US069683A1		
NL010238	27-apr-01	MARTIN KNOTT@PHILIPS.COM	SWITCH IN THE BANDPASS	10120200141.1	09/05/2006	WO	US069684A1		
SGO10004	27-apr-01	KWONG KAM CHOON@PHILIPS.COM	IMPROVEMENT OF TIS RESISTANCE	1201639.4	09/05/2006	EP	US069685A1		
NL010254	26-apr-01	GERARD VAN DE VEN@PHILIPS.COM	ON CHIP TRIMMING CAPACITANCE WITH BONDING OPTION	1890123.1	09/05/2006	EP	US069686A1		

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Patent No.	Class	Title	Inventor	Pub No.	Pub Date
DE010114	05-RF Devices	DUAL BAND BAND FILTER	OLAF WUNNICKE@PHILIPS.COM	1201847.3	US6810830
CH010011	05-Mobile Communication	BIST FOR FLASH MEMORIES-ACCELERATED TESTING OF ...	STEFFEN GAPPISCH@PHILIPS.COM	1110213.4	US6810831
DE010112	07-Identification	SPICE DETECTOR	WALTER EINFELDT@PHILIPS.COM	10120142.7	US6810832
DE010113	07-Identification	IMPROVED ERROR CORRECTION SCHEME FOR USE IN FLASH MEME	ERNST BRETSCHNEIDER@PHILIPS.COM	10120147.8	US6810833
CH01001C	04-Audio/Video	METHOD FOR RETRIEVING OBJECT-RELATED INFORMATION	STEFFEN GAPPISCH@PHILIPS.COM	1019893.8	US6810834
DE010108	01-Other	TRENCH 2T FLASH CELL	MICHAEL LITTEBER@PHILIPS.COM	1201454.8	US6810835
NL010242	01-CMOS and embedded p	DEVICE FOR SYNCHRONISING A RECEIVER AND A TRANSMITTER	ROB VERHAAR@PHILIPS.COM	1401009.4	US6810836
FR010053	05-Mobile Communication	POLYPHASE NOTCHFILTER	CAROLINE STEHLE@PHILIPS.COM	109518.3	US6810837
DE010105	04-Audio/Video	ONE-STEP CONTACT-METAL INTERCONNECTION PROCESS	BURKHARD DICK@PHILIPS.COM	1201392	US6810838
FR010050	01-CMOS and embedded p	BUS MEMORY MINI GATEWAY UNIT	MARC DURANTON@PHILIPS.COM	105022	US6810839
NL010226	04-Audio/Video	TUNABLE RING OSCILLATOR HAVING PARALLEL STAGES	ROELAND HELINA@PHILIPS.COM	1201341.3	US6810840
FR010052	04-Audio/Video	HIGH DUTY CYCLE OFFSET CANCELLATION CIRCUIT	ANDREA MILANESI@PHILIPS.COM	1098017.2	US6810841
NL010213	04-Audio/Video	PICTURE SIGNAL CONTRAST CONTROL	JEROEN STESSEN@PHILIPS.COM	1201334.8	US6810842
CH01000E	05-Connectivity	DISTRIBUTED ARBITRATION AND SCHEDULING ALGORITHM (#)	JEROEN STESSEN@PHILIPS.COM	10118126.4	US6810843
NL010227	04-Audio/Video	DETECTOR CIRCUIT WITH POWER SUPPLY TEST INTERFACE	DRIES VAN WAGENINGEN@PHILIPS.COM	1201336.8	US6810844
DE010091	02-Packaging & Testing	IMPROVED BIT-ERROR TOLERANCE	FRANS DE JONG@PHILIPS.COM	1201304.1	US6810845
NL010207	04-Audio/Video	PHASE-SMEARING PROCESSING	ERCAN GIG@PHILIPS.COM	1201300.9	US6810846
NL010203	04-Audio/Video	A CLAMPING CIRCUIT	ERCAN GIG@PHILIPS.COM	1201301.7	US6810847
NL010204	04-Audio/Video	METHOD FOR IMPROVING SUBSTRATE ALIGNMENT	PETER STROET 1804@PF.PHILIPS.COM	090829105	US6810848
NL010205	04-Audio/Video	DETECTION OF AN OPTICAL TRANSPARENT GLASS SUBSTRATE	PETER G. BLANKEN@PHILIPS.COM	090829339	US6810849
US010186	05-Mobile Communication	RESET CIRCUIT AND METHOD THEREFOR	ABOL FAZI KHOSROWBEYG@PHILIPS.COM	1201289.3	US6810850
NL010228	09-Discrete and Multimat	ELECTROVALVE	JOHANN FOCK@PHILIPS.COM	0908287625	US6810851
US018048	05-Mobile Communication	INITIALIZATION SCHEME FOR A HYBRID FREQUENCY-TIME	RUNE JENSEN@PHILIPS.COM	090828570	US6810852
US010187	05-Mobile Communication	DYNAMICALLY CONFIGURABLE PAGE TABLE FOR USE IN	PAUL ILLEGEMS 1852@PF.PHILIPS.COM	090825974	US6810853
DE010095	05-Other	GENERATING RECOMMENDATION SCORES	ERIC MEULENKAMP@PHILIPS.COM	1201228.2	US6810854
US018045	05-Mobile Communication	ADAPTIVE CHIP-EQUALIZERS FOR SYNCHRONOUS DS-CSS	CLIVE TAYLOR 2074@PF.PHILIPS.COM	090822720	US6810855
US018046	05-Mobile Communication	LOW CURRENT CLOCK SENSOR	DAGHACHEW BURRU@PHILIPS.COM	090822454	US6810856
DE010078	05-Mobile Communication	A SPECIAL ADJUC STRUCTURE TO DETECT AND GENERATE	VISHAL ANAND 2110@PF.PHILIPS.COM	090822368	US6810857
GB010052	09-Discrete and Multimat	GSM-PAGER	WIEBE DE BOER@PHILIPS.COM	090821277	US6810858
FR010036	04-Audio/Video	AUTONOMOUS SYNCHRONOUS RECTIFIER	WOLFGANG WIPPICH@PHILIPS.COM	090823417	US6810859
FR010037	04-Audio/Video	CONTOUR ENHANCEMENT METHOD USED IN A CAMERA	FREDERIC BOMPARD@PHILIPS.COM	090823160	US6810860
FR010038	04-Audio/Video	FILTERING PICTURE METHOD WITH DEFECT PIXEL CORRECTION	FREDERIC BOMPARD@PHILIPS.COM	120115343	US6810861
DE010079	07-Identification	IMAGE RECONSTRUCTION METHOD USED IN CAMERA	FREDERIC BOMPARD@PHILIPS.COM	1048108	US6810862
NL010176	08-Connectivity	OBSCURING TRAFFIC ON DATA AND ADDRESS LINES	RAUF MALZHAR@PHILIPS.COM	104110	US6810863
DE010017	07-Identification	IMAGE SENSOR OUTPUT SIGNAL IS WEIGHTED SUM OF SENSOR	MARU KOLE@PHILIPS.COM	1201118.7	US6810864
US010044	09-Discrete and Multimat	WEDGE ON BUMP-REVERSE BOND	JOACHIM SCHOBER@PHILIPS.COM	1201113.6	US6810865
FR010039	04-Audio/Video	SOI LATERAL MOS WITH LOW CGD & GATE PROTECTION	STEVEN PEAKE@PHILIPS.COM	107408.7	US6810866
US018033	09-Discrete and Multimat	RESURF LATERAL MOS WITH LOW CGD & GATE PROTECTION	STEVEN PEAKE@PHILIPS.COM	107408.7	US6810867
NL010173	01-CMOS and embedded p	LOW COST PULSE WIDTH MODULATOR (PWM)	BILL HOUGHTON@PHILIPS.COM	107408.7	US6810868
NL010175	01-CMOS and embedded p	IC'S WITH A W METAL - W PLUG METALLIZATION	ALBERT VAN DE GOOR@PHILIPS.COM	090815145	US6810869
DE010068	04-Audio/Video	HEADSET WITHOUT A BOOM FOR A MICROPHONE	HEINZ RENNER@PHILIPS.COM	1201050	US6810870
US010082	05-Mobile Communication	ELECTRONIC DEVICE	MAREIKE KLEE@PHILIPS.COM	1048009.1	US6810871
US010171	05-Mobile Communication	A LOW-COST HIGH-SPEED MULTIPLEXER/ACCUMULATOR UNIT	DAGHACHEW BURRU@PHILIPS.COM	60127736	US6810872
DE010067	08-Identification	CIRCUIT AND METHOD FOR INPUT SIDE IMPEDANCE	VICKRAM VATHYALA@PHILIPS.COM	090812437	US6810873
US010066	05-Automotive	TRANSPONDER WITH DOUBLE-FLAIED SUBSTRATE AS AN ENVEL	BEN BORDES@PHILIPS.COM	090811647	US6810874
FR010040	05-Mobile Communication	COMMUNICATION SYSTEM WITH BAD TIME SLOT IDENTIFICATION	WOLFGANG O. BUDDER@PHILIPS.COM	1200884.1	US6810875
FR010041	04-Audio/Video	REAL TIME CLOCK CONTROLLED BY SYSTEM CLOCK	ANDREAS BERING@PHILIPS.COM	10112895.6	US6810876
FR010042	05-Connectivity	DATA CACHE WAY PREDICTION SCHEME BASED ON	JAN-WILLEM VAN DE WAERDT@PHILIPS.COM	10112373.6	US6810877
DE010064	07-Identification	USER-DATA TRANSMISSION DURING ANTI-COLLISION PROCEDURE	FRANZ AMTMANN@PHILIPS.COM	090803384	US6810878
NL010145	09-Discrete and Multimat	SELF-DIAGNOSIS FOR ANGULAR MEASUREMENT SYSTEMS	MARCUS WESER@PHILIPS.COM	1890073.8	US6810879
NL010146	08-Automotive	LINE END OF TRANSMISSION DETECTOR	CLEMENS DE HAAS@PHILIPS.COM	10112352.3	US6810880
NL010150	08-Automotive	ACTIVE STAR NODE FOR AUTOMOTIVE BUS	CLEMENS DE HAAS@PHILIPS.COM	1200940.8	US6810881
US018008	09-Other	BYTE CODED INSTRUCTION PROCESSOR WITH SWITCH	HENK BOEZEM@PHILIPS.COM	1200898.8	US6810882
DE010056	04-Audio/Video	INTEGRATED TV INTERFACE MODULE	BONNIE SEXTON@PHILIPS.COM	1200805.8	US6810883
US010054	02-Packaging & Testing	DEVICE AND METHOD FOR TESTING IC'S	HENK VAN DER WILST@PHILIPS.COM	090602584	US6810884
US010058	05-RF Devices	DUAL MODE (BAND) DS-PFA	FRIEDRICH HAPKE@PHILIPS.COM	1185737.9	US6810885
US010059	05-RF Devices	DUAL MODE (BAND) DS-PFA	KEVIN BOYLE@PHILIPS.COM	10110777.3	US6810886
				105440.2	US6810887

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Pub No	App No	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass	IPC Class	IPC Subclass
US20100332	US20100332	G03-RF Devices	03-RF Devices	MATCHED PLANAR INVERTED L ANTENNA	GB	105441	202102071541.A1	US200907201	US6867411										
US20100158	US20100158	06-Connectivity	06-Connectivity	ANTENNA INTEGRATION IN RF MODULE	EP	1207784.4	2002071847.A1	US200807131	US5862731										
US20100045	US20100045	04-Audio/Video	04-Audio/Video	DIGITAL SIGNAL PROCESSOR INTERRUPT ACCELERATOR	US	037750271	2002060152.A2	US200748472	US6524842										
FR0110021	FR0110021	05-Mobile Communication	05-Mobile Communication	SOI LDMOS DEVICES WITH LOW GATE TO DRAIN	US	037945682	2002060152.A2	US200698389.A2	US6198803										
FR0110022	FR0110022	06-Connectivity	06-Connectivity	MAPPING OF MPEG-4 PARTITIONS	EP	140060445	2002060152.A1	US200698389.A2	US6198803										
AT1010007	AT1010007	07-Identification	07-Identification	EXTERNAL ACCESS CONTROLLER FOR A DIGITAL TV SYSTEM	FR	102864	232412.A1	US20057416.A1	US6194723										
CH0100006	CH0100006	02-Packaging & Testing	02-Packaging & Testing	PROCESSOR CONTROL MODULES INSTEAD OF CHIPS	EP	1890883.8	2002060152.A1	US20057416.A1	US6194723										
NL0101119	NL0101119	04-Audio/Video	04-Audio/Video	TURBO DECODER SYSTEM COMPRISING PARALLEL DECODERS	DE	120885.6	2002060152.A1	US20057416.A1	US6194723										
US20100021	US20100021	05-Mobile Communication	05-Mobile Communication	SELF-ADAPTIVE START-UP SEQUENCE FOR VDI TAGS MULTIPLIER DE	EP	1200668.4	2002060152.A1	US20057416.A1	US6194723										
DE0105649	DE0105649	09-Discretizes and Multimarks	09-Discretizes and Multimarks	MAGNETIC SENSOR FOR LINEAR MOVEMENTS	US	091792325	2002060152.A1	US20057416.A1	US6194723										
GB0101813	GB0101813	09-Discretizes and Multimarks	09-Discretizes and Multimarks	MOSFET PRECISION CURRENT LIMIT MEASURE SCHEME	DE	10108732.2	2002060152.A1	US20057416.A1	US6194723										
DE0100038	DE0100038	05-Mobile Communication	05-Mobile Communication	TRENCHED SEMI-INSULATOR DEVICES WITH ALIGNMENT	GB	126598.2	2002060152.A1	US20057416.A1	US6194723										
DE0100647	DE0100647	05-Mobile Communication	05-Mobile Communication	RAKE RECEIVER FOR UMTS DOWNLINK MULTICODE RECEPTION	GB	10443.1	2002060152.A1	US20057416.A1	US6194723										
US20100116	US20100116	05-Mobile Communication	05-Mobile Communication	INITIAL SYNCHRONISATION FOR A DIGITAL RADIO RECEIVER	DE	101084137.7	2002060152.A1	US20057416.A1	US6194723										
NL0101085	NL0101085	05-Mobile Communication	05-Mobile Communication	A SIGNAL DISCRIMINATOR FOR A SPREAD SPECTRUM SYSTEM	US	09782147	2002060152.A1	US20057416.A1	US6194723										
DE0100046	DE0100046	07-Identification	07-Identification	CALIBRATION OF IN-PHASE AND QUADRATURE TRANSMIT BRANCHES	US	097792146	2002060152.A1	US20057416.A1	US6194723										
US201001812	US201001812	08-Discretizes and Multimarks	08-Discretizes and Multimarks	COMMON MODE DEPENDENT LEVEL SHIFTER FOR SOI/SOHNET	EP	1200819.3	2002060152.A1	US20057416.A1	US6194723										
US20100007	US20100007	08-Discretizes and Multimarks	08-Discretizes and Multimarks	METHOD FOR BREAK-SOURCE DETECTION	EP	1010107.3	2002060152.A1	US20057416.A1	US6194723										
JPO100022	JPO100022	08-Discretizes and Multimarks	08-Discretizes and Multimarks	CYCLOCALLY SEQUENTIAL MEMORY PREFETCH	US	097868891	2002060152.A1	US20057416.A1	US6194723										
US201001801	US201001801	08-Discretizes and Multimarks	08-Discretizes and Multimarks	CYCLOCALLY SEQUENTIAL MEMORY PREFETCH	US	097789892	2002060152.A1	US20057416.A1	US6194723										
US201001814	US201001814	05-Mobile Communication	05-Mobile Communication	MULTIPLE CHANNEL JOINT DECODING AT MOBILE	US	097765988	2002060152.A1	US20057416.A1	US6194723										
NL0101085	NL0101085	05-Mobile Communication	05-Mobile Communication	ELECTRONIC DEVICE AND MANUFACTURING METHOD THEREOF	JP	01-40309	2002060152.A1	US20057416.A1	US6194723										
DE0100046	DE0100046	07-Identification	07-Identification	POWER SUPPLY ON MICROELECTRONIC CHIP	JP	01-40310	2002060152.A1	US20057416.A1	US6194723										
US201001801	US201001801	05-Mobile Communication	05-Mobile Communication	BUS BANDWIDTH CONSUMPTION PROFILER	DE	10107639	2002060152.A1	US20057416.A1	US6194723										
DE0100069	DE0100069	05-Mobile Communication	05-Mobile Communication	TRENCHED SEMI-INSULATOR DEVICES WITH EDGE TERMINATION	US	097785845	2002060152.A1	US20057416.A1	US6194723										
US201001802	US201001802	05-Mobile Communication	05-Mobile Communication	METHOD AND ARRANGEMENT FOR INSTRUCTION WORD GENERATION	GB	1037115.9	2002060152.A1	US20057416.A1	US6194723										
US201001803	US201001803	03-RF Devices	03-RF Devices	SELF RESONANT CAPACITIVELY BACK-COUPLED HANDSET	GB	104465	2002060152.A1	US20057416.A1	US6194723										
AT1010004	AT1010004	03-RF Devices	03-RF Devices	TUNING METHOD FOR ANTENNAS IN MODULES	EP	1208502.1	2002060152.A1	US20057416.A1	US6194723										
US201001803	US201001803	05-Mobile Communication	05-Mobile Communication	MEMBRANE COMBINED WITH A FASTENING RING	EP	18880037.3	2002060152.A1	US20057416.A1	US6194723										
US201001804	US201001804	05-Mobile Communication	05-Mobile Communication	PROGRAMMABLE PULSE-WIDTH MODULATION MODULE	US	08781473	2002060152.A1	US20057416.A1	US6194723										
NL0100988	NL0100988	06-Connectivity	06-Connectivity	LC CONTROLLABLE OSCILLATOR-PHASE SHIFTING	US	1200475	2002060152.A1	US20057416.A1	US6194723										
DE0100036	DE0100036	05-Mobile Communication	05-Mobile Communication	ZERO POWER STAND-BY CHIP	EP	1000132.3	2002060152.A1	US20057416.A1	US6194723										
NL0100668	NL0100668	02-Packaging & Testing	02-Packaging & Testing	SEMICONDUCTOR DEVICE ON FLEX FOIL	DE	10106123.3	2002060152.A1	US20057416.A1	US6194723										
DE0100358	DE0100358	05-Mobile Communication	05-Mobile Communication	METHOD AND APPARATUS FOR FINITE FIELD MULTIPLICATION	DE	10106085.8	2002060152.A1	US20057416.A1	US6194723										
US201001806	US201001806	05-Mobile Communication	05-Mobile Communication	IMPLEMENTATION OF FAST WRITE 2X4X TO MINIMALLY	US	097778853	2002060152.A1	US20057416.A1	US6194723										
NL0100085	NL0100085	04-Audio/Video	04-Audio/Video	PIPELINE INSERTION MECHANISM FOR DSP	EP	1200442.5	2002060152.A1	US20057416.A1	US6194723										
GB0100076	GB0100076	05-Mobile Communication	05-Mobile Communication	METHOD OF ACQUIRING GPS SIGNALS WITH A LOW SNR	GB	102883	2002060152.A1	US20057416.A1	US6194723										
US201001807	US201001807	05-Mobile Communication	05-Mobile Communication	METHOD OF ACQUIRING GPS SIGNALS WITH A LOW SNR	GB	103228.3	2002060152.A1	US20057416.A1	US6194723										
GB0100077	GB0100077	05-Mobile Communication	05-Mobile Communication	CONTROL OF SYNC IC-DC CONVERTER USING SWITCH NODE RE	GB	112589.2	2002060152.A1	US20057416.A1	US6194723										
US201001808	US201001808	05-Mobile Communication	05-Mobile Communication	LOGIC INPUT SENSING FET, E.G. FOR SYNC. DC-DC CONVERTER	GB	112583	2002060152.A1	US20057416.A1	US6194723										
US201001809	US201001809	05-Mobile Communication	05-Mobile Communication	MOSFET DRIVER WITH DUAL FLOATING GATES, LOCAL SOURCE C	GB	112884.8	2002060152.A1	US20057416.A1	US6194723										
US201001810	US201001810	05-Mobile Communication	05-Mobile Communication	ULTRAFAST PN DIODES WITH TRENCH STRUCTURE	GB	102754.1	2002060152.A1	US20057416.A1	US6194723										
US201001811	US201001811	05-Mobile Communication	05-Mobile Communication	DIVERSITY FOR CAPACITIVELY BACK-COUPLED HANDSET	GB	102768.8	2002060152.A1	US20057416.A1	US6194723										
US201001812	US201001812	05-Mobile Communication	05-Mobile Communication	IMPROVED PROGRAMMABLE CHARGE PUMP	EP	1102381.7	2002060152.A1	US20057416.A1	US6194723										
US201001813	US201001813	05-Mobile Communication	05-Mobile Communication	QUADRATURE RING LC-COIL DRIVER	EP	1200387.9	2002060152.A1	US20057416.A1	US6194723										
US201001814	US201001814	05-Mobile Communication	05-Mobile Communication	POTENTIAL-FREE MEASUREMENT OF HIGH CURRENTS	EP	10104453.4	2002060152.A1	US20057416.A1	US6194723										
US201001815	US201001815	05-Mobile Communication	05-Mobile Communication	DETERMINATION OF ANGLES LARGER THAN 180	DE	10104116	2002060152.A1	US20057416.A1	US6194723										
US201001816	US201001816	05-Mobile Communication	05-Mobile Communication	BUS-SYSTEM WITH LOW-POWER-PHASE	DE	10103323	2002060152.A1	US20057416.A1	US6194723										
US201001817	US201001817	05-Mobile Communication	05-Mobile Communication	FRONT END AND HF RECEIVER HAVING QUADRATURE LINA	EP	1300238.2	2002060152.A1	US20057416.A1	US6194723										
US201001818	US201001818	05-Mobile Communication	05-Mobile Communication	TUNABLE INTEGRATED RF FILTER HAVING SWITCHED FET CAPAC	EP	1200239	2002060152.A1	US20057416.A1	US6194723										
US201001819	US201001819	05-Mobile Communication	05-Mobile Communication	CAN TRANSCIEVER WITH MEANS FOR AN ERROR-MANAGEMENT	DE	101867.4	2002060152.A1	US20057416.A1	US6194723										
US201001820	US201001820	05-Mobile Communication	05-Mobile Communication	DIFFERENTIALLY SLOTTED PLANAR INVERTED F ANTENNA	GB	101882.2	2002060152.A1	US20057416.A1	US6194723										
US201001821	US201001821	05-Mobile Communication	05-Mobile Communication	MOBILE DEVICE INCORPORATING A GPS RECEIVER	EP	1200336.9	2002060152.A1	US20057416.A1	US6194723										
US201001822	US201001822	05-Mobile Communication	05-Mobile Communication	A DIGITALLY CONTROLLED DC/DC CONVERTER	GB	101859.5	2002060152.A1	US20057416.A1	US6194723										
US201001823	US201001823	05-Mobile Communication	05-Mobile Communication	SELF-ALIGNED TRENCH MOS FABRICATION	EP	101659.5	2002060152.A1	US20057416.A1	US6194723										
US201001824	US201001824	05-Mobile Communication	05-Mobile Communication	FREQUENCY COMPENSATION OF A DIFFERENTIAL AMPLIFIER	DE	10102622.8	2002060152.A1	US20057416.A1	US6194723										
US201001825	US201001825	05-Mobile Communication	05-Mobile Communication	REAL-TIME CHANNEL CALIBRATION METHOD	US	091786308	2002060152.A1	US20057416.A1	US6194723										
US201001826	US201001826	05-Mobile Communication	05-Mobile Communication	POWER TRANSISTOR WITH INTERNAL LOW-PASS AND BAND-PASS	EP	1200168.1	2002060152.A1	US20057416.A1	US6194723										
US201001827	US201001827	05-Mobile Communication	05-Mobile Communication	MULTI-OUTPUT DC/DC UP/DOWN - CONVERTER	EP	1200158.9	2002060152.A1	US20057416.A1	US6194723										
US201001828	US201001828	05-Mobile Communication	05-Mobile Communication	DC/DC CONVERTER WITH SINGLE LEVEL REFERENCE	EP	1200170.7	2002060152.A1	US20057416.A1	US6194723										
US201001829	US201001829	05-Mobile Communication	05-Mobile Communication	ANALOG FILTER WITH VERY LOW DISTORTIONS AND OUT-OFF FRE	EP	1200172.3	2002060152.A1	US20057416.A1	US6194723										
US201001830	US201001830	05-Mobile Communication	05-Mobile Communication	DC/DC CONVERTER WITH SECOND OUTPUT IN PFM MODE	EP	1200158.5	2002060152.A1	US20057416.A1	US6194723										

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Patent No.	Date	Class.	Inventor	Applicant	Title	Pub. No.	Pub. Date
NLD00057	12-jan-01	04-Audio/Video	EGBERT, JASPER@PHILIPS.COM	PHILIPS	ADDRESS MAPPING BASED ON ACTUAL DATA BLOCKS	1206205.9	12/06/05
NLD00113	11-jan-01	05-Mobile Communication	MART COENEN@PHILIPS.COM	PHILIPS	GRADUAL CLOCK ENABLING TO ESTABLISH CORE SUPPLY CURRENT	1200084	12/00/04
CH0100203	10-jan-01	05-Mobile Communication	ROLF BECKER@PHILIPS.COM	PHILIPS	LOCAL SUPPLY GENERATOR	1100534.5	11/05/04
NLD0012020	10-jan-01	06-Connectivity	MIHAL SANDULEANU@PHILIPS.COM	PHILIPS	TIA FOR NETWORK SWITCH	1200864.2	12/08/04
GB010002	4-jan-01	05-Mobile Communication	ADRIAN PAYNE@PHILIPS.COM	PHILIPS	IMPROVEMENTS IN VARIABLE THRESHOLD SLICER CIRCUITS	1003072.1	10/03/01
US0003362	23-dec-00	05-Mobile Communication	D. GIANNOPoulos@PHILIPS.COM	PHILIPS	CONTROL METHOD AND APPARATUS FOR FLYBACK CONVERTER	08732144	08/27/01
FR000155	28-dec-00	04-Audio/Video	ROBIN LIDIERER@PHILIPS.COM	PHILIPS	IP IN DIFFERENT MEMORY ACCESS CONTEXT	4023699.2	04/23/00
US000078	27-dec-00	04-Audio/Video	KENNETH GRAY 21242@PF.PHILIPS.COM	PHILIPS	TECHNIQUES TO ASYNCHRONOUSLY OPERATE A SYNCHRONOUS 2-D RAMP RECEIVER	09748871	09/27/00
FR000145	26-dec-00	03-RF Devices	HERVE JACOB@PHILIPS.COM	PHILIPS	LOW-VOLTAGE, ELECTRICALLY PROGRAMMABLE SUBMICRON N/A APPARATUS AND METHOD TO IMPROVE METAL CMP	2047768.8	20/04/00
NL000730	22-dec-00	01-CMOS and embedded pr	GIUOGLIO TAO@PHILIPS.COM	PHILIPS	TWO SOLUTIONS FOR DEFINING A FREELY PROGRAMMABLE DISPLAY CHARGE PUMP WITH ADAPTIVE PHASE GENERATION	128445.4	12/84/05
CH000032	22-dec-00	04-Audio/Video	ADAM SMITH@PHILIPS.COM	PHILIPS	SHARING AUTHORISATION BETWEEN MULTIPLE TV'S IN HOME DVD IC ARCHITECTURE	11064819.3	11/06/04
GB000182	22-dec-00	04-Audio/Video	FRANCESCO BRANI 22089@PF.PHILIPS.COM	PHILIPS	DIVIDED SERIAL DATABUS, NEW BIT CODING	31437.7	03/14/00
GB000183	22-dec-00	04-Audio/Video	MIKE D. JAMES@PHILIPS.COM	PHILIPS	MULTI-BEAM DVD ROM IC ARCHITECTURE	31436.9	03/14/00
GB000184	22-dec-00	04-Audio/Video	TREVOR HALL@PHILIPS.COM	PHILIPS	MULTI-BEAM DVD ROM IC ARCHITECTURE	31435.3	03/14/00
NL000745	22-dec-00	08-Discrete and Multimeter	RAF ROOYERS@PHILIPS.COM	PHILIPS	FOLDED PTAT CURRENT SOURCING	204796.7	02/04/00
US000494	22-dec-00	09-Discrete and Multimeter	VICKRAM VATHIYALA@PHILIPS.COM	PHILIPS	FOLDED PTAT CURRENT SOURCING	08747121	08/27/00
NL000722	20-dec-00	05-Mobile Communication	FRANS WIDERSHOVEN@PHILIPS.COM	PHILIPS	COMPACT CASCODE RADIO FREQUENCY CMOS POWER	204652.2	20/12/00
NL000723	20-dec-00	04-Audio/Video	ERIK VAN DER VEN@PHILIPS.COM	PHILIPS	WOM CODE FLASH MEMORY	304651.4	03/04/00
AT000071	20-dec-00	08-Connectivity	REINHARD MEINDL@PHILIPS.COM	PHILIPS	STICK A "SIM" SMART LABEL ON A MOBILE PHONE	880381.7	08/30/00
AT000072	20-dec-00	07-Identification	ANDREAS MUEHLBERGER@PHILIPS.COM	PHILIPS	MOBILE PHONE WITH A TAG ON A PHONE ACCESSORY	880382.5	08/30/00
NL000687	18-dec-00	04-Audio/Video	EDUARD STIKWIK@PHILIPS.COM	PHILIPS	0 + 90 DEGREE NETWORK FOR LOCAL OSC SIGNAL	204631.6	20/12/00
US000388	18-dec-00	05-Mobile Communication	TIRADAD SOWLATI 20216@PF.PHILIPS.COM	PHILIPS	LINEARIZED CLASS C RF AMPLIFIER WITH DYNAMIC MULTIPLE FEED (IN BAND) FILTERING FOR IN-BAND GPS CW REJECTOR	09739500	09/17/00
GB000176	16-dec-00	03-RF Devices	KEVIN BOYLE@PHILIPS.COM	PHILIPS	MULTIPLE FEED (IN BAND) FILTERING FOR IN-BAND GPS CW REJECTOR	30741.3	03/07/00
GB000174	15-dec-00	04-Audio/Video	SEBASTIAN EGNER@PHILIPS.COM	PHILIPS	WOM CODE MEMORY	204699.5	02/04/00
US000680	15-dec-00	09-Discrete and Multimeter	DAVID HARDY 22130@PF.PHILIPS.COM	PHILIPS	OPTIMISING ACTIVE AREAS OF E.G. DOUBLE-DIODES	30595.3	03/05/00
US000682	15-dec-00	09-Discrete and Multimeter	ANAND GANESAN 22361@PF.PHILIPS.COM	PHILIPS	PULSED D-FLIP-FLOP USING DIFFERENTIAL CASCODE SWITCH	08738781	08/27/00
AT000069	13-dec-00	01-CMOS and embedded pr	PIERRE LEROUX@PHILIPS.COM	PHILIPS	A SELF-COMPENSATING MARK DESIGN FOR STEPPER SUB-RECTIVE BASS IN RINGING TONES OF MOBILE PHONES	09737606	09/27/00
DE000224	12-dec-00	05-Mobile Communication	CARL POLDY@PHILIPS.COM	PHILIPS	SWITCHING BASS IN RINGING TONES OF MOBILE PHONES	093371.8	09/33/00
GB000165	8-dec-00	05-Mobile Communication	MANFRED ATORF@PHILIPS.COM	PHILIPS	OUTPUT DRIVER CIRCUIT WITH CURRENT DETECTION OF SINGLE DOUBLE POLYMER FILTERING FOR IN-BAND GPS CW REJECTOR	1235929.4	12/35/00
GB000170	7-dec-00	05-Mobile Communication	PAUL ANDREWS@PHILIPS.COM	PHILIPS	ESD PROTECTION DEVICES	202047039.4	20/04/00
US000686	6-dec-00	04-Audio/Video	CHRISTOPHER GOODINGS 16403@PF.PHILIPS.COM	PHILIPS	IONIZED METAL PLASMA DEPOSITION PROCESS HAVING METHOD OF POWERING UP BATTERY POWERED APPARATUS	202047185.4	20/04/00
US000687	6-dec-00	04-Audio/Video	JEFFREY KATTI@PHILIPS.COM	PHILIPS	IONIZED METAL PLASMA DEPOSITION PROCESS HAVING METHOD OF POWERING UP BATTERY POWERED APPARATUS	202047185.4	20/04/00
GB000171	6-dec-00	05-Mobile Communication	CHRIS MARSHALL@PHILIPS.COM	PHILIPS	FREQUENCY COMPENSATION OF A LEVEL VOLTAGE SCALING OF DEMODULATED DATA IN AN INTERLEAVER	202047185.4	20/04/00
FR000131	5-dec-00	05-Mobile Communication	CORO HEINRICH KOHSEK@PHILIPS.COM	PHILIPS	IMPROVEMENT OF THE QUALITY FACTOR OF PASSIVE DEVICES	202047185.4	20/04/00
US000873	4-dec-00	05-Mobile Communication	BENOIT BUTAYE 19268@PF.PHILIPS.COM	PHILIPS	SCALING OF DEMODULATED DATA IN AN INTERLEAVER	1101020.1	11/01/00
US000876	1-dec-00	02-Packaging & Testing	CHARLES RAZZELL@PHILIPS.COM	PHILIPS	LINE IMPEDANCE CALIBRATION USING ACTUAL IMPEDANCE MODULE COMPRISING CONNECTION MEANS	202047276.4	20/04/00
FR000026	30-nov-00	06-Connectivity	JOSE SOLLA@PHILIPS.COM	PHILIPS	MODULE COMPRISING CONNECTION MEANS	09729748	09/27/00
DE000214	30-nov-00	04-Audio/Video	DOMINIK ZEITNER 29550@PF.PHILIPS.COM	PHILIPS	ADAPTIVE SELECTION OF SIMULTANEOUSLY ROWS FOR MRA CONTROLLED AMPLIFIER WITH DEFINED MINIMUM GAIN	10059769.6	10/05/00
DE000217	30-nov-00	04-Audio/Video	ANDREAS WICHERING@PHILIPS.COM	PHILIPS	CIRCUIT FOR ERROR RECOGNITION OF A TWO WIRE-BUS CONTROLLED AMPLIFIER WITH DEFINED MINIMUM GAIN	10155921.7	10/15/00
DE010206	30-nov-00	09-Discrete and Multimeter	ANDREAS WICHERING@PHILIPS.COM	PHILIPS	CIRCUIT FOR ERROR RECOGNITION OF A TWO WIRE-BUS CONTROLLED AMPLIFIER WITH DEFINED MINIMUM GAIN	18359	18/35/00
FR000127	28-nov-00	04-Audio/Video	DAVID CANARD 17603@PF.PHILIPS.COM	PHILIPS	NON-ELECTRICAL SOUND TRANSMISSION FOR MOBILE PHONE POLY FUSE ROM WITH MOS DEVICE BASED CELLS	10058834.4	10/05/00
DE000208	27-nov-00	05-Mobile Communication	MARILIS KOHSEK 21617@PF.PHILIPS.COM	PHILIPS	HIGHLY-INTEGRATED MULTIMODE RECEIVER	09723413	09/27/00
GB000178	27-nov-00	07-Identification	ELIE HOUURY@PHILIPS.COM	PHILIPS	SCENE-CUT HANDLING IN MPEG-4	28652.6	02/28/00
GB000181	24-nov-00	05-Mobile Communication	BRIAN MIRNIN@PHILIPS.COM	PHILIPS	TEMPORAL SCALABLE DISPLAY	204165.3	20/04/00
FR000177	23-nov-00	05-Mobile Communication	STEVEN DE CUYPER@PHILIPS.COM	PHILIPS	MPEG-4 TEMPORAL SCALABLE DISPLAY	1402341	14/02/00
FR000158	23-nov-00	05-Mobile Communication	YVES RAMANANJAN@PHILIPS.COM	PHILIPS	DC OFFSET CORRECTION CIRCUIT WITH DC BLOCKING CIRCUIT	1402341	14/02/00
NL000640	23-nov-00	05-Mobile Communication	YVES RAMANANJAN@PHILIPS.COM	PHILIPS	DC OFFSET CORRECTION CIRCUIT WITH DC BLOCKING CIRCUIT	204164.8	20/04/00
FR000160	22-nov-00	04-Audio/Video	ANDRE VAN BEZOUJEN@PHILIPS.COM	PHILIPS	POWER SUPPLY FOR VERTICAL DEFLECTION ON-TIME ADJUSTMENT DEVICE	204135.9	20/04/00
FR000161	22-nov-00	07-Identification	PIERRE DULKERS@PHILIPS.COM	PHILIPS	ON-TIME ADJUSTMENT DEVICE	15814	15/81/00
US000877	20-nov-00	05-Mobile Communication	STEPHANE BOUYER@PHILIPS.COM	PHILIPS	MEASURES FOR PREVENTING OVERHEATING OF CONTACTLESS IEP	880344.5	08/30/00
US000878	17-nov-00	05-Mobile Communication	MICHAEL CERNUCA@PHILIPS.COM	PHILIPS	MEASURES FOR PREVENTING OVERHEATING OF CONTACTLESS IEP	09716306	09/17/00
FR000156	17-nov-00	09-Discrete and Multimeter	DEBARAJ BANERJEE 21882@PF.PHILIPS.COM	PHILIPS	PLOT-SIGNAL SEARCHING WITH DECIMATION	28691.3	02/28/00
NL000616	16-nov-00	02-Packaging & Testing	NGUYEN BOUDEVINS@PHILIPS.COM	PHILIPS	TRENCH MOS STRUCTURE WITH LOW CGD AND GATE PROTECTIVE PIPE LINE AD-CONVERTER	204033.5	20/04/00
US000688	15-nov-00	04-Audio/Video	DAVE EVOY@PHILIPS.COM	PHILIPS	ON THE FLY DATA TRANSFER BETWEEN RGB AND YCBCR MULTISTAGE FUSE FOR SMART CARD CONTROLLERS	1005650.5	10/05/00
DE000197	15-nov-00	07-Identification	DETLEF MUELLER 2@PHILIPS.COM	PHILIPS	BLOCKING OF SCAN-FRAME AFTER PRODUCTION TEST	1005650.5	10/05/00
DE000198	15-nov-00	07-Identification	DETLEF MUELLER 2@PHILIPS.COM	PHILIPS	BLOCKING OF SCAN-FRAME AFTER PRODUCTION TEST	1005650.5	10/05/00
DE000199	15-nov-00	07-Identification	MARKUS FELSER@PHILIPS.COM	PHILIPS	SMART CARD CONTROLLER WITH DIFFERENT COMMUNICATION C DE	1005652.1	10/05/00
DE000200	15-nov-00	07-Identification	ERNST BRETSCHNEIDER@PHILIPS.COM	PHILIPS	LINEAR TUNABLE CMOS RC-OSCILLATOR	1005654.6	10/05/00

US5002667	16-fev-00	10-Other	RAJEEV SETHIA.21430@FF.PHILIPS.COM	09/13/073	20020311294A2	US5648441
US000299	18-may-00	B4-Audio/Video	JENS RENNERT.17303@FF.UNKNOWNLK	09/17/0997	20020311295A2	US6648688
GB000156	10-nov-00	05-Mobile Communication	BRIAN MINNIS@PHILIPS.COM	27/03/02	20020311296A2	
NL000595	8-nov-00	03-RF Devices	J.W.WEBKAMP@PHILIPS.COM	20/3/05/01	20020311297A1	US5662163
US000317	04-nov-00	04-Audio/Video	JERGEN HEUVELMAN@PHILIPS.COM	09/17/10738	20020311298A2	
GB000151	4-nov-00	05-Mobile Communication	SAUL DOOLEY@PHILIPS.COM	26/09/06	20020311299A1	US7024846
US000307	3-nov-00	09-Mobile Communication	BOB LI.1802B@FF.PHILIPS.COM	09/17/05665	20020311300A1	US5663910
NL000586	2-nov-00	09-Discretes and Multitasking	LOES VAN WERSHOVEN@PHILIPS.COM	20/3/05/01	20020311301A1	US5817624
BE000020	1-nov-00	04-Audio/Video	ERIK CLIPPEL.21691@FF.UNKNOWNL	20/3/05/01	20020311302A2	US6661141
NL000552	1-nov-00	06-Automotive	HENK BOEVEN@PHILIPS.COM	20/3/05/01	20020311303A1	US6818711
CH000022	31-08-00	04-Audio/Video	ADRIAN MESSMER@PHILIPS.COM	20/3/05/01	20020311304A2	US6661141
NL000596	31-08-00	05-Mobile Communication	ARNO EMMERIK@PHILIPS.COM	20/3/05/01	20020311305A2	US6661141
GB010026	26-08-00	05-Mobile Communication	JOSE SAYERS@PHILIPS.COM	12/08/02	20020311306A2	US6763071
US000296	25-08-00	05-Mobile Communication	TONY DORDOBA.20435@FF.UNKNOWNLK	20/3/05/01	20020311307A2	US6763071
FR000111	24-08-00	05-Mobile Communication	SYLVAIN CHARLEY.22371@FF.PHILIPS.COM	20/3/05/01	20020311308A1	US6763071
NL000542	20-08-00	05-Mobile Communication	PAUL MARSHALL@PHILIPS.COM	10/08/05	20020311309A1	US6763071
NL000559	18-08-00	05-Mobile Communication	HAN MISSION@PHILIPS.COM	08/08/05	20020311310A2	US6611307
US000636	17-08-00	04-Audio/Video	RUSS.OTT.21381@FF.PHILIPS.COM	25/7/09	20020311311A1	US6763071
DE000182	17-08-00	05-Mobile Communication	CORD-HEINRICH KOHSEK@PHILIPS.COM	20/3/05/01	20020311312A1	US6611307
AT000059	17-08-00	05-Mobile Communication	ERICH KLEIN@PHILIPS.COM	8/03/17	20020311313A2	US6611307
DE000181	17-08-00	05-Mobile Communication	ARTHUR TRITTHART@PHILIPS.COM	10/03/12/43	11/08/09	US6611307
NL000513	16-08-00	01-CMOS and embedded pt	HENRICUS LIEVERLOO.1904@FF.UNKNOWNLK	20/3/05/01	20020311314A2	US6611307
DE000260	16-08-00	05-Connectivity	WOLFRAM DRESCHER@PHILIPS.COM	20/3/05/01	20020311315A2	US6611307
DE000251	13-08-00	05-Connectivity	VOLKER AUE@PHILIPS.COM	20/3/05/01	20020311316A2	US6611307
NL000568	13-08-00	05-Mobile Communication	TON MOBERS@PHILIPS.COM	10/03/12/44	20020311317A2	US6611307
DE000248	13-08-00	05-Connectivity	VOLKER AUE@PHILIPS.COM	20/3/05/01	20020311318A2	US6611307
AT000056	11-08-00	09-Automotive	GUENTER AFLENZER@PHILIPS.COM	10/03/09/80	20020311319A1	US6611307
FR000108	10-08-00	04-Audio/Video	NICOLAS CONSTANTINIDIS.1905B@FF.PHILIPS.COM	8/08/08	20020311320A1	US6611307
US000496	10-08-00	04-Audio/Video	EVERT-JAN POL@PHILIPS.COM	1/28/08	20020311321A2	US6611307
US000549	8-08-00	09-Discretes and Multitasking	PETER MAGNEE@PHILIPS.COM	09/08/05/441	20020311322A2	US6611307
CH000022	6-08-00	05-Mobile Communication	BERNHARD SCHAFFER.1993X@FF.UNKNOWNLK	20/3/04/01/6	20020311323A2	US6611307
NL000555	5-08-00	01-CMOS and embedded pt	LEON WINTERS@PHILIPS.COM	20/3/04/01/1	20020311324A2	US6611307
US000833	2-08-00	02-Packaging & Testing	RAJEEV SETHIA.21420@FF.PHILIPS.COM	05/07/1939	20020311325A2	
US000632	2-08-00	02-Packaging & Testing	KEN JARAMILLO.21273@FF.PHILIPS.COM	09/07/7937	20020311326A2	
US000634	2-08-00	02-Packaging & Testing	KEN JARAMILLO.21273@FF.PHILIPS.COM	09/07/7937	20020311327A2	
US000831	2-08-00	05-Mobile Communication	LUIS ALDAZ.21103@FF.PHILIPS.COM	09/07/8412	20020311328A2	
DE000187	28-sep-00	05-Mobile Communication	AXEL HERTWIG@PHILIPS.COM	09/07/8490	20020311329A2	
FR000139	28-sep-00	05-Mobile Communication	FABRICE JOVENIN.17187@FF.PHILIPS.COM	09/07/8471	20020311330A2	
FR000143	28-sep-00	05-Mobile Communication	FABRICE JOVENIN.17187@FF.PHILIPS.COM	10/14/87/32	11/08/11	US6763071
FR000260	28-sep-00	05-Mobile Communication	FABRICE JOVENIN.17187@FF.PHILIPS.COM	17/94	20020311331A1	
US000285	28-sep-00	09-Mobile Communication	TIRADAD SOWLATI.20219@FF.PHILIPS.COM	17/94	20020311332A1	
NL000523	27-sep-00	09-Discretes and Multitasking	M.A.P.PERTUS@EWI.TUDELFT.NL	08/07/2806	20020311333A1	
NL000524	27-sep-00	09-Automotive	JAN WELTRA.18847@FF.UNKNOWNL	20/3/03/7	20020311334A2	
US000630	26-sep-00	04-Audio/Video	JERGEN VAN DEN BOOM@PHILIPS.COM	08/07/3486	11/08/11	US6611307
FR000697	25-sep-00	05-Mobile Communication	LOHNIK GOFF.21237@FF.PHILIPS.COM	12/187	20020311335A2	
DE000153	23-sep-00	05-Mobile Communication	DOMINIQUE BRUNEL@PHILIPS.COM	100/472/14,1	20020311336A1	
FR000152	23-sep-00	02-Packaging & Testing	ANDREAS WICHERN@PHILIPS.COM	28/224/7	20020311337A2	
FR000174	23-sep-00	05-Mobile Communication	ANDREAS WICHERN@PHILIPS.COM	28/168/3	20020311338A1	
FR000248	22-sep-00	05-Connectivity	WOLFRAM DRESCHER@PHILIPS.COM	18/09/02/9	20020311339A1	
FR000155	20-sep-00	05-Mobile Communication	SAUL DOOLEY@PHILIPS.COM	20/3/188,8	20020311340A2	
FR000101	19-sep-00	07-Identification	RONALD INGL.22409@FF.PHILIPS.COM	11/8/11	20020311341A1	
US000148	18-sep-00	04-Audio/Video	GERHARD LIEBSCHER.16521@FF.PHILIPS.COM	11/8/11	20020311342A2	
NL000521	15-sep-00	04-Audio/Video	JEFFREY KANG@PHILIPS.COM	20/3/188,8	20020311343A1	
FR000083	12-sep-00	04-Audio/Video	NICOLAS CONSTANTINIDIS.19059@FF.PHILIPS.COM	11/8/11	20020311344A2	
US000628	8-sep-00	06-Connectivity	REJ THOMAS.21463@FF.PHILIPS.COM	11/8/11	20020311345A1	
US000628	8-sep-00	04-Audio/Video	DAVE EYOT@PHILIPS.COM	20/05/1,8	20020311346A1	
CH000021	8-sep-00	05-Mobile Communication	STEPHAN M KOCH@PHILIPS.COM	20/04/0,1	20020311347A1	
NL000091	5-sep-00	05-Mobile Communication	PATRICE GAMAIND@PHILIPS.COM	20/03/03,6	20020311348A1	
NL000495	4-sep-00	05-Mobile Communication	JOHN DELISEN@PHILIPS.COM	00-2000/96	20020311349A2	
DE000443	1-sep-00	01-CMOS and embedded pt	WIM BESLING@PHILIPS.COM			
NL000491	1-sep-00	04-Audio/Video	HANS VOORMAN@UNKNOWNL			
JPO00031	30-aug-00	03-RF Devices	MASAAKI ABE.28479@FF.PHILIPS.COM			

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Pub. No.	Appl. No.	Inventor(s)	Title	Pub. No.	Appl. No.	Inventor(s)	Title
US20080226	23-aug-00	04-Audio/Video	04-Audio/Video	US20080226	23-aug-00	04-Audio/Video	04-Audio/Video
US20080226	23-aug-00	05-Mobile Communication	05-Mobile Communication	US20080226	23-aug-00	05-Mobile Communication	05-Mobile Communication
US20080226	23-aug-00	05-Mobile Communication	05-Mobile Communication	US20080226	23-aug-00	05-Mobile Communication	05-Mobile Communication
US20080226	23-aug-00	04-Audio/Video	04-Audio/Video	US20080226	23-aug-00	04-Audio/Video	04-Audio/Video
US20080226	23-aug-00	04-Discrete and Multichip	04-Discrete and Multichip	US20080226	23-aug-00	04-Discrete and Multichip	04-Discrete and Multichip
FR20080816	21-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr	FR20080816	21-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr
CH000017	21-aug-00	02-Packaging & Testing	02-Packaging & Testing	CH000017	21-aug-00	02-Packaging & Testing	02-Packaging & Testing
NL2008054	18-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr	NL2008054	18-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr
NL20080453	18-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr	NL20080453	18-aug-00	01-CMOS and embedded pr	01-CMOS and embedded pr
DE000122	17-aug-00	05-Mobile Communication	05-Mobile Communication	DE000122	17-aug-00	05-Mobile Communication	05-Mobile Communication
NL20080359	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	NL20080359	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
US2008025	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	US2008025	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
US20080505	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	US20080505	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
US20080523	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	US20080523	17-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
US20080445	16-aug-00	05-Mobile Communication	05-Mobile Communication	US20080445	16-aug-00	05-Mobile Communication	05-Mobile Communication
GB2008104	12-aug-00	04-Audio/Video	04-Audio/Video	GB2008104	12-aug-00	04-Audio/Video	04-Audio/Video
CH000011	11-aug-00	05-Mobile Communication	05-Mobile Communication	CH000011	11-aug-00	05-Mobile Communication	05-Mobile Communication
NL20080428	11-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	NL20080428	11-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
NL20080428	11-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	NL20080428	11-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
DE000117	10-aug-00	02-Packaging & Testing	02-Packaging & Testing	DE000117	10-aug-00	02-Packaging & Testing	02-Packaging & Testing
DE000118	10-aug-00	02-Packaging & Testing	02-Packaging & Testing	DE000118	10-aug-00	02-Packaging & Testing	02-Packaging & Testing
DE000120	10-aug-00	09-Automotive	09-Automotive	DE000120	10-aug-00	09-Automotive	09-Automotive
NL20080455	10-aug-00	10-Other	10-Other	NL20080455	10-aug-00	10-Other	10-Other
FR000081	8-aug-00	02-Packaging & Testing	02-Packaging & Testing	FR000081	8-aug-00	02-Packaging & Testing	02-Packaging & Testing
GB2010056	8-aug-00	03-IP Devices	03-IP Devices	GB2010056	8-aug-00	03-IP Devices	03-IP Devices
NL20080438	7-aug-00	08-Connectivity	08-Connectivity	NL20080438	7-aug-00	08-Connectivity	08-Connectivity
US20080159	7-aug-00	08-Discrete and Multichip	08-Discrete and Multichip	US20080159	7-aug-00	08-Discrete and Multichip	08-Discrete and Multichip
DE000110	5-aug-00	02-Packaging & Testing	02-Packaging & Testing	DE000110	5-aug-00	02-Packaging & Testing	02-Packaging & Testing
DE000111	5-aug-00	02-Packaging & Testing	02-Packaging & Testing	DE000111	5-aug-00	02-Packaging & Testing	02-Packaging & Testing
DE000112	5-aug-00	09-Discrete and Multichip	09-Discrete and Multichip	DE000112	5-aug-00	09-Discrete and Multichip	09-Discrete and Multichip
GB2008101	2-aug-00	04-Audio/Video	04-Audio/Video	GB2008101	2-aug-00	04-Audio/Video	04-Audio/Video
AT000044	31-jul-00	02-Packaging & Testing	02-Packaging & Testing	AT000044	31-jul-00	02-Packaging & Testing	02-Packaging & Testing
AT000045	31-jul-00	07-Identification	07-Identification	AT000045	31-jul-00	07-Identification	07-Identification
GB2008102	31-jul-00	08-Connectivity	08-Connectivity	GB2008102	31-jul-00	08-Connectivity	08-Connectivity
GB2008096	28-jul-00	08-Discrete and Multichip	08-Discrete and Multichip	GB2008096	28-jul-00	08-Discrete and Multichip	08-Discrete and Multichip
US2008022	25-jul-00	08-Automotive	08-Automotive	US2008022	25-jul-00	08-Automotive	08-Automotive
GB2008092	24-jul-00	02-Packaging & Testing	02-Packaging & Testing	GB2008092	24-jul-00	02-Packaging & Testing	02-Packaging & Testing
DE000100	21-jul-00	03-IP Devices	03-IP Devices	DE000100	21-jul-00	03-IP Devices	03-IP Devices
US20080172	21-jul-00	05-Mobile Communication	05-Mobile Communication	US20080172	21-jul-00	05-Mobile Communication	05-Mobile Communication
NL20080426	20-jul-00	05-Mobile Communication	05-Mobile Communication	NL20080426	20-jul-00	05-Mobile Communication	05-Mobile Communication
US2008027	20-jul-00	05-Mobile Communication	05-Mobile Communication	US2008027	20-jul-00	05-Mobile Communication	05-Mobile Communication
NL20080415	19-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr	NL20080415	19-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr
CH000031	18-jul-00	05-Mobile Communication	05-Mobile Communication	CH000031	18-jul-00	05-Mobile Communication	05-Mobile Communication
US2008010	17-jul-00	08-Discrete and Multichip	08-Discrete and Multichip	US2008010	17-jul-00	08-Discrete and Multichip	08-Discrete and Multichip
NL20080398	13-jul-00	07-Identification	07-Identification	NL20080398	13-jul-00	07-Identification	07-Identification
US20080407	12-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr	US20080407	12-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr
NL20080450	10-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr	NL20080450	10-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr
NL20080389	10-jul-00	06-Connectivity	06-Connectivity	NL20080389	10-jul-00	06-Connectivity	06-Connectivity
FR000010	10-jul-00	08-Discrete and Multichip	08-Discrete and Multichip	FR000010	10-jul-00	08-Discrete and Multichip	08-Discrete and Multichip
FR000011	7-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr	FR000011	7-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr
FR000012	6-jul-00	04-Audio/Video	04-Audio/Video	FR000012	6-jul-00	04-Audio/Video	04-Audio/Video
FR000013	5-jul-00	05-Mobile Communication	05-Mobile Communication	FR000013	5-jul-00	05-Mobile Communication	05-Mobile Communication
FR000014	5-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr	FR000014	5-jul-00	01-CMOS and embedded pr	01-CMOS and embedded pr
FR000017	30-jun-00	04-Audio/Video	04-Audio/Video	FR000017	30-jun-00	04-Audio/Video	04-Audio/Video
FR000018	29-jun-00	01-CMOS and embedded pr	01-CMOS and embedded pr	FR000018	29-jun-00	01-CMOS and embedded pr	01-CMOS and embedded pr
CH000011	27-jun-00	04-Audio/Video	04-Audio/Video	CH000011	27-jun-00	04-Audio/Video	04-Audio/Video
US20080147	27-jun-00	06-Mobile Communication	06-Mobile Communication	US20080147	27-jun-00	06-Mobile Communication	06-Mobile Communication
CH000011	27-jun-00	06-Connectivity	06-Connectivity	CH000011	27-jun-00	06-Connectivity	06-Connectivity

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US 09/669,693 EFFICIENT HARDWARE IMPLEMENTATION OF A TRACKING OF A MULTI-PATH RESOLVED SIGNAL IN AN EARLY-LATE DETECTION POWER MANAGEMENT IN A JAVA ACCELERATOR NON-POWER-OF-TWO GREY-CODE COUNTER SYSTEM HAVING BI US 09/664,868 LATERAL ISOLATION FOR HIGH DENSITY CMOS USING OXIDE FOR FR 10/788 20080117899.A2 2008012988.A1 118900.A2 US 09/645,463 LATERAL ISOLATION FOR HIGH DENSITY CMOS USING OXIDE FOR FR 10/788 20080117899.A2 2008012988.A1 118900.A2 US 09/664,868 LATERAL ISOLATION FOR HIGH DENSITY CMOS USING OXIDE FOR FR 10/788 20080117899.A2 2008012988.A1 118900.A2 US 09/664,177 TRENCH MOS WITH ULTRA NARROW TRENCH WIDTH VOLTAGE STABILIZED LOW LEVEL DRIVER MULTIPLE PORT 72C HUB MULTIPLYER WITH ULTRA NARROW TRENCH WIDTH BI DIRECTIONAL REPEATER FOR ASYNCHRONOUS INPUTS MODIFIED FILTERING FOR ASYNCHRONOUS INPUTS SELECTIVE DELIVERY OF DATA LEVERAGING WITH CELL-PHONES CONCEALING ERRORS AD SYNCHRONISATION AD SUPPRESSION OF CLOCK-SKEW ON ICS GATE-DELAY TEST LOGIC ACTIVITY DETECTOR FOR COMMUNICATION NETWORKS TWO TERMINAL ELECTRONIC FLASH INDICATOR (AUTOMOTIVE) FLAG COMPLAINT IN CHIP IMULATION CAPACITIVELY BACK-COUPLED HANDSET LAURENCE BOULLON 1986@PHILIPS.COM FRIEDRICH HAPKE@PHILIPS.COM HANS-JUERGEN KUEHN@PHILIPS.COM MICHAEL MUTH@PHILIPS.COM BILL REDMAN-WHITE@PHILIPS.COM FRANZ AMTMANN@PHILIPS.COM WERNER ZETTLER@PHILIPS.COM MERLYN YOUNG@PHILIPS.COM JOHN THOMPSON 05628@PHILIPS.COM JOHN DI CONE 21104@PHILIPS.COM PATRICK HELITS@PHILIPS.COM JOHN CUTLER@PHILIPS.COM RAINER KREWITT@PHILIPS.COM JAN HOOGERBRUGGE@PHILIPS.COM SIFEN LUD.1762@PHILIPS.COM EWALD BERGLER@PHILIPS.COM ERWIN SEIBEN@PHILIPS.COM DEKAG.BANERJEE.2182@PHILIPS.COM MARTIN KNOTTER@PHILIPS.COM THOMAS WOLFF.1882@PHILIPS.COM ANTON BAKKER@PHILIPS.COM MICHAEL CERNUSCA@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM SLEBAS.ROTHA.21133@PHILIPS.COM GEORG DOGG.20895@PHILIPS.COM GERIT DEN BESTEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM BERNHARD BOLLE@PHILIPS.COM ADRIAN SPENCER@PHILIPS.COM DOECO.TERPSTRA@PHILIPS.COM GILLES CHEVALERIE.06524@PHILIPS.COM DAVID ZIGER@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM

US 09/649,672 TRACKING OF A MULTI-PATH RESOLVED SIGNAL IN AN EARLY-LATE DETECTION POWER MANAGEMENT IN A JAVA ACCELERATOR NON-POWER-OF-TWO GREY-CODE COUNTER SYSTEM HAVING BI US 09/645,468 LATERAL ISOLATION FOR HIGH DENSITY CMOS USING OXIDE FOR FR 10/788 20080117899.A2 2008012988.A1 118900.A2 DE 10035008.3 METHOD AND STRUCTURE FOR ADHERING MSQ MATERIAL TO METHOD OF ENCAP-SULATING USING AN ANCHOR VIRTUAL CACHE MANAGEMENT FOR SERIAL FLASH MEMORY TRENCH MOS WITH ULTRA NARROW TRENCH WIDTH VOLTAGE STABILIZED LOW LEVEL DRIVER MULTIPLE PORT 72C HUB MULTIPLYER WITH ULTRA NARROW TRENCH WIDTH BI DIRECTIONAL REPEATER FOR ASYNCHRONOUS INPUTS MODIFIED FILTERING FOR ASYNCHRONOUS INPUTS SELECTIVE DELIVERY OF DATA LEVERAGING WITH CELL-PHONES CONCEALING ERRORS AD SYNCHRONISATION AD SUPPRESSION OF CLOCK-SKEW ON ICS GATE-DELAY TEST LOGIC ACTIVITY DETECTOR FOR COMMUNICATION NETWORKS TWO TERMINAL ELECTRONIC FLASH INDICATOR (AUTOMOTIVE) FLAG COMPLAINT IN CHIP IMULATION CAPACITIVELY BACK-COUPLED HANDSET LAURENCE BOULLON 1986@PHILIPS.COM FRIEDRICH HAPKE@PHILIPS.COM HANS-JUERGEN KUEHN@PHILIPS.COM MICHAEL MUTH@PHILIPS.COM BILL REDMAN-WHITE@PHILIPS.COM FRANZ AMTMANN@PHILIPS.COM WERNER ZETTLER@PHILIPS.COM MERLYN YOUNG@PHILIPS.COM JOHN THOMPSON 05628@PHILIPS.COM JOHN DI CONE 21104@PHILIPS.COM PATRICK HELITS@PHILIPS.COM JOHN CUTLER@PHILIPS.COM RAINER KREWITT@PHILIPS.COM JAN HOOGERBRUGGE@PHILIPS.COM SIFEN LUD.1762@PHILIPS.COM EWALD BERGLER@PHILIPS.COM ERWIN SEIBEN@PHILIPS.COM DEKAG.BANERJEE.2182@PHILIPS.COM MARTIN KNOTTER@PHILIPS.COM THOMAS WOLFF.1882@PHILIPS.COM ANTON BAKKER@PHILIPS.COM MICHAEL CERNUSCA@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM SLEBAS.ROTHA.21133@PHILIPS.COM GEORG DOGG.20895@PHILIPS.COM GERIT DEN BESTEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM BERNHARD BOLLE@PHILIPS.COM ADRIAN SPENCER@PHILIPS.COM DOECO.TERPSTRA@PHILIPS.COM GILLES CHEVALERIE.06524@PHILIPS.COM DAVID ZIGER@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM

US 09/649,672 TRACKING OF A MULTI-PATH RESOLVED SIGNAL IN AN EARLY-LATE DETECTION POWER MANAGEMENT IN A JAVA ACCELERATOR NON-POWER-OF-TWO GREY-CODE COUNTER SYSTEM HAVING BI US 09/645,468 LATERAL ISOLATION FOR HIGH DENSITY CMOS USING OXIDE FOR FR 10/788 20080117899.A2 2008012988.A1 118900.A2 DE 10035008.3 METHOD AND STRUCTURE FOR ADHERING MSQ MATERIAL TO METHOD OF ENCAP-SULATING USING AN ANCHOR VIRTUAL CACHE MANAGEMENT FOR SERIAL FLASH MEMORY TRENCH MOS WITH ULTRA NARROW TRENCH WIDTH VOLTAGE STABILIZED LOW LEVEL DRIVER MULTIPLE PORT 72C HUB MULTIPLYER WITH ULTRA NARROW TRENCH WIDTH BI DIRECTIONAL REPEATER FOR ASYNCHRONOUS INPUTS MODIFIED FILTERING FOR ASYNCHRONOUS INPUTS SELECTIVE DELIVERY OF DATA LEVERAGING WITH CELL-PHONES CONCEALING ERRORS AD SYNCHRONISATION AD SUPPRESSION OF CLOCK-SKEW ON ICS GATE-DELAY TEST LOGIC ACTIVITY DETECTOR FOR COMMUNICATION NETWORKS TWO TERMINAL ELECTRONIC FLASH INDICATOR (AUTOMOTIVE) FLAG COMPLAINT IN CHIP IMULATION CAPACITIVELY BACK-COUPLED HANDSET LAURENCE BOULLON 1986@PHILIPS.COM FRIEDRICH HAPKE@PHILIPS.COM HANS-JUERGEN KUEHN@PHILIPS.COM MICHAEL MUTH@PHILIPS.COM BILL REDMAN-WHITE@PHILIPS.COM FRANZ AMTMANN@PHILIPS.COM WERNER ZETTLER@PHILIPS.COM MERLYN YOUNG@PHILIPS.COM JOHN THOMPSON 05628@PHILIPS.COM JOHN DI CONE 21104@PHILIPS.COM PATRICK HELITS@PHILIPS.COM JOHN CUTLER@PHILIPS.COM RAINER KREWITT@PHILIPS.COM JAN HOOGERBRUGGE@PHILIPS.COM SIFEN LUD.1762@PHILIPS.COM EWALD BERGLER@PHILIPS.COM ERWIN SEIBEN@PHILIPS.COM DEKAG.BANERJEE.2182@PHILIPS.COM MARTIN KNOTTER@PHILIPS.COM THOMAS WOLFF.1882@PHILIPS.COM ANTON BAKKER@PHILIPS.COM MICHAEL CERNUSCA@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM SLEBAS.ROTHA.21133@PHILIPS.COM GEORG DOGG.20895@PHILIPS.COM GERIT DEN BESTEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM FRANK WIDERSHOVEN@PHILIPS.COM BERNHARD BOLLE@PHILIPS.COM ADRIAN SPENCER@PHILIPS.COM DOECO.TERPSTRA@PHILIPS.COM GILLES CHEVALERIE.06524@PHILIPS.COM DAVID ZIGER@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM HANS-JOACHIM GELKE@PHILIPS.COM



Patent No.	IPC Class.	App. No.	Pub. No.	Pub. Date	IPC Class.	App. No.	Pub. No.	Pub. Date	IPC Class.	App. No.	Pub. No.	Pub. Date	IPC Class.	App. No.	Pub. No.	Pub. Date	IPC Class.	App. No.	Pub. No.	Pub. Date	
AT1000039	07-Identification																				
DE0000080	05-Discretes and Multitasking																				
US0001468	05-Mobile Communication																				
NL0003771	01-CMOS and embedded pr																				
US0008612	01-CMOS and embedded pr																				
GB0000092	03-RF Devices																				
NL0003336	04-Audio/Video																				
US0008006	01-CMOS and embedded pr																				
FR0000071	01-CMOS and embedded pr																				
FR0000081	03-RF Devices																				
FR0000084	05-Mobile Communication																				
FR0000119	05-Mobile Communication																				
US0008621	01-CMOS and embedded pr																				
US0008612	05-Mobile Communication																				
US0008618	05-Mobile Communication																				
GB0000071	03-RF Devices																				
GB0000082	05-Mobile Communication																				
NL0003327	01-CMOS and embedded pr																				
NL0003346	04-Audio/Video																				
US0008616	05-Mobile Communication																				
US0008612	05-Mobile Communication																				
US0008612	07-Identification																				
US0008612	01-CMOS and embedded pr																				
US0008622	03-RF Devices																				
GB0000087	05-Mobile Communication																				
GB0000062	01-CMOS and embedded pr																				
GB0000094	05-Mobile Communication																				
GB0000094	01-CMOS and embedded pr																				
US0008616	05-Mobile Communication																				
US0008612	07-Identification																				
AT0000033	02-Packaging & Testing																				
US0008622	01-CMOS and embedded pr																				
US0008622	04-Audio/Video																				
GB0000082	05-Mobile Communication																				
NL0003317	01-CMOS and embedded pr																				
NL0003318	05-Automotive																				
NL0003319	25-mel-00																				
NL0003320	25-mel-00																				
NL0003321	25-mel-00																				
AT0000033	07-Identification																				
NL0003326	05-Discretes and Multitasking																				
DE0000247	05-Discretes and Multitasking																				
DE0000246	05-Discretes and Multitasking																				
US0008626	02-Packaging & Testing																				
GB0000071	16-mel-00																				
US0008604	05-Mobile Communication																				
US0008605	05-Mobile Communication																				
NL0003317	13-mel-00																				
NL0003320	11-mel-00																				
NL0003266	04-Audio/Video																				
DE0000073	07-Identification																				
US0008602	10-Other																				
US0008602	04-Audio/Video																				
US0008602	05-Mobile Communication																				
US0008602	07-Identification																				
US0008602	05-Mobile Communication																				
US0008602	05-Mobile Communication																				
US0008602	02-Packaging & Testing																				
US0008602	05-Discretes and Multitasking																				
US0008602	05-Discretes and Multitasking																				
US0008602	01-CMOS and embedded pr																				
AT0000036	07-Identification																				

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Pub No	Pub Date	IPC Class	Inventors	Assignor	Title	IPC Class	Pub No	Pub Date	IPC Class	Inventors	Assignor	Title
US0008041	13-apr-00	01-CMOS and embedded pr	JEAN DUBOC, 20817@PF.PHILIPS.COM	PHILIPS	GRAPHICAL DEBUGGER FOR Vhdl/VHDL CODE EMBEDDED PROCESS	US 09/552395	2001020620817.A1	09/552395	EP	09/552395	PHILIPS	GRAPHICAL DEBUGGER FOR Vhdl/VHDL CODE EMBEDDED PROCESS
US0008042	17-apr-00	08-Automotive	HENK BOEZEN@PHILIPS.COM	PHILIPS	FAULT TOLERANT AIRBAG BUS WITHOUT TRANSFORMER	US 09/552396	2001020620817.A1	2013988.9	EP	2013988.9	PHILIPS	FAULT TOLERANT AIRBAG BUS WITHOUT TRANSFORMER
US0008043	17-apr-00	10-Mobile Communication	ANABEL SOUTO-DIEZ@PHILIPS.COM	PHILIPS	IMPROVED FREQUENCY CONVERTER	US 09/552397	2001020620817.A1	4936	FR	4936	PHILIPS	IMPROVED FREQUENCY CONVERTER
US0008044	14-apr-00	01-CMOS and embedded pr	LONNIE GOPF, 21231@PF.PHILIPS.COM	PHILIPS	MULTI-PROCESS JAVA SUBSYSTEM	US 09/552398	2001020620817.A1	09/552398	US	09/552398	PHILIPS	MULTI-PROCESS JAVA SUBSYSTEM
US0008045	14-apr-00	04-Audio/Video	GUGLIO TAD@PHILIPS.COM	PHILIPS	EEPROM MEMORY (CELL) WITH REDUCED SILC	US 09/552399	2001020620817.A1	09/552399	US	09/552399	PHILIPS	EEPROM MEMORY (CELL) WITH REDUCED SILC
US0008046	14-apr-00	09-Discretes and Multimar	ZHIMIN JING, 27783@PF.UNKNOW.NL	ORG	DISPLAY DRIVER WITH DOUBLE CALIBRATION MEANS	US 09/552400	2001020620817.A1	09/552400	US	09/552400	PHILIPS	DISPLAY DRIVER WITH DOUBLE CALIBRATION MEANS
US0008047	14-apr-00	09-Discretes and Multimar	ZHIMIN JING, 27783@PF.UNKNOW.NL	ORG	UNIVERSAL POINTER DYNAMICALLY SELECTABLE STACK FRAME FOR	US 09/552401	2001020620817.A1	09/552401	US	09/552401	PHILIPS	UNIVERSAL POINTER DYNAMICALLY SELECTABLE STACK FRAME FOR
US0008048	13-apr-00	01-CMOS and embedded pr	ALAIN PEGA TOUQUET, 20921@PF.PHILIPS.COM	PHILIPS	ASSEMBLY CODE PERFORMANCE EVALUATION APPARATUS AND	US 09/552402	2001020620817.A1	09/552402	US	09/552402	PHILIPS	ASSEMBLY CODE PERFORMANCE EVALUATION APPARATUS AND
US0008049	13-apr-00	05-Mobile Communication	TRIDAD SOWLATI, 20218@PF.PHILIPS.COM	PHILIPS	PCMCIA PROBABLE BOARD STRUCTURE FOR CMOS PROCESSES	US 09/552403	2001020620817.A1	09/552403	US	09/552403	PHILIPS	PCMCIA PROBABLE BOARD STRUCTURE FOR CMOS PROCESSES
US0008050	12-apr-00	01-CMOS and embedded pr	ALAIN PEGA TOUQUET, 20921@PF.PHILIPS.COM	PHILIPS	ADAPTIVE NON OVERLAP CIRCUIT USING RATIO AS CRITERIUM	US 09/552404	2001020620817.A1	09/552404	US	09/552404	PHILIPS	ADAPTIVE NON OVERLAP CIRCUIT USING RATIO AS CRITERIUM
US0008051	12-apr-00	01-CMOS and embedded pr	ZHONGNING LIANG, 19231@PF.PHILIPS.COM	PHILIPS	METHOD OF IMPROVING ADHESION OF CAP OXIDE TO	US 09/552405	2001020620817.A1	09/552405	US	09/552405	PHILIPS	METHOD OF IMPROVING ADHESION OF CAP OXIDE TO
US0008052	12-apr-00	01-CMOS and embedded pr	PETER STELON@PHILIPS.COM	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE	US 09/552406	2001020620817.A1	09/552406	US	09/552406	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE
US0008053	12-apr-00	04-Audio/Video	MARTIN ELMONS, 19800@PF.UNKNOW.NL	ORG	TUNABLE FILTER COMPRISING BAW RESONATORS	US 09/552407	2001020620817.A1	09/552407	US	09/552407	PHILIPS	TUNABLE FILTER COMPRISING BAW RESONATORS
US0008054	11-apr-00	01-CMOS and embedded pr	SAHAR SAHA, 21418@PF.PHILIPS.COM	PHILIPS	MAGNETIC WIRE AIRCOLD	US 09/552408	2001020620817.A1	09/552408	US	09/552408	PHILIPS	MAGNETIC WIRE AIRCOLD
US0008055	11-apr-00	05-Mobile Communication	NICOLAS RESENT@PHILIPS.COM	PHILIPS	RESOLVING MEDIUM ACCESS CONFLICT IN NETWORKS	US 09/552409	2001020620817.A1	09/552409	US	09/552409	PHILIPS	RESOLVING MEDIUM ACCESS CONFLICT IN NETWORKS
US0008056	11-apr-00	01-CMOS and embedded pr	TIRDAD SOWLATI, 20218@PF.PHILIPS.COM	PHILIPS	NEAR CAPACITIVE MODE DETECTION IN RESONANT CONVERTER	US 09/552410	2001020620817.A1	09/552410	US	09/552410	PHILIPS	NEAR CAPACITIVE MODE DETECTION IN RESONANT CONVERTER
US0008057	10-apr-00	09-Discretes and Multimar	HANS HALBERSTADT@PHILIPS.COM	PHILIPS	CURRENT SOURCE WITH CASCODED BIASING	US 09/552411	2001020620817.A1	09/552411	US	09/552411	PHILIPS	CURRENT SOURCE WITH CASCODED BIASING
US0008058	10-apr-00	09-Discretes and Multimar	HANS HALBERSTADT@PHILIPS.COM	PHILIPS	ADAPTIVE NON OVERLAP CIRCUIT USING RATIO AS CRITERIUM	US 09/552412	2001020620817.A1	09/552412	US	09/552412	PHILIPS	ADAPTIVE NON OVERLAP CIRCUIT USING RATIO AS CRITERIUM
US0008059	7-apr-00	01-CMOS and embedded pr	TIRDAD SOWLATI, 20218@PF.PHILIPS.COM	PHILIPS	METHOD OF IMPROVING ADHESION OF CAP OXIDE TO	US 09/552413	2001020620817.A1	09/552413	US	09/552413	PHILIPS	METHOD OF IMPROVING ADHESION OF CAP OXIDE TO
US0008060	7-apr-00	01-CMOS and embedded pr	RAO ANNA PRAGADA, 21109@PF.PHILIPS.COM	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE	US 09/552414	2001020620817.A1	09/552414	US	09/552414	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE
US0008061	7-apr-00	01-CMOS and embedded pr	RAO ANNA PRAGADA, 21109@PF.PHILIPS.COM	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE	US 09/552415	2001020620817.A1	09/552415	US	09/552415	PHILIPS	END POINT DETECTION METHOD FOR REMOTE MICROWAVE
US0008062	7-apr-00	03-RF Devices	MAREIKE KLEEE@PHILIPS.COM	PHILIPS	TUNABLE FILTER COMPRISING BAW RESONATORS	US 09/552416	2001020620817.A1	09/552416	US	09/552416	PHILIPS	TUNABLE FILTER COMPRISING BAW RESONATORS
US0008063	6-apr-00	03-RF Devices	MAREIKE KLEEE@PHILIPS.COM	PHILIPS	TUNABLE FILTER COMPRISING BAW RESONATORS	US 09/552417	2001020620817.A1	09/552417	US	09/552417	PHILIPS	TUNABLE FILTER COMPRISING BAW RESONATORS
US0008064	6-apr-00	04-Audio/Video	DETLEF MARBACK, 20121@PF.PHILIPS.COM	PHILIPS	MAGNETIC WIRE AIRCOLD	US 09/552418	2001020620817.A1	09/552418	US	09/552418	PHILIPS	MAGNETIC WIRE AIRCOLD
US0008065	6-apr-00	08-Automotive	PETER FUHRMANN@PHILIPS.COM	PHILIPS	RESOLVING MEDIUM ACCESS CONFLICT IN NETWORKS	US 09/552419	2001020620817.A1	09/552419	US	09/552419	PHILIPS	RESOLVING MEDIUM ACCESS CONFLICT IN NETWORKS
US0008066	6-apr-00	02-Packaging & Testing	MARCUS KUEGLER, 20037@PF.UNKNOW.NL	ORG	TEST LOOPS BACK BETWEEN CONTROL & DATA, HAS BIST	US 09/552420	2001020620817.A1	09/552420	US	09/552420	PHILIPS	TEST LOOPS BACK BETWEEN CONTROL & DATA, HAS BIST
US0008067	5-apr-00	01-CMOS and embedded pr	VICKRAM VATHUYA@PHILIPS.COM	PHILIPS	MULTI-LAYER CAPACITOR STRUCTURE HAVING AN ARRAY OF...	US 09/552421	2001020620817.A1	09/552421	US	09/552421	PHILIPS	MULTI-LAYER CAPACITOR STRUCTURE HAVING AN ARRAY OF...
US0008068	4-apr-00	01-CMOS and embedded pr	VICKRAM VATHUYA@PHILIPS.COM	PHILIPS	COMBINED TRANSISTOR-CAPACITOR STRUCTURE IN DEEP SUB-M	US 09/552422	2001020620817.A1	09/552422	US	09/552422	PHILIPS	COMBINED TRANSISTOR-CAPACITOR STRUCTURE IN DEEP SUB-M
US0008069	4-apr-00	01-Identification	VICKRAM VATHUYA@PHILIPS.COM	PHILIPS	CHIP CARD CONTROLLER PROVIDES SUPPLY VOLTAGE FOR ADDI	US 09/552423	2001020620817.A1	09/552423	US	09/552423	PHILIPS	CHIP CARD CONTROLLER PROVIDES SUPPLY VOLTAGE FOR ADDI
US0008070	4-apr-00	07-Identification	CLAUS JULY@PHILIPS.COM	PHILIPS	OUTPUT STAGE FOR A TRANSPODER	US 09/552424	2001020620817.A1	09/552424	US	09/552424	PHILIPS	OUTPUT STAGE FOR A TRANSPODER
US0008071	4-apr-00	09-Discretes and Multimar	PETER THUERINGER@PHILIPS.COM	PHILIPS	A/D CONVERTER	US 09/552425	2001020620817.A1	09/552425	US	09/552425	PHILIPS	A/D CONVERTER
US0008072	4-apr-00	01-CMOS and embedded pr	PETER THUERINGER@PHILIPS.COM	PHILIPS	A/D CONVERTER	US 09/552426	2001020620817.A1	09/552426	US	09/552426	PHILIPS	A/D CONVERTER
US0008073	3-apr-00	01-CMOS and embedded pr	SAHAR SAHA, 21418@PF.PHILIPS.COM	PHILIPS	METHOD FOR FORMING AREAS WITH IMPROVED ISOLATION OXID	US 09/552427	2001020620817.A1	09/552427	US	09/552427	PHILIPS	METHOD FOR FORMING AREAS WITH IMPROVED ISOLATION OXID
US0008074	31-mnt-00	01-CMOS and embedded pr	WALTER DE COSTER@PHILIPS.COM	PHILIPS	SACRIFICIAL SPACERS FOR REDUCTION OF THE REVERSE MARRI	US 09/552428	200107076071.A1	09/552428	US	09/552428	PHILIPS	SACRIFICIAL SPACERS FOR REDUCTION OF THE REVERSE MARRI
US0008075	31-mnt-00	04-Audio/Video	KAWE KIANUSH, 92258@PF.PHILIPS.COM	PHILIPS	RF WITH SWITCHED CAPACITORS	US 09/552429	200107076071.A1	09/552429	US	09/552429	PHILIPS	RF WITH SWITCHED CAPACITORS
US0008076	30-mnt-00	01-CMOS and embedded pr	BRIAN APPOLO, 21089@PF.PHILIPS.COM	PHILIPS	HIGH VOLTAGE CMOS SIGNAL DRIVER WITH MINIMUM	US 09/552430	200107076071.A1	09/552430	US	09/552430	PHILIPS	HIGH VOLTAGE CMOS SIGNAL DRIVER WITH MINIMUM
US0008077	30-mnt-00	09-Discretes and Multimar	ERIC BERNASCONI@PHILIPS.COM	PHILIPS	AUTOMATED DESIGN OF DIGITAL SIGNAL PROCESSING	US 09/552431	200107076071.A1	09/552431	US	09/552431	PHILIPS	AUTOMATED DESIGN OF DIGITAL SIGNAL PROCESSING
US0008078	30-mnt-00	09-Discretes and Multimar	THOMAS EPKE, 19803@PF.PHILIPS.COM	PHILIPS	SCHOTTKY DIODE	US 09/552432	200107076071.A1	09/552432	US	09/552432	PHILIPS	SCHOTTKY DIODE
US0008079	30-mnt-00	05-Mobile Communication	THOMAS G J RICHTER@PHILIPS.COM	PHILIPS	SMD CASCODE CONFIGURATION	US 09/552433	200107076071.A1	09/552433	US	09/552433	PHILIPS	SMD CASCODE CONFIGURATION
US0008080	28-mnt-00	01-CMOS and embedded pr	SATYENDRA SEETHI, 21428@PF.PHILIPS.COM	PHILIPS	RECEPTION OF WIDE-BAND DATA RECEIVING IN AMPS VOICE MO	US 09/552434	200107076071.A1	09/552434	DE	10014780.1	PHILIPS	RECEPTION OF WIDE-BAND DATA RECEIVING IN AMPS VOICE MO
US0008081	28-mnt-00	05-Mobile Communication	TIRDAD SOWLATI, 20218@PF.PHILIPS.COM	PHILIPS	MAGNETORESISTIVE SENSOR	US 09/552435	200107076071.A1	09/552435	DE	10014779.8	PHILIPS	MAGNETORESISTIVE SENSOR
US0008082	27-mnt-00	03-RF Devices	KEVIN BOYLE@PHILIPS.COM	PHILIPS	SI WITH GRADUATE ACTIVE-OXIDE TRANSITION FOR ELIMINATIO	US 09/552436	200107076071.A1	09/552436	GB	9955.9	PHILIPS	SI WITH GRADUATE ACTIVE-OXIDE TRANSITION FOR ELIMINATIO
US0008083	27-mnt-00	05-Mobile Communication	JEAN BONNOT, 21131@PF.PHILIPS.COM	PHILIPS	LATERAL DEEP-TRENCH RESURF MOS DEVICES	US 09/552437	200107076071.A1	09/552437	GB	9955.9	PHILIPS	LATERAL DEEP-TRENCH RESURF MOS DEVICES
US0008084	24-mnt-00	09-Discretes and Multimar	MICHAEL DIESCHER@PHILIPS.COM	PHILIPS	IQ MODULATION USING RF LOCAL OSCILLATOR	US 09/552438	200107076071.A1	09/552438	FR	9607.5	PHILIPS	IQ MODULATION USING RF LOCAL OSCILLATOR
US0008085	23-mnt-00	05-Mobile Communication	RISHI MOHINDRA, 11567@PF.PHILIPS.COM	PHILIPS	CO AUTO-CALIBRATION OF A TRANSMITTER THROUGH NULLING...	US 09/552439	200107076071.A1	09/552439	US	09/552439	PHILIPS	CO AUTO-CALIBRATION OF A TRANSMITTER THROUGH NULLING...
US0008086	21-mnt-00	07-Identification	ROBERT RECHBERGER@PHILIPS.COM	PHILIPS	CHANGING THE CONTENT OF A MEMORY OF MANY DATA CARRIER	US 09/552440	200107076071.A1	09/552440	EP	890089.6	PHILIPS	CHANGING THE CONTENT OF A MEMORY OF MANY DATA CARRIER
US0008087	21-mnt-00	07-Identification	CLAUS JULY@PHILIPS.COM	PHILIPS	TRANSPODER WITH ADDITIONAL COMMANDS AND ADDRESSING	US 09/552441	200107076071.A1	09/552441	EP	201008.4	PHILIPS	TRANSPODER WITH ADDITIONAL COMMANDS AND ADDRESSING
US0008088	20-mnt-00	02-Packaging & Testing	MARK BAPST, 21119@PF.PHILIPS.COM	PHILIPS	COMPUTER-SYSTEM-ON-A-CHIP WITH TEST-MODE	US 09/552442	200107076071.A1	09/552442	US	09/552442	PHILIPS	COMPUTER-SYSTEM-ON-A-CHIP WITH TEST-MODE
US0008089	20-mnt-00	09-Automotive	JUERGEN NOWOTNICK@PHILIPS.COM	PHILIPS	PASSIVE KEYLESS ENTRY SYSTEM	US 09/552443	200107076071.A1	09/552443	DE	16013542	PHILIPS	PASSIVE KEYLESS ENTRY SYSTEM
US0008090	15-mnt-00	06-Mobile Communication	TEO LETAVIC@PHILIPS.COM	PHILIPS	METHOD AND APPARATUS OF TERMINATING A HIGH VOLTAGE...	US 09/552444	200107076071.A1	09/552444	DE	09/552444	PHILIPS	METHOD AND APPARATUS OF TERMINATING A HIGH VOLTAGE...
US0008091	15-mnt-00	08-Automotive	HENK BOEZEN@PHILIPS.COM	PHILIPS	QUADRATURE SIGMA-DELTA MODULATOR	US 09/552445	200107076071.A1	09/552445	EP	200942.1	PHILIPS	QUADRATURE SIGMA-DELTA MODULATOR
US0008092	15-mnt-00	09-Discretes and Multimar	MATTHIAS LOCHER@PHILIPS.COM	PHILIPS	LOW POWER, NO DEAD TIME PHASE FREQ. DETECTOR WITH CHA	US 09/552446	200107076071.A1	09/552446	EP	200945.4	PHILIPS	LOW POWER, NO DEAD TIME PHASE FREQ. DETECTOR WITH CHA
US0008093	13-mnt-00	01-CMOS and embedded pr	MARCEL BROEKAART@PHILIPS.COM	PHILIPS	SIC VIA ETCH STOP FOR LOW-EPSILON MATERIAL	US 09/552447	200107076071.A1	09/552447	EP	201928.9	PHILIPS	SIC VIA ETCH STOP FOR LOW-EPSILON MATERIAL
US0008094	11-mnt-00	05-Mobile Communication	WINFRID BIRTH@PHILIPS.COM	PHILIPS	QUADRATURE MODULATOR WITH ALLPASS COMPENSATION	US 09/552448	200107076071.A1	09/552448	DE	10912003.2	PHILIPS	QUADRATURE MODULATOR WITH ALLPASS COMPENSATION

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Patent No.	IPC Class.	Inventor	Company	Title	Pub No.	Pub Date	Pub Type	Pub Loc.	Pub Desc.
FR0000229	10-mfr-00	DAVID CANARD	PHILIPS.COM	INTERFACE BETWEEN CONVENTIONAL AND FRACTIONAL-N TUNER	3135		FR		
FR0000324	04-Audio/Video	DAVID CANARD	PHILIPS.COM	IMPROVED FRACTIONAL-N PLL-TUNER	3134		FR		
NL000131	04-Audio/Video	RAY HUETING	PHILIPS.COM	LV-MOSFET WITH 1-OF-N DRAIN CONNECTION	5650,7		GB		
GB0000225	09-Discrete and Multicomm	DOMENICO FORCONI	PHILIPS.COM	FAST DATA PRESENCE INDICATION FOR 2-FSK SYSTEMS	5650,1		GB		
NL0005531	01-CMOS and embedded	NICOLE WILSON	PHILIPS.COM	COMPACT 2/3-TVM DEVICE	2005621,6		EP		
NL0008023	01-CMOS and embedded	PIERRE LEROUX	PHILIPS.COM	NEW WATER TARGET DESIGN FOR CMP PROCESSES	US 09520655		US		
US0008024	01-CMOS and embedded	OLIVER LARRA	PHILIPS.COM	TRENCH-DIFFUSION CORNER ROUNDING IN A QUADRANT MODULATOR WITH COMPENSATION FOR TRANSMISSION	10011067,4		US		
DE0000039	06-Mobility	WINFRIED BIRTHNER	PHILIPS.COM	DATA CLOCK RECOVERY CIRCUIT	2000804,3		EP		
NL0000391	01-CMOS and embedded	KAILASH SINGH	PHILIPS.COM	SHALLOW TRENCH ISOLATION METHOD FOR FORMING	US 095180310		US		
US0000092	06-Connectivity	MICHAEL EPSTEIN	PHILIPS.COM	METHOD AND APPARATUS FOR GENERATING RANDOM NUMBERS	0951264,9		US		
US0000037	06-Connectivity	GEORGE FLEMING	PHILIPS.COM	IEEE 1394 LINK LAYER CHIP WITH "SC" SYNCHRONIZING BETWEEN READERS/SMART CARD	096517084		US		
AT0000116	07-Identification	MARTIN POSCH	PHILIPS.COM	CIRCUIT FOR PROCESSING SENSOR SIGNALS	200717,7		EP		
NL0000080	08-Discrete and Multicomm	RONALD DENKER	PHILIPS.COM	VOICE BUFFER SYSTEM FOR ASYNCHRONOUS CONNECTIONS	DE 10010370,7		DE		
DE0000037	1-mfr-00	MICHAEL MUTH	PHILIPS.COM	MOST WITH BACK GATE IN ACCESS TO SUPPRESS SCA	US 095166819		US		
NL0000088	05-Mobility	ROEL VANDERTULUN	PHILIPS.COM	DOWNLOAD OF CODEC ALGORITHM	200717,7		EP		
DE0000039	29-feb-00	YOURI PONOMAREV	PHILIPS.COM	WLMV SBT-ASSOCIATIVE BRANCH TARGET MEMORY CACHE FOR GPS EMERGENCY CALL FUNCTION	10009444,9		DE		
NL0000075	28-feb-00	THOMAS G.J. RICHTER	PHILIPS.COM	MOBILE CELLULAR TELEPHONE COMPRISING A GPS RECEIVER	GB 2371,1		GB		
GB0000016	24-feb-00	JAN HODGERBRUGER	PHILIPS.COM	DIA CONVERTER WITH AMPLIFIER BRIDGE MULTIPLE SUPPLY VOLT	2008537,7		EP		
GB0000055	24-feb-00	STEVE TOMASEND	PHILIPS.COM	LINEARIZED TRANSDUCER WITH STABILIZED TRANSDUCER	2008536,9		EP		
NL0000064	24-feb-00	PETER G. BLANKEN	PHILIPS.COM	MAKING A HYBRID INTEGRATED CIRCUIT WITH PIEZOELECTRIC FILTER	20010617,8		EP		
NL0000057	22-feb-00	RONALD DENKER	PHILIPS.COM	RET-POCKET IMPLANTS	200738,3		EP		
NL0000058	22-feb-00	RONALD DENKER	PHILIPS.COM	SYSTEM AND METHOD FOR REDUCING WRITE TRAFFIC IN APPARATUS AND METHOD FOR REDUCING REGISTER WRITE TRAFFIC	US 095055885		US		
NL000114	17-feb-00	YOURI PONOMAREV	PHILIPS.COM	DYNAMICALLY-CONTROLLED CLOCK DRIVER	200106197,8,2		EP		
US0000039	16-feb-00	PAULUS STRAVERS	PHILIPS.COM	LC FILTER WITH CONSTANT RESONANCE FREQUENCY	200106182,2,2		EP		
DE0000022	16-feb-00	PETER FUHRMANN	PHILIPS.COM	INVERTED PUNCH THROUGH STRUCTURE	200106182,2,2		EP		
NL0000073	16-feb-00	MICK PULSFORD	PHILIPS.COM	RESISTOR MATCHING IN PROGRAMMABLE CURRENT-TO-VOLTAGE	200106182,2,2		EP		
FR0000015	15-feb-00	FRED HURKX	PHILIPS.COM	MULTI-RESURF DIODE WITH SEMI-INSULATING LAYERS	3184,9		FR		
NL0000041	15-feb-00	FRED HURKX	PHILIPS.COM	MULTI-RESURF MOSFET WITH SEMI-INSULATING LAYERS	3185,8		EP		
CH0000004	14-feb-00	WILLEM GROENEWEG	PHILIPS.COM	MULTI-RESURF DEVICE WITH SEMI-INSULATING LAYERS	3186,4		CH		
NL0000066	12-feb-00	FRED HURKX	PHILIPS.COM	FLEXIBLE BURST PROCESSING MODE UNIT	1601		EP		
NL0000067	12-feb-00	FRED HURKX	PHILIPS.COM	MEASURING ANTENNA SIGNAL STRENGTH USING AGC	200001748,3		EP		
DE0000022	10-feb-00	ALAN YEO	PHILIPS.COM	TRENCH EDGE TERMINATION OF TMS SCHOTTKY RECTIFIER	2000000591,8		EP		
SG0000001	8-feb-00	ALAN YEO	PHILIPS.COM	OPTIMIZED GATE IMPLANTS FOR REDUCING DOPPLANT	2235		SG		
NL0000043	2-feb-00	J.B.HAT	PHILIPS.COM	EXPANSION MODULE WITH EXTERNAL BUS BRIDGE	US 09465415		US		
NL0000020	31-jan-00	CALVIN GABRIEL	PHILIPS.COM	BRIDGING A HOST BUS TO AN EXTERNAL BUS USING A HOST BUS INSTRUCTION DECODERS, PROCESSORS, DIGITAL SIGNAL	US 09465043		US		
US0000018	31-jan-00	LOREN REISS	PHILIPS.COM	STABILIZING WALLS BETWEEN COIL HOLDING PROTRUDANCES	US 690028,4		US		
US0000018	31-jan-00	LOREN REISS	PHILIPS.COM	DUMPFENDE HALTEFOLIEN FÜR SCHWINGSPULEN-ANSCHLÜSSE	EP 890028,2		EP		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	SEMI-CONDUCTOR DEVICE WITH METAL LAYER	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	CMP PAD CONDITIONER ARRANGEMENT AND METHOD	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	FAST HIGH VOLTAGE LEVEL SHIFTER WITH GATE OXIDE PROTECTION	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	BANDGAP VOLTAGE REFERENCE WITH PLURALITY OF CORRECTING	EP 890028,2		EP		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	POWER ON RESET CIRCUIT WITH PROCESS DEPENDENT THRESHOLD	EP 890028,2		EP		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	SWITCHABLE RECEIVING MEANS CONFIGURATION	EP 890028,2		EP		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	CONTROL LOOP FOR OPTIMAL CONTROL OF A BIPOLAR TRANSISTOR	EP 890028,2		EP		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	D-VHS SIGNAL PROCESSING USING A SINGLE X-TAL	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	RANDOM CONTROLLED USE OF PARALLEL DATA PATHS	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	ON-CHIP RANDOM NUMBER GENERATOR	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	SPECTRALLY DETECTABLE LOW-K DIELECTRIC MARKER LAYER	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	BACKEND PROCESS FOR FUSE LINK OPENING	US 690028,2		US		
US0000014	29-jan-00	JEAN-DUBOIS	PHILIPS.COM	METHOD TO MEASURE ALIGNMENT USING LATENT IMAGE GRATIN	US 690028,2		US		

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No.	Inventor	Inventor Address	Inventor City/State	Inventor Country	Inventor Email	Title	Pub. No.	Pub. Date	Pub. Date	Pub. Date	Pub. Date
A 051258	US-CMOS and embedded p...										
A 051244	05-Mobile Communication					PROCESS TO CONTROL ETCH PROFILES IN		09/47/9891			
D 099214	30-dcc-99	JEAN CHIAZZA, 21131@PF.PHILIPS.COM		US		METHOD TO REDUCE POWER CONSUMPTION OF WIRELESS PHOT US	200102031802-52	09/47/9890			
D 099218	05-Connectivity	MATTHIAS WEISS@PHILIPS.COM		US		METHOD AND APPARATUS FOR PRODUCING INSTRUCTION WORD DE		1996-0028-9			
A 023882	28-dcc-99	UWE FORST@PHILIPS.COM		US		VERFAHREN ZUR DIGITALEN MODULATIONDEMODULATION ZUM DE					
A 023953	05-Mobile Communication	DANIEL GREENHOE 20473@PF.PHILIPS.COM		US		ZERO DELAY MASK ARRANGEMENT FOR JUMPING IN PSEUDO-NOISE US	20010502030-22	09/47/4303			
D 099216	29-dcc-99	DANIEL GREENHOE 20473@PF.PHILIPS.COM		US		MULTIPLE MASK ARRANGEMENT FOR JUMPING IN PSEUDO-NOISE DE	20010502030-22	09/47/4302			
D 099217	05-Connectivity	WOLFRAM DRESCHER@PHILIPS.COM		DE		ANORDNUNG ZUR VERARBEITUNG VON DATEN					
A 023903	29-dcc-99	WOLFRAM DRESCHER@PHILIPS.COM		DE		DEVICE AND METHOD FOR CONTROL OF THE DATA STREAM	20010502030-22	1996-3613-3			
D 099215	04-Audio/Video	YVES DUFOUR 06607@PF.PHILIPS.COM		DE		AN INTEGRATED CIRCUIT WITH METAL PROGRAMMABLE LOGIC...					
N 017858	05-Connectivity	RONALD DRESCHER@PHILIPS.COM		US		ANORDNUNG ZUR KONTROLLE DES DATENFLUSSES IN...	20010502030-22	09/47/4304			
N 017822	01-CMOS and embedded p...	RONALD DRESCHER@PHILIPS.COM		US		MAKING S/DI DEVICES WITH DEEP TRENCHES	20010502030-22	08/47/0779			
N 017831	08-Connectivity	ZONGJIANG WU 15685@PF.UNKNOWN.ORG		US		USBS2.0 (USB)CONNECT EMULATION					
A 051243	09-Discretes and Multimar...	SUBHAS BOTHA 21133@PF.PHILIPS.COM		US		DOUBLE INPUT BUFFER FOR TRACK-AND-HOLD AMPLIFIER	20010502030-22	09/20/4889			
A 051246	33-dcc-99	SUBHAS BOTHA 21133@PF.PHILIPS.COM		US		PAD METALLIZATION OVER ACTIVE CIRCUITRY					
B 099437	04-Audio/Video	GREGORY SCOTT 21428@PF.PHILIPS.COM		US		METHOD FOR CONVERTING FEATURES IN AN INTEGRATED CIRCUIT US	20010502030-22	09/47/4364			
N 017856	08-Connectivity	KAVE KANUSH 02956@PF.PHILIPS.COM		US		OTHER CHANNEL TACS RATING	20010502030-22	09/30/4812			
D 099197	05-Mobile Communication	HARALD N. BAUER@PHILIPS.COM		DE		ENERGY SAVING BY PARTLY SWITCHING OFF LCD-DISPLAY	20010502030-22	09/20/4889			
A 051200	01-CMOS and embedded p...	CHARLES DRILL 21180@PF.PHILIPS.COM		US		APPARATUS FOR PERFORMING CHEMICAL-MECHANICAL PLANARIZ	20010502030-22	1996-3282-5			
A 023848	02-Packaging & Testing	HO WAI WONG-LAM@PHILIPS.COM		US		SYSTEM AND METHOD FOR ACCESSING INTERNAL REGISTERS...					
D 099195	02-Packaging & Testing	HO WAI WONG-LAM@PHILIPS.COM		US		SYSTEM AND METHOD FOR COMPUTER CONTROLLED INTERACTIO	20010502030-22	09/47/0296			
A 023914	03-RF Devices	MAREIKE KLEE@PHILIPS.COM		DE		FILTER MODULE					
A 023915	05-Mobile Communication	RISHI MOHINDRA 11567@PF.PHILIPS.COM		US		HIGH DYNAMIC RANGE LOW RIPPLE RSSI FOR ZERO-IF OR LOW-IF	20010502030-22	1996-0028-8			
A 023916	05-Mobile Communication	RISHI MOHINDRA 11567@PF.PHILIPS.COM		US		CO AUTOMATIC GAIN CONTROL IN A ZERO INTERMEDIATE FREQUEN	20010502030-22	09/46/9922			
A 023919	05-Mobile Communication	RISHI MOHINDRA 11567@PF.PHILIPS.COM		US		TRANSMITTER POWER AMPLIFIER RAMPING METHOD.					
A 051359	05-Mobile Communication	RISHI MOHINDRA 11567@PF.PHILIPS.COM		US		METHOD FOR EXTENDING DIGITAL RECEIVER SENSITIVITY...	20010502030-22	09/46/9887			
D 099194	05-Mobile Communication	DANIEL HSIA 21181@PF.PHILIPS.COM		US		METHOD TO IMPROVE SYSTEM PERFORMANCE WITH	20010502030-22	09/17/1700			
A 023904	08-Discretes and Multimar...	HARALD N. BAUER@PHILIPS.COM		US		CELLULAR MOBILE WITH INTEGRATED BROADCAST RECEIVER					
N 017837	01-CMOS and embedded p...	ROY COLCLASER@PHILIPS.COM		US		INTEGRATED CIRCUIT WITH REMOVABLE ESD PROTECTION.	20010502030-22	09/46/9874			
N 017838	01-CMOS and embedded p...	ROY COLCLASER@PHILIPS.COM		US		VIRTUAL-GROUND, SPLIT-GATE FLASH MEMORY CELL	20010502030-22	09/20/4452-9			
D 099184	04-CMOS and embedded p...	ROY COLCLASER@PHILIPS.COM		US		MEMORY CELL WITH REMOVABLE ESD PROTECTION.	20010502030-22	09/46/9874			
A 023908	07-Identification	CHRISTOPHER STORAR 18201@PF.PHILIPS.COM		US		PORTABLE-TO-PORTABLE COMMUNICATION IN A TD SYSTEM	20010502030-22	09/20/4452-6			
A 023909	09-Discretes and Multimar...	ERAN SITHIK 16363@PF.PHILIPS.COM		US		METHOD AND APPARATUS FOR AUTHENTICATING TIME-SENSITIV	20010502030-22	1996-1674-4			
A 023900	21-dcc-99	DEV ALOK 16377@PF.UNKNOWN.ORG		US		SELF-ALIGNED SILICON CARBIDE MOSFET.					
N 017815	09-Discretes and Multimar...	DEV ALOK 16377@PF.UNKNOWN.ORG		US		SILICON CARBIDE N-CANNEL POWER MOSFET.	20010502030-22	09/46/9454			
A 051202	09-Discretes and Multimar...	ANDREW ROLAND ANTHELINS@PHILIPS.COM		US		VOLTAGE REGULATOR PROVIDED WITH A CURRENT LIMITER	20010502030-22	09/46/9450			
N 017809	01-CMOS and embedded p...	SUBHAS BOTHA 21133@PF.PHILIPS.COM		US		THIN CAPACITIVE STRUCTURES AND METHODS FOR MAKING THE	20010502030-22	09/46/7734			
A 023880	17-dcc-99	STAN MUELWSEN@PHILIPS.COM		US		BURIED N-CM MOST WITH OP FOR REMOVING HOLES	20010502030-22	09/20/4405-7			
A 023881	17-dcc-99	ROY COLCLASER@PHILIPS.COM		US		BI-DIRECTIONAL ESD STRUCTURE.					
N 017823	01-CMOS and embedded p...	PIERRE WOEHLER@PHILIPS.COM		US		IMPROVED ESD DIODE STRUCTURE	20010502030-22	09/46/6401			
A 023886	17-dcc-99	PIERRE WOEHLER@PHILIPS.COM		US		BORDERLESS CONTACT BY REPLACEMENT GATE TECHNOLOGY	20010502030-22	09/46/6411			
D 023991	17-dcc-99	GEORGE FLEMING@PHILIPS.COM		US		SIMPLE ALGORITHMIC CRYPTOGRAPHY ENGINE.	20010502030-22	09/20/4374-5			
N 017821	17-dcc-99	FARRELL OSTLER 05914@PF.PHILIPS.COM		US		BUS BRANCH INSTRUCTIONS WITH BECOLUPD CONDITION AND ADDI	20010502030-22	09/46/6405			
N 017815	05-Mobile Communication	ADWIN TIMMER 16102@PF.PHILIPS.COM		US		COMA FINGER LOCK ALGORITHM SPECIFICATION	20010502030-22	60/17/2392			
N 017825	05-Mobile Communication	LUIS ALDAR 21103@PF.PHILIPS.COM		US		COMA FINGER LOCK ALGORITHM SPECIFICATION	20010502030-22	60/17/2392			
N 017815	05-Mobile Communication	SIBONG JEONG 21832@PF.PHILIPS.COM		US		TRENCH-DIFFUSION CORNER ROUNDING IN SHALLOW-TRENCH DE	20010502030-22	09/46/5191			
N 017815	05-Mobile Communication	FARRELL OSTLER 05914@PF.PHILIPS.COM		US		SHARED RESOURCE ARBITRATION METHOD AND APPARATUS.	20010502030-22	09/46/4246			
N 017815	05-Mobile Communication	JOHN PROVENDA 20472@PF.PHILIPS.COM		US		COOL DOWN MOSFETS USING SELECTIVE EPITAXIAL GROWTH	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	MIKULU LUIGI 17863@PF.PHILIPS.COM		US		COOL DOWN MOSFETS USING SELECTIVE EPITAXIAL GROWTH	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	RUNE JENSEN@PHILIPS.COM		US		AN INTEGRATED CIRCUIT WITH A SERRATED CONDUCTOR TRAK US	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	MICHAEL MISHELOFF 21353@PF.PHILIPS.COM		US		WAVEGUIDE STRUCTURES INTEGRATED WITH STANDARD	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	RISHI MOHINDRA 11567@PF.PHILIPS.COM		US		IMAGE SUPPRESSION IN TDD TRANSMITTERS.	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	CHRISTOPHER CORDIER 13992@PF.PHILIPS.COM		US		SMALL SIGNAL SWITCH	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	PATRICK ARKOLL 21112@PF.PHILIPS.COM		US		METHOD OF OPERATING ON A NET-LIST DESCRIBING AN INTEGRAT	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	JOHN PETRUZZELLO@PHILIPS.COM		US		LATERAL INSULATED-GATE BIPOLAR TRANSISTOR (LIGHT) DEVICE US	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	KEN WRIGHT@PHILIPS.COM		US		METHOD AND APPARATUS FOR DIGITAL CORRELATION	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	SUBHAS BOTHA 21133@PF.PHILIPS.COM		US		CC INTELLEGENY GATE-LEVEL FILL METHODS FOR REDUCING GLOBAU	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	MARCEL BOHMER@PHILIPS.COM		US		COATING WITH 98.9% INHIBITION OF LIGHT-INDUCED RADIATION	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	FERNANDO CUESTA 21171@PF.PHILIPS.COM		US		METHOD AND APPARATUS FOR SYNTHESIZING COMMUNICATION SIG	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	PETER HOEFMIST 14018@PF.PHILIPS.COM		US		WAFER SCALE PACKAGED DIODE	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	HO WAI WONG-LAM@PHILIPS.COM		US		PERIODIC SIGNAL ANALYSIS VIA CORRELATION	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	HO WAI WONG-LAM@PHILIPS.COM		US		HIGH AND LOW VOLTAGE MEASUREMENT IN WAVEFORM ANALYS US	20010502030-22	09/46/5126			
N 017815	05-Mobile Communication	THOMAS S.G. RICHTER@PHILIPS.COM		US		RECOGNITION OF CELLULAR ENVIRONMENT AT SWITCH ON	20010502030-22	09/46/5126			

Table with columns: Patent No., Inventor Name, Title, Date Filed, and Classification. The table lists numerous patents, such as 09-Discretes and Multimark, 02-Packaging & Testing, and 04-Audio/Video, with associated inventor names and dates.

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Table with columns for Patent No., Class, Title, Applicant, and Inventor. Contains numerous patent entries such as '01-CMOS and embedded pr LAMON,VINES@PHILIPS.COM', 'FLUID DISPENSING FIXED ABRASIVE POLISHING PAD', 'METHOD OF MAKING SEMICONDUCTOR DEVICES WITH GRADIED IUS', etc.

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Pub No	App No	Class	Inventor	Inventor	Pub No	App No	Class	Inventor	Inventor
J 039017	08-Discretes and Multimedia	21-aug-99	MASAYA, KISHIDA, 16600@PF.PHILIPS.COM AND CONVERTER	US01003662	JP	99-2400683	200100103282	200100103282	US01003662
J 039016	04-Audio/Video	26-aug-99	YOSHIZAKI, SATOH, 02537@PF.PHILIPS.COM METHOD OF WRITING AND READING DATA	US01003663	JP	99-239424	200100103283	200100103283	US01003663
N 0117801	05-Mobile Communication	25-aug-99	HARRY NELLEBOM@PHILIPS.COM BATTERY LOW INDICATOR	US01003664	EP	99-239424	200100103284	200100103284	US01003664
N 017595	09-Discretes and Multimedia	24-aug-99	CHRIS TOPHE NERON, 21364@PF.PHILIPS.COM FREE-RUNNING MODE DEVICE FOR PHASE LOCKED LOOP	US01003665	US	99-202750.5	200100103285	200100103285	US01003665
D 059106	05-Mobile Communication	21-aug-99	DIRK-JAN RIEZBOS@PHILIPS.COM SUSPENSION OF WRITE IN PROGRAM + DATA EEPROM	US01003666	EP	99-202727.6	200100103286	200100103286	US01003666
B 034378	06-Mobile Communication	21-aug-99	AXEL HERTWIG@PHILIPS.COM CACHE PREFETCH STRATEGY	US01003667	DE	1999-9784.3	200100103287	200100103287	US01003667
B 034378	09-Discretes and Multimedia	21-aug-99	AXEL HERTWIG@PHILIPS.COM MULTIPROCESSOR SYSTEM	US01003668	DE	1999-9784.3	200100103288	200100103288	US01003668
A 023736	03-RF Devices	19-aug-99	KEVIN BOYLE@PHILIPS.COM PLANAR THYRISTOR WITH LOXEX ISOLATION	US01003669	GB	99-19784.2	200100103289	200100103289	US01003669
A 051010	01-CMOS and embedded pr	18-aug-99	WINTROP, SAVILLE, 18859@PF.PHILIPS.COM ANTENNA DIVERSITY FOR PEN-SHAPED HANDSET	US01003670	GB	99-19784.2	200100103290	200100103290	US01003670
N 017584	04-Audio/Video	15-aug-99	HUNTER, BRIDGE, 21135@PF.PHILIPS.COM SERVICING OF INTERRUPTS WITH STORED AND RESTORED FLAG	US01003671	US	99-19668.5	200100103291	200100103291	US01003671
A 051037	05-Mobile Communication	10-aug-99	BRIAN DE BART@PHILIPS.COM JERRY LIEBLER, 20331@PF.PHILIPS.COM FORWARD UPDATE OF STATE POINTER IN VITERBI DECODER	US01003672	EP	99-177358	200100103292	200100103292	US01003672
Q 039023	07-Identification	4-aug-99	LILY AGGARWAL, 18321@PF.PHILIPS.COM SLEEP-MODE-READY SWITCHING POWER CONVERTER	US01003673	US	99-22647.8	200100103293	200100103293	US01003673
A 051080	09-Discretes and Multimedia	4-aug-99	STEFAN OTT, 10236@PF.UNKNOWN.CRC CASCADING PLL UNITS FOR ARCHIVING RAPID SYNCHRONIZATION	US01003674	US	99-353280	200100103294	200100103294	US01003674
A 051187	05-Mobile Communication	3-aug-99	FRANZ, ANTMANN@PHILIPS.COM DETERMINATION OF ATTENDANCE TIME OF A TRANSPONDER	US01003675	US	99-371371	200100103295	200100103295	US01003675
F 039058	05-Mobile Communication	30-Jul-99	FERDINAND SLUNIS@PHILIPS.COM COMPARATORS, MEMORY DEVICES, COMPARISON METHODS AND	US01003676	EP	99-090250.8	200100103296	200100103296	US01003676
N 017561	05-Mobile Communication	30-Jul-99	ABDELWAHEB, MARCOULI, 18327@PF.PHILIPS.COM DOWN-CONVERTER WITH SINGLE REFERENCE CONTROL	US01003677	US	99-368400	200100103297	200100103297	US01003677
B 034366	01-CMOS and embedded pr	29-Jul-99	DO, DORIMANS@PHILIPS.COM ARITHMETIC UNIT, DIGITAL SIGNAL PROCESSOR, METHOD OF	US01003678	US	99-366504	200100103298	200100103298	US01003678
A 051074	03-RF Devices	28-Jul-99	KEVIN BOYLE@PHILIPS.COM COMPACT 2T OR 3T NON-VOLATILE MEMORY CELL	US01003679	FR	99-099925	200100103299	200100103299	US01003679
N 017568	05-Mobile Communication	26-Jul-99	NAVEED, MAJID, 13657@PF.PHILIPS.COM ORTHOGONAL HELICAL ANTENNA WATCH	US01003680	GB	99-17678.8	200100103300	200100103300	US01003680
F 039056	07-Identification	23-Jul-99	DANISH, ALI@PHILIPS.COM POWER SUPPLY HAVING OVER-TEMPORARY AND OVER-CURRENT	US01003681	EP	99-362466	200100103301	200100103301	US01003681
N 017545	02-Packaging & Testing	22-Jul-99	DAVE VOY@PHILIPS.COM VARIABLE ORDER SIGMA-DELTA MODULATOR	US01003682	GB	99-17567.1	200100103302	200100103302	US01003682
D 039054	02-Identification	22-Jul-99	PHILIP, MAUGARS@PHILIPS.COM VOLTAGE-CURRENT CONVERTER	US01003683	US	99-363604	200100103303	200100103303	US01003683
N 017572	09-Discretes and Multimedia	20-Jul-99	KENICHI HORIE, 18166@PF.PHILIPS.COM BIDIIRECTIONAL INTERFACE	US01003684	EP	99-204746.8	200100103304	200100103304	US01003684
N 017564	09-Discretes and Multimedia	20-Jul-99	ERIK JAN MARUNISSEN@PHILIPS.COM MEMORY TEST WITH COMPRESSED OUTPUT	US01003685	EP	99-208818	200100103305	200100103305	US01003685
A 023704	05-Mobile Communication	18-Jul-99	ANDREA, MUEHLBERGER, 19882@PF.PHILIPS.COM TRENCH MOST EDGE TERMINATION BY WIDEEPS ACCESS	US01003686	EP	99-20416.8	200100103306	200100103306	US01003686
A 023721	05-Mobile Communication	16-Jul-99	ERWIN, HUIZEN@PHILIPS.COM INVERTED HV TRENCH MOST, NON-FLOATING P-BODY	US01003687	GB	99-17039.5	200100103307	200100103307	US01003687
B 017541	09-Discretes and Multimedia	14-Jul-99	KENG FUJING@PHILIPS.COM VARIABLE GAIN AMPLIFIER WITH IMPROVED LINEARITY	US01003688	US	99-354603	200100103308	200100103308	US01003688
B 034362	09-Discretes and Multimedia	13-Jul-99	RONALD, CLINE, 07909@PF.PHILIPS.COM FINE GRAIN FIELD, PROGRAMMABLE GATE ARRAY ARCHITECTURE	US01003689	US	99-254807	200100103309	200100103309	US01003689
D 039045	01-CMOS and embedded pr	12-Jul-99	HENRIK VAN DER WILST@PHILIPS.COM OFFSET CORRECTION IN A/D CONVERTER	US01003690	EP	99-202333.1	200100103310	200100103310	US01003690
A 051115	04-Audio/Video	7-Jul-99	BERNARD, GINETI, 21235@PF.PHILIPS.COM DESIGN FOR TEST AREA OPTIMIZATION ALGORITHM	US01003691	US	99-353306	200100103311	200100103311	US01003691
D 039046	02-Packaging & Testing	7-Jul-99	HENK, VAN DER WILST@PHILIPS.COM AUTOMATIC BANDSWITCHING	US01003692	DE	1999-2563.4	200100103312	200100103312	US01003692
A 023702	03-RF Devices	7-Jul-99	PHILIPPE, SILVESTRE, 21498@PF.PHILIPS.COM BUFFERING SYSTEM BUS FOR EXTERNAL MEMORY ACCESS	US01003693	US	99-351053	200100103313	200100103313	US01003693
039017	09-Discretes and Multimedia	7-Jul-99	HANS, PETER LOEHL@PHILIPS.COM BULK ACOUSTIC WAVE (BAW) FILTER	US01003694	DE	1999-31297.4	200100103314	200100103314	US01003694
060082	09-Discretes and Multimedia	1-Jul-99	RONALD, CLINE, 07590@PF.PHILIPS.COM FAST RECONFIGURABLE PROGRAMMABLE DEVICE	US01003695	US	99-24698.1	200100103315	200100103315	US01003695
051159	09-Discretes and Multimedia	30-Jun-99	LEO DE GROOT@PHILIPS.COM HIGH PRECISION CURRENT MIRROR	US01003696	US	99-18249.5	200100103316	200100103316	US01003696
N 017524	01-CMOS and embedded pr	30-Jun-99	BURKHARD, DICK@PHILIPS.COM LASER INTERFEROMETRY ENDPOINT DETECTION WITH WIDEN RANGES	US01003697	DE	1999-30391.9	200100103317	200100103317	US01003697
060081	04-Audio/Video	30-Jun-99	PETER VAN DER KERKOF@PHILIPS.COM VIDEO OVERLAY PROCESSOR WITH REDUCED RAM SIZE AND	US01003698	EP	99-202115.4	200100103318	200100103318	US01003698
039017	08-Automotive	30-Jun-99	PETER BUEHRING@PHILIPS.COM TRANSMITTER FOR DATA BUS	US01003699	US	99-24919.6	200100103319	200100103319	US01003699
05106	08-Connectivity	29-Jun-99	ALOK SINGH, 18874@PF.PHILIPS.COM FLAG-CONTROLLED ARBITRATION OF REQUESTING AGENTS	US01003700	DE	1999-30094.1	200100103320	200100103320	US01003700
039017	01-CMOS and embedded pr	21-Jun-99	XI-MEI, LING, 21318@PF.PHILIPS.COM SEMICONDUCTOR ARRANGEMENT HAVING CAPACITIVE STRUCTURE	US01003701	US	99-23300.0	200100103321	200100103321	US01003701
039017	01-CMOS and embedded pr	17-Jun-99	MARK JOHNSON, 21270@PF.PHILIPS.COM SET-ASSOCIATIVE CACHE MANAGEMENT METHOD WITH PARALLEL	US01003702	US	99-337059	200100103322	200100103322	US01003702
039017	04-Audio/Video	16-Jun-99	KAVE, KANUSH, 03288@PF.PHILIPS.COM ETCH RATE ALGORITHM FOR BATH CONTROL	US01003703	EP	99-21854.7	200100103323	200100103323	US01003703
039017	03-RF Devices	15-Jun-99	KENICHI HORIE, 18166@PF.PHILIPS.COM FM RECEIVER WITH BANDWIDTH CONTROL MEANS	US01003704	EP	99-21819.8	200100103324	200100103324	US01003704
039017	04-Audio/Video	15-Jun-99	MARC DURANTON@PHILIPS.COM INSTRUCTION-EFFICIENT MULTIPROCESSOR CONTROL	US01003705	EP	99-167888	200100103325	200100103325	US01003705
039017	04-Audio/Video	15-Jun-99	MARC DURANTON@PHILIPS.COM SINGLE INSTRUCTION HAVING OP-CODE AND STACK CONTROL	US01003706	US	99-233633	200100103326	200100103326	US01003706
039017	04-Audio/Video	15-Jun-99	MARC DURANTON@PHILIPS.COM STACK PROCESSOR WITH 1-ADDRESS INSTRUCTIONS	US01003707	US	99-20481.2	200100103327	200100103327	US01003707
039017	01-CMOS and embedded pr	11-Jun-99	PETER, STOKER@PHILIPS.COM LATERALLY-GRADED GATE MOST IN REPLACEMENT GATE	US01003708	EP	99-201866.7	200100103328	200100103328	US01003708
039017	02-Packaging & Testing	10-Jun-99	DETLEF, GOETTING, 15512@PF.PHILIPS.COM C TRELISCYCODER	US01003709	DE	1999-26649.2	200100103329	200100103329	US01003709
039017	02-Packaging & Testing	5-Jun-99	DEXIN LIANG, 21315@PF.PHILIPS.COM INTEGRATED CIRCUIT PACKAGES AND BALL-BRID ARRAY INTERF	US01003710	US	99-233240.1	200100103330	200100103330	US01003710
039017	09-Discretes and Multimedia	3-Jun-99	E STEFFENS@PHILIPS.COM BLEEDER WITH NON-LINEAR FIELD-PLATE DISTRIBUTION	US01003711	DE	1999-2768.2	200100103331	200100103331	US01003711
039017	02-Packaging & Testing	28-mei-99	STAN, MEEUSEN@PHILIPS.COM JAMES CUNNINGHAM, 21174@PF.PHILIPS.COM METHOD AND APPARATUS FOR PROVIDING AN EMBEDDED FLASH	US01003712	EP	99-32217.2	200100103332	200100103332	US01003712
A 051115	05-Mobile Communication	28-mei-99	GORDON, YANG, 21494@PF.PHILIPS.COM FRAME RATE DETERMINATION USING CORRELATION METRICS AN	US01003713	US	99-3511699	200100103333	200100103333	US01003713

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No.	Class.	Inventor	Title	Pub. No.	Pub. Date	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	IPC Class.	
A 051714	05-Mobile Communication	JOHN REAGAN	IMPROVED VOODOO METHOD	US 693222259	09/30/2006	US	093122259									
A 051221	05-Mobile Communication	HOWARD TRAN	METRIC MEMORY CONFIGURATION FOR THE VITERBI US	US 69321682	09/30/2006	US	09321682									
A 051223	05-Mobile Communication	TIEN NGUYEN	EFFICIENT METHOD OF PROVIDING SYMBOL DATA FROM DEMOD US	US 693221681	09/30/2006	US	093221681									
A 051224	05-Mobile Communication	HOWARD TRAN	APPARATUS AND METHOD FOR GENERATING A TRELUS US	US 69322703	09/30/2006	US	09322703									
A 051225	05-Mobile Communication	HOWARD TRAN	APPARATUS AND METHOD FOR INITIALIZING STATE METRICS FOR TR US	US 69322283	09/30/2006	US	09322283									
N 017455	01-CMOS and embedded pr	KEHLAS DRUJIF	DESIGN RULE FOR C49 INTO C54 PHASE TRANSFORMATION	US 69321660	09/30/2006	US	09321660									
F 069543	04-Audio/Video	MARC DURANTON	GENERIC INTERRUPT GENERATOR	EP 69201893.1	09/20/2006	EP	69201893.1									
N 017469	05-Mobile Communication	ARJAN STRUIJKER	ADAPTIVE VALLEY SWITCHING FOR SIMPS	US 692030908	09/20/2006	US	692030908									
D 059065	09-Discrete and Multimar	BURKHARD DICK	LOW NOISE LINEAR DIFFERENTIAL AMPLIFIER	US 69201654.3	09/20/2006	US	69201654.3									
A 023682	05-Mobile Communication	RISHI MOHINDRA	INCREASE OF TRANSMITTER ACPP BY GAIN MODULATION	DE 19524107.A	09/20/2006	DE	19524107.A									
N 017436	04-Audio/Video	JAN VAN GELOVEN	IC HAVING DYNAMIC MEMORY WITH BOOSTED PLATELINE	US 691318330	09/13/2006	US	691318330									
A 023680	04-Discrete and Multimar	RISHI MOHINDRA	REDUCTION OF TRANSMITTER INDUCED CROSS MODULATION IN I	US 691318330	09/13/2006	US	691318330									
A 023679	04-Discrete and Multimar	RUNE JENSEN	CLOCK SYSTEM FOR MULTIPLE COMPONENT SYSTEM	US 691318330	09/13/2006	US	691318330									
A 051144	01-CMOS and embedded pr	GALVIN GABRIEL	RESET SYSTEM FOR MULTIPLE COMPONENT SYSTEM	US 691318330	09/13/2006	US	691318330									
N 017454	04-Audio/Video	HARALD VRANKEN	BLOCKING LAYER TO PREVENT UV RADIATION DAMAGE TO IMDS	EP 69201658.6	09/20/2006	EP	69201658.6									
N 017447	05-Mobile Communication	ZHENHUA WANG	TRIMMED VECTOR DEBUG	US 69201578.4	09/20/2006	US	69201578.4									
D 059065	02-Packaging & Testing	JAYARAMA BHENY	RING OSC. PROVIDING SECOND SIGNAL AT POWER SUPPLY TERM	EP 69201578.4	09/20/2006	EP	69201578.4									
F 069544	05-Mobile Communication	SWAROOP ADUSUMILLI	INTERCONNECT LAYOUT PATTERN FOR INTEGRATED CIRCUIT PAI	US 693138695	09/13/2006	US	693138695									
A 051185	01-CMOS and embedded pr	FREDERIC DARTHERNA	PHI REDUCE POWER CONSUMPTION IN ARM-BASED SYSTEMS	FR 9906289	09/13/2006	FR	9906289									
A 051187	01-CMOS and embedded pr	ANDREW J BLACK	LOW SUPPLY TRACK & HOLD FOR A CCD	US 693113633	09/13/2006	US	693113633									
J 098003	03-RF Devices	LANDON VINES	IN-SITU BACKGROUND WAFER THICKNESS MONITOR	US 69312776	09/13/2006	US	69312776									
A 051130	04-Audio/Video	KENICHI HORIE	TEMPERATURE CONTROL CARRIER HEAD FOR CMP CHEMICAL ME	US 693136385	09/13/2006	US	693136385									
N 017438	09-Discrete and Multimar	ZENNIS KOUTSOURAS	ASYNCHRONOUS MEMORY SYSTEM WITH AUTOMATIC BURST MOD	US 693138244	09/13/2006	US	693138244									
A 051100	01-CMOS and embedded pr	LOUIS MELI	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
A 051150	05-Mobile Communication	TAMMY ZHENG	ASYMPTOTIC DETECTION DURING DRY ETCH OF SUBM	US 69312730	09/13/2006	US	69312730									
A 051075	06-Connectivity	TIMOTHY AUD	METHOD AND ARRANGEMENT FOR CYRATION FILTERING WITH LK	US 69312132	09/13/2006	US	69312132									
A 051078	06-Connectivity	DAVE EVOY	PC1 BRIDGE CONFIGURATION HAVING PHYSICALLY SEPARATE PAI	US 69311911	09/13/2006	US	69311911									
A 051148	04-Discrete and Multimar	TIMOTHY AUD	METHOD AND ARRANGEMENT FOR CYRATION FILTERING WITH LK	US 69312206	09/13/2006	US	69312206									
N 017445	09-Discrete and Multimar	TIMOTHY AUD	HIGH-VOLTAGE TOLERANT CASCADE CIRCUIT	EP 69201518	09/20/2006	EP	69201518									
A 051114	09-Discrete and Multimar	A.J. ANNEMAR	OBJECT-ORIENTED PROCESSING WITH DEDICATED POINTER MEA	US 69310470	09/13/2006	US	69310470									
A 051174	01-CMOS and embedded pr	SUBHAS BOTHRA	ARCHITECTURE HANDLING TWO PROCESSING SPEEDS	FR 9906018	09/13/2006	FR	9906018									
A 023635	12-mel-88	SAMIT SENGUPTA	COMPUTER-IMPLEMENTED COMPRESSION OF COMBINATION LOGI	US 69307165	09/10/2006	US	69307165									
F 069539	04-Audio/Video	KEVIN DUARIN	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051147	05-Mobile Communication	ERIC DUARIN	OBJECT-ORIENTED PROCESSING WITH DEDICATED POINTER MEA	US 69307164	09/10/2006	US	69307164									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									
A 051154	01-CMOS and embedded pr	SUBHAS BOTHRA	PACKAGE STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051135	02-Packaging & Testing	MIKE LOO	MOISTURE REPELLANT INTEGRATED CIRCUIT DIELECTRIC MATER	US 69306230	09/10/2006	US	69306230									
A 051126	02-Packaging & Testing	CHARLES DRILL	STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051136	05-Mobile Communication	SWAROOP ADUSUMILLI	SYSTEM AND APPARATUS FOR A GASEOUS ENVIRONMENT PROV	US 69306517	09/10/2006	US	69306517									
A 051126	04-Audio/Video	MATTHIAS MEYER	DIGITAL AM-DEMOCULATOR	DE 19520382.8	09/20/2006	DE	19520382.8									
A 051091	05-Mobile Communication	FERDINAND SLUIJS	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									
A 051154	01-CMOS and embedded pr	SUBHAS BOTHRA	PACKAGE STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051135	02-Packaging & Testing	MIKE LOO	MOISTURE REPELLANT INTEGRATED CIRCUIT DIELECTRIC MATER	US 69306230	09/10/2006	US	69306230									
A 051126	02-Packaging & Testing	CHARLES DRILL	STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051136	05-Mobile Communication	SWAROOP ADUSUMILLI	SYSTEM AND APPARATUS FOR A GASEOUS ENVIRONMENT PROV	US 69306517	09/10/2006	US	69306517									
A 051126	04-Audio/Video	MATTHIAS MEYER	DIGITAL AM-DEMOCULATOR	DE 19520382.8	09/20/2006	DE	19520382.8									
A 051091	05-Mobile Communication	FERDINAND SLUIJS	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									
A 051154	01-CMOS and embedded pr	SUBHAS BOTHRA	PACKAGE STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051135	02-Packaging & Testing	MIKE LOO	MOISTURE REPELLANT INTEGRATED CIRCUIT DIELECTRIC MATER	US 69306230	09/10/2006	US	69306230									
A 051126	02-Packaging & Testing	CHARLES DRILL	STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051136	05-Mobile Communication	SWAROOP ADUSUMILLI	SYSTEM AND APPARATUS FOR A GASEOUS ENVIRONMENT PROV	US 69306517	09/10/2006	US	69306517									
A 051126	04-Audio/Video	MATTHIAS MEYER	DIGITAL AM-DEMOCULATOR	DE 19520382.8	09/20/2006	DE	19520382.8									
A 051091	05-Mobile Communication	FERDINAND SLUIJS	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									
A 051154	01-CMOS and embedded pr	SUBHAS BOTHRA	PACKAGE STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051135	02-Packaging & Testing	MIKE LOO	MOISTURE REPELLANT INTEGRATED CIRCUIT DIELECTRIC MATER	US 69306230	09/10/2006	US	69306230									
A 051126	02-Packaging & Testing	CHARLES DRILL	STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051136	05-Mobile Communication	SWAROOP ADUSUMILLI	SYSTEM AND APPARATUS FOR A GASEOUS ENVIRONMENT PROV	US 69306517	09/10/2006	US	69306517									
A 051126	04-Audio/Video	MATTHIAS MEYER	DIGITAL AM-DEMOCULATOR	DE 19520382.8	09/20/2006	DE	19520382.8									
A 051091	05-Mobile Communication	FERDINAND SLUIJS	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									
A 051154	01-CMOS and embedded pr	SUBHAS BOTHRA	PACKAGE STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051135	02-Packaging & Testing	MIKE LOO	MOISTURE REPELLANT INTEGRATED CIRCUIT DIELECTRIC MATER	US 69306230	09/10/2006	US	69306230									
A 051126	02-Packaging & Testing	CHARLES DRILL	STRUCTURE FOR LOW COST AND ULTRA THIN CHIP SC	US 69306517	09/10/2006	US	69306517									
A 051136	05-Mobile Communication	SWAROOP ADUSUMILLI	SYSTEM AND APPARATUS FOR A GASEOUS ENVIRONMENT PROV	US 69306517	09/10/2006	US	69306517									
A 051126	04-Audio/Video	MATTHIAS MEYER	DIGITAL AM-DEMOCULATOR	DE 19520382.8	09/20/2006	DE	19520382.8									
A 051091	05-Mobile Communication	FERDINAND SLUIJS	DC/DC UP/DOWN CONVERTER WITH DIGITAL CONTROLLER	EP 69201403.5	09/20/2006	EP	69201403.5									
B 034340	05-Mobile Communication	CHRIS MARSHALL	CIRCUIT ARRANGEMENT PROVIDING IMPEDANCE TRANS. FILTER	US 69307164	09/10/2006	US	69307164									
A 051148	10-Other	TIM PONTIUS	FIFO SYSTEM WITH VARIABLE-WIDTH INTERFACE TO HOST PROC	US 69306230	09/10/2006	US	69306230									



Patent No.	IPC Class.	Inventor	App. No.	Pub. No.	Pub. Date	Pub. Type	Pub. Status
D 096051	06-Connectivity	GUENTER, TAUGHEN, 07053@PF.PHILIPS.COM	2000050565-01	US611129	19017016,9	DE	19017016,9
A 051125	06-Connectivity	SWAROOP, ADUSUMILLI, 21101@PF.PHILIPS.COM	2000050566-01	US611130	09/28/1402	US	09/28/1402
A 051038	02-Packaging & Testing	JAYARAMA, SHENDY, 21494@PF.PHILIPS.COM	2000050567-01	US611131	09/28/1402	US	09/28/1402
D 096050	09-Discretles and Multimedia	KLAUS, DIETMAYER, 17094@PF.PHILIPS.COM		US611132		DE	
G 034337	01-CMOS and embedded in	MANUFACTURABLE PUNCH THROUGH DIODE		US611133	18913968,8	DE	18913968,8
G 034337	01-CMOS and embedded in	A SYSTEM AND METHOD FOR PREVENTING ELECTROCHEMICAL E		US611134	993207162,2	EP	993207162,2
G 034337	01-CMOS and embedded in	THIN-FILM CAPACITOR WITH ELECTRIC DISCHARGE DAMAGE PROT		US611135	99/2873220	US	99/2873220
G 034337	01-CMOS and embedded in	STI WITH REDUCED EDGE RECESSION 88-CCP-206		US611136	99/2873150	US	99/2873150
G 034337	01-CMOS and embedded in	METHOD OF MANUFACTURING STRIPLINE COMPONENTS		US611137	99/2873228	US	99/2873228
A 051085	01-CMOS and embedded in	CMF PAD CONDITIONING ARRANGEMENT AND METHOD THEREFOR		US611138	18915246,4	DE	18915246,4
A 051085	01-CMOS and embedded in	DIAGNOSTIC METHOD AND ARRANGEMENT FOR CONTROLLING MULTIPLE		US611139	09/28/3949	US	09/28/3949
A 051085	01-CMOS and embedded in	MEMORY GENERATING ADDRESSES AFTER SET UP AUTONOMOUS		US611140	09/28/3949	US	09/28/3949
A 051085	01-CMOS and embedded in	COMBINING SUB-CHIP RESOLUTION SAMPLES IN ARMS		US611141	09/28/3949	US	09/28/3949
A 051085	01-CMOS and embedded in	C-AMPLIFIER WITH OUTPUT POWER CONTROL		US611142	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	MOS BI-DIRECTIONAL HIGH-SIDE SWITCH WITH DIODE		US611143	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	A SYSTEM BUS WITH A VARIABLE WIDTH SELECTIVELY CONFIGUR		US611144	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	DETECTION OF CD-RW DISCS		US611145	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	WITH RESPECT TO PCB SHIFTABLE HOLDER FOR CAPSULE		US611146	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	COMBINED IMPEDANCE TRANSFORMER AND BALUN TRANSFORMER		US611147	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	METHODS OF INSPECTING FOR MASK-DEFINED FEATURE DIMEN		US611148	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	TRENCH-GATE MOS WHEEL GEOMETRY		US611149	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	REPLACEMENT GATE WITH (ANGLED) IMPLANTATION		US611150	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	COUPLING RATIO NVM IN REPLACEMENT GATE TECHNOLOGY		US611151	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	INTEGRATED CIRCUIT WITH RELATIVE SENSE INVERSION OF SIGH		US611152	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	UNIPOLAR CONTROL MODES FOR THROTTLE PLATE ACTUATOR		US611153	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	RECONFIGURABLE SIGMA-DELTA ADC		US611154	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	METHODS FOR REDUCING SEMICONDUCTOR CONTACT RESISTAN		US611155	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	INTELLIGENT SPEAKER TRAINING USING MICROPHONE FEEDBACK		US611156	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	CUT-OFF LEVEL SETTING		US611157	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	SIMPLE PRECISION RECTIFIER		US611158	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	MAKING AN IC WITH A POLY EMITTER TRANSISTOR AND A CAPACIT		US611159	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	CAPSULE CONTACTS AND PRINTED BOARD CONTACTS EXTEND A		US611160	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	VIRTUAL GROUND EPROM WITHOUT CD BETWEEN WORDS		US611161	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	ARRANGEMENT AND METHOD FOR CALIBRATING OPTICAL LINE S		US611162	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	TEST SYSTEM FOR ELECTRONIC CIRCUITS		US611163	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	DIFFERENTIAL AMPLIFIER		US611164	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	INTEGRATED CIRCUIT DEVICES WITH HIGH AND LOW VOLTAGE		US611165	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	MULTIPLE STANDARD BUS ARCHITECTURE		US611166	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	METHOD FOR DIGITAL SYNCHRONIZATION OF VIDEO SIGNALS		US611167	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	HIGH POWER AMPLIFIER HAVING NON LINEARITY CORRECTOR		US611168	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	LEAD-FRAME FOR MEDIUM POWER IC'S		US611169	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	PROTECTION OF CHIP CARD IC'S AGAINST DPA		US611170	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	A SYSTEM AND METHOD TO PREDICT CONFIGURATION OF A BUS		US611171	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	PROTECTION OF DAMASCENE 88-CCP-229		US611172	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	PIPELINED MULTIPROCESSING WITH CONCURRENT WRITING TO		US611173	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	QUADRATURE OSCILLATOR WITH SOFT FREQUENCY REVERSAL		US611174	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	ADDT TEST ON DIGITAL PUT NEEDS NO DATA		US611175	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	COPROCESSOR FOR FAST MEMORY TRANSACTION		US611176	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	ALTERNATIVE PLASMA CHEMISTRY FOR ENHANCED PHOTORETIS		US611177	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	POWER-ON-RESET LOGIC WITH SECURE POWER DOWN CAPABILIT		US611178	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	METHOD AND APPARATUS FOR SECURE ADDRESS RE-MAPPING		US611179	99/2873150	US	99/2873150
A 051085	01-CMOS and embedded in	INTEGRATION OF SECURITY MODULES ON AN INTEGRATED CIRCU		US611180	99/2873150	US	99/2873150

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Date	Applicant	Inventor	Patent No.	Title	IPC Class
25-jan-99	03-RF Devices	MAREIKE KLEE@PHILIPS.COM	D 059016	THIN FILM NETWORKS	DE 19903500.8
25-jan-99	01-CMOS and embedded p	XI-WEI LIU 21316@PF.PHILIPS.COM	A 051105	LOCAL INTERCONNECT FORMED USING SILICON SPACER	US 09239456
25-jan-99	03-RF Devices	MAREIKE KLEE@PHILIPS.COM	D 059014	MULTI LAYER THIN FILM NETWORK	US 0703686
28-jan-99	03-RF Devices	MAREIKE KLEE@PHILIPS.COM	D 059017	MULTI LAYER THIN FILM NETWORK	US 0597488
28-jan-99	05-Connectivity	SWAROOP ADUSUMILLI 21101@PF.PHILIPS.COM	A 051087	ACOUSTIC RESISTANCES OF MOBILE PHONE AT CASING SEPAR. 2	EP 09239467
28-jan-99	05-Mobile Communication	ERICH KLEING@PHILIPS.COM	C 089502	SMART CARD COMPRISING AT LEAST TWO DECODER	EP 08950015.3
26-jan-99	07-Identification	FRANZ AMTMANN@PHILIPS.COM	B 034303	RC NOTCH FILTER	EP 0895016.1
25-jan-99	04-Audio/Video	MICHAEL NEILL 19439@PF.PHILIPS.COM	D 089012	LOG ANTENNA FOR MOBILE TELEPHONY BY SATELLITE	GB 8901314.1
25-jan-99	08-Discretes and Multimar	HANS PETER LECH@PHILIPS.COM	A 051081	CIRCUIT FOR ELECTRONIC DATA PROCESSING	DE 19902865.6
25-jan-99	09-Discretes and Multimar	STEPHAN LINKE 19989@PF.UNKNOWN.COM	A 051111	THIN FILM CAPACITOR WITH HIGH VOLTAGE STABILITY	US 09236126
21-jan-99	01-CMOS and embedded p	RAO ANNAFRAGADA 21109@PF.PHILIPS.COM	A 051016	THIN FILM CAPACITOR WITH HIGH VOLTAGE STABILITY	DE 19902168
20-jan-99	01-CMOS and embedded p	RAO ANNAFRAGADA 21109@PF.PHILIPS.COM	D 099008	THIN FILM CAPACITOR WITH HIGH VOLTAGE STABILITY	EP 09234235
30-jan-99	03-RF Devices	MAREIKE KLEE@PHILIPS.COM	F 059504	LOG ANTENNA FOR MOBILE TELEPHONY BY SATELLITE	DE 19902029.9
19-jan-99	07-Identification	HERVE JACOB@PHILIPS.COM	A 051063	CIRCUIT FOR ELECTRONIC DATA PROCESSING	FR 9300526
12-jan-99	09-RF Devices	WOLF GANG BUHR@PHILIPS.COM	N 017261	SYSTEM AND METHOD FOR MAXIMIZING DMA TRANSFERS OF ARE US	FR 9301629.4
11-jan-99	09-Discretes and Multimar	SUBHAS BOTHA 21133@PF.PHILIPS.COM	N 017261	SYSTEM AND METHOD FOR MAXIMIZING DMA TRANSFERS OF ARE US	EP 99204046.3
8-jan-99	04-Audio/Video	DAVE EVOY@PHILIPS.COM	A 051080	SECONDARY RADIATOR WITH TWO RESONANT FREQUENCIES	EP 092287122
7-jan-99	01-CMOS and embedded p	MILLIND WELING 21487@PF.PHILIPS.COM	A 051079	TRANSMISSION COIL CONFIGURATION COMPRISING THREE COILS EP	EP 09227031
7-jan-99	06-Connectivity	PETER CHAMBERS 21153@PF.PHILIPS.COM	D 059001	TRANSMISSION COIL CONFIGURATION COMPRISING THREE COILS EP	US 0910168.6
7-jan-99	07-Identification	GERRIT DEN BESTEN@PHILIPS.COM	N 017259	CONSTANT CURRENT SUPPLY REGULATOR FOR SMART CARD. 47	EP 99200228.5
30-dec-98	05-Mobile Communication	PETER RAEGAM@PHILIPS.COM	C 098562	CORIC FREQUENCY OFFSET CORRECTION FOR RECEIVER	EP 08980330.3
30-dec-98	07-Identification	ERICH MERLIN@PHILIPS.COM	C 098563	CORIC FREQUENCY OFFSET CORRECTION FOR RECEIVER	EP 08980331.1
26-dec-98	01-CMOS and embedded p	FABRICE JOVENIN 17187@PF.PHILIPS.COM	F 099531	TRANSMISSION COIL CONFIGURATION COMPRISING THREE COILS EP	EP 08980331.1
25-dec-98	05-Mobile Communication	LOI RICORDEL 18035@PF.PHILIPS.COM	F 099528	TRANSMISSION COIL CONFIGURATION COMPRISING THREE COILS EP	FR 9616569
24-dec-98	04-Audio/Video	LOI RICORDEL 18035@PF.PHILIPS.COM	A 023584	SECONDARY RADIATOR WITH TWO RESONANT FREQUENCIES	FR 9616569
24-dec-98	05-Mobile Communication	TOM DAVIES 08997@PF.UNKNOWN.COM	A 023584	SECONDARY RADIATOR WITH TWO RESONANT FREQUENCIES	FR 9616569
22-dec-98	04-Audio/Video	YVES DUFOUR 06507@PF.PHILIPS.COM	N 017234	OFF STATE BATTERY REMOVAL DETECTION	FR 9616569
22-dec-98	04-Audio/Video	YVES DUFOUR 06507@PF.PHILIPS.COM	N 017234	OFF STATE BATTERY REMOVAL DETECTION	FR 9616569
22-dec-98	05-Mobile Communication	EVERT SEEVINK 02663@PF.UNKNOWN.COM	A 023570	FRACTIONAL DIVISION CHARGE COMPENSATION	US 09221948
22-dec-98	05-Mobile Communication	VLADIMIR DVORKIN 18720@PF.UNKNOWN.COM	F 098624	FRACTIONAL DIVISION CHARGE COMPENSATION	EP 98204380.4
22-dec-98	05-Mobile Communication	PASCAL PHILIPPE@PHILIPS.COM	D 098186	LOW VOLTAGE CURRENT MODE SENSE AMP	US 09218647
22-dec-98	08-Connectivity	MATTHIAS WEISS@PHILIPS.COM	C 098561	DIGITAL MODULATION LOOP	EP 09218647
22-dec-98	08-Automotive	MARCUS TOIT@PHILIPS.COM	C 098561	DIGITAL MODULATION LOOP	EP 09218647
22-dec-98	08-Automotive	MARCUS TOIT@PHILIPS.COM	C 098561	DIGITAL MODULATION LOOP	EP 09218647
18-dec-98	01-CMOS and embedded p	DC SENSATIONS@PHILIPS.COM	A 051094	DEVICE AND METHOD FOR GENERATING AND EXECUTING COMPDE	FR 9816208
18-dec-98	01-CMOS and embedded p	SAM DUNTON 21192@PF.PHILIPS.COM	A 051156	DATA CARRIER WITH ENCAPSULATED COMPONENTS HOLDER ME EP	US 0920376.1
18-dec-98	01-CMOS and embedded p	MARK BAPST 21119@PF.PHILIPS.COM	N 017240	DATA CARRIER WITH ENCAPSULATED COMPONENTS CONNECTIN EP	EP 98990377.8
18-dec-98	02-Packaging & Testing	ANDREW MOKTREE@PHILIPS.COM	A 050896	CMOS HIGH-TO-LOW VOLTAGE BUFFER	US 09218701
18-dec-98	02-Packaging & Testing	DERVIN MATTOS 21081@PF.PHILIPS.COM	A 051102	CMOS HIGH-TO-LOW VOLTAGE BUFFER	US 09218701
18-dec-98	05-Mobile Communication	ANTONIO RICHIAEL 17391@PF.PHILIPS.COM	A 051103	REMOVAL OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	05-Mobile Communication	TIM PONTIUS@PHILIPS.COM	A 023588	REMOVAL OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	05-Mobile Communication	JEAN CHABAS 18693@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	07-Identification	STEVE CORNELIUS 21189@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	07-Identification	ANTONIO REIJERS 18579@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	07-Identification	ANTONIO REIJERS 18579@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	06-Connectivity	YAVARAMA SHENOY 21434@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
16-dec-98	04-Audio/Video	WIM VAN HOUTUM@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
15-dec-98	05-Mobile Communication	JEAN CHABAS 18693@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
15-dec-98	05-Mobile Communication	STEVE CORNELIUS 21189@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
14-dec-98	07-Identification	ANTONIO REIJERS 18579@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
14-dec-98	07-Identification	ANTONIO REIJERS 18579@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
14-dec-98	09-Discretes and Multimar	BRIAN MINNIS@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
14-dec-98	09-Discretes and Multimar	BRIAN MINNIS@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
11-dec-98	06-Connectivity	TEO LETAVIC@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
9-dec-98	05-Mobile Communication	SWAROOP ADUSUMILLI 21101@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
9-dec-98	05-Mobile Communication	JARMO SALO 18539@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
9-dec-98	04-Audio/Video	NIGEL GREEN 17798@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
5-dec-98	04-Audio/Video	FARZAD RAIYAT@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
5-dec-98	04-Audio/Video	FARZAD RAIYAT@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	01-CMOS and embedded p	HENK KLOENEN@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	01-CMOS and embedded p	HENK KLOENEN@PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942
4-dec-98	04-Audio/Video	ALEK MOVSHOVICH 21357@PF.PHILIPS.COM	A 051157	METHOD OF INORGANIC ANTI-REFLECTIVE COATING USING FLUX US	EP 09215942

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A 050369	4-dec-98	04-Audio/Video	ALEX MOVSHOVICH	TRANSPORT DEMULTIPLEXOR ARCHITECTURE WITH NON-US PROGRAM CONTROLLED TIME-STAMPS OVER VARIABLE LATENCY EP		050369	4-dec-98	04-Audio/Video	ALEX MOVSHOVICH	TRANSPORT DEMULTIPLEXOR ARCHITECTURE WITH NON-US PROGRAM CONTROLLED TIME-STAMPS OVER VARIABLE LATENCY EP		050369	4-dec-98	04-Audio/Video	ALEX MOVSHOVICH	TRANSPORT DEMULTIPLEXOR ARCHITECTURE WITH NON-US PROGRAM CONTROLLED TIME-STAMPS OVER VARIABLE LATENCY EP		050369	4-dec-98	04-Audio/Video	ALEX MOVSHOVICH	TRANSPORT DEMULTIPLEXOR ARCHITECTURE WITH NON-US PROGRAM CONTROLLED TIME-STAMPS OVER VARIABLE LATENCY EP	
N 017197	2-dec-98	08-Automatic	LUCK VAK PAEPEWICH	OPTIMIZED METAL ETCH PROCESS TO ENABLE THE USE OF ALUMINUM UV ERASE OF EPROM WITH PHOTOVOLTAAGE ON CONTROL GATE EP		017197	2-dec-98	08-Automatic	LUCK VAK PAEPEWICH	OPTIMIZED METAL ETCH PROCESS TO ENABLE THE USE OF ALUMINUM UV ERASE OF EPROM WITH PHOTOVOLTAAGE ON CONTROL GATE EP		017197	2-dec-98	08-Automatic	LUCK VAK PAEPEWICH	OPTIMIZED METAL ETCH PROCESS TO ENABLE THE USE OF ALUMINUM UV ERASE OF EPROM WITH PHOTOVOLTAAGE ON CONTROL GATE EP		017197	2-dec-98	08-Automatic	LUCK VAK PAEPEWICH	OPTIMIZED METAL ETCH PROCESS TO ENABLE THE USE OF ALUMINUM UV ERASE OF EPROM WITH PHOTOVOLTAAGE ON CONTROL GATE EP	
A 051108	1-dec-98	01-CMOS	TAMMY ZHENG	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		051108	1-dec-98	01-CMOS	TAMMY ZHENG	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		051108	1-dec-98	01-CMOS	TAMMY ZHENG	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		051108	1-dec-98	01-CMOS	TAMMY ZHENG	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS	
N 017205	1-dec-98	01-CMOS	KEVIN WIDDERSHOVEN	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		017205	1-dec-98	01-CMOS	KEVIN WIDDERSHOVEN	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		017205	1-dec-98	01-CMOS	KEVIN WIDDERSHOVEN	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS		017205	1-dec-98	01-CMOS	KEVIN WIDDERSHOVEN	CONCURRENT SERIAL INTERCONNECT FOR INTEGRATED FUNCTIONAL BLOCKS	
A 023545	30-nov-98	06-Connectivity	KEVIN ROSS	TRENCHMOST USING RESURF		023545	30-nov-98	06-Connectivity	KEVIN ROSS	TRENCHMOST USING RESURF		023545	30-nov-98	06-Connectivity	KEVIN ROSS	TRENCHMOST USING RESURF		023545	30-nov-98	06-Connectivity	KEVIN ROSS	TRENCHMOST USING RESURF	
G 034259	28-nov-98	05-Mobile Communication	JUD LEHMAN	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		034259	28-nov-98	05-Mobile Communication	JUD LEHMAN	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		034259	28-nov-98	05-Mobile Communication	JUD LEHMAN	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		034259	28-nov-98	05-Mobile Communication	JUD LEHMAN	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION	
N 017214	28-nov-98	05-Mobile Communication	MARTIN WILCOX	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		017214	28-nov-98	05-Mobile Communication	MARTIN WILCOX	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		017214	28-nov-98	05-Mobile Communication	MARTIN WILCOX	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION		017214	28-nov-98	05-Mobile Communication	MARTIN WILCOX	PULSE EDGE DETECTOR WITH DOUBLE RESOLUTION	
A 023546	25-nov-98	06-Connectivity	JERRY HONG BIN HAO	METHOD OF USING FILMS HAVING OPTIMIZED OPTICAL PROPERTIES		023546	25-nov-98	06-Connectivity	JERRY HONG BIN HAO	METHOD OF USING FILMS HAVING OPTIMIZED OPTICAL PROPERTIES		023546	25-nov-98	06-Connectivity	JERRY HONG BIN HAO	METHOD OF USING FILMS HAVING OPTIMIZED OPTICAL PROPERTIES		023546	25-nov-98	06-Connectivity	JERRY HONG BIN HAO	METHOD OF USING FILMS HAVING OPTIMIZED OPTICAL PROPERTIES	
A 051066	24-nov-98	01-CMOS	SUBHAS BOTHPRA	MANUFACTURING OF DOUBLE POLY TRANSISTOR WITH THIN BAS		051066	24-nov-98	01-CMOS	SUBHAS BOTHPRA	MANUFACTURING OF DOUBLE POLY TRANSISTOR WITH THIN BAS		051066	24-nov-98	01-CMOS	SUBHAS BOTHPRA	MANUFACTURING OF DOUBLE POLY TRANSISTOR WITH THIN BAS		051066	24-nov-98	01-CMOS	SUBHAS BOTHPRA	MANUFACTURING OF DOUBLE POLY TRANSISTOR WITH THIN BAS	
A 023544	24-nov-98	09-Discretes and Multimeter	WILMA SLAVKOFF	NESTED CHOPPER AMPLIFIER TECHNIQUE		023544	24-nov-98	09-Discretes and Multimeter	WILMA SLAVKOFF	NESTED CHOPPER AMPLIFIER TECHNIQUE		023544	24-nov-98	09-Discretes and Multimeter	WILMA SLAVKOFF	NESTED CHOPPER AMPLIFIER TECHNIQUE		023544	24-nov-98	09-Discretes and Multimeter	WILMA SLAVKOFF	NESTED CHOPPER AMPLIFIER TECHNIQUE	
A 051101	20-nov-98	01-CMOS	SUBHAS BOTHPRA	HIGH-ACCURACY P/T GENERATOR FOR SMART SENSORS		051101	20-nov-98	01-CMOS	SUBHAS BOTHPRA	HIGH-ACCURACY P/T GENERATOR FOR SMART SENSORS		051101	20-nov-98	01-CMOS	SUBHAS BOTHPRA	HIGH-ACCURACY P/T GENERATOR FOR SMART SENSORS		051101	20-nov-98	01-CMOS	SUBHAS BOTHPRA	HIGH-ACCURACY P/T GENERATOR FOR SMART SENSORS	
N 017183	18-nov-98	01-CMOS	PETER MEJER	CURRENT DIAPYCNIC CIRCUIT		017183	18-nov-98	01-CMOS	PETER MEJER	CURRENT DIAPYCNIC CIRCUIT		017183	18-nov-98	01-CMOS	PETER MEJER	CURRENT DIAPYCNIC CIRCUIT		017183	18-nov-98	01-CMOS	PETER MEJER	CURRENT DIAPYCNIC CIRCUIT	
A 051011	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051011	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051011	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051011	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT	
A 051012	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051012	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051012	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT		051012	18-nov-98	02-Packaging & Testing	AKHMAI HAMEZHOOST	ORGANIC DIAPYCNIC CIRCUIT	
A 051052	17-nov-98	01-CMOS	KENNETH REIS	OPTIMIZING THE PERFORMANCE OF ASYNCHRONOUS BUS BRIDG		051052	17-nov-98	01-CMOS	KENNETH REIS	OPTIMIZING THE PERFORMANCE OF ASYNCHRONOUS BUS BRIDG		051052	17-nov-98	01-CMOS	KENNETH REIS	OPTIMIZING THE PERFORMANCE OF ASYNCHRONOUS BUS BRIDG		051052	17-nov-98	01-CMOS	KENNETH REIS	OPTIMIZING THE PERFORMANCE OF ASYNCHRONOUS BUS BRIDG	
A 060022	16-nov-98	09-Discretes and Multimeter	J H HUIJUNG	WAFER TEST AFTER SEPARATING OF CRYSTALS		060022	16-nov-98	09-Discretes and Multimeter	J H HUIJUNG	WAFER TEST AFTER SEPARATING OF CRYSTALS		060022	16-nov-98	09-Discretes and Multimeter	J H HUIJUNG	WAFER TEST AFTER SEPARATING OF CRYSTALS		060022	16-nov-98	09-Discretes and Multimeter	J H HUIJUNG	WAFER TEST AFTER SEPARATING OF CRYSTALS	
N 017185	13-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		017185	13-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		017185	13-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		017185	13-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION	
A 051028	12-nov-98	05-Mobile Communication	HABIB COLNABI	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		051028	12-nov-98	05-Mobile Communication	HABIB COLNABI	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		051028	12-nov-98	05-Mobile Communication	HABIB COLNABI	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		051028	12-nov-98	05-Mobile Communication	HABIB COLNABI	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY	
A 060003	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		060003	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		060003	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		060003	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT	
N 017180	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		017180	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		017180	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT		017180	12-nov-98	09-Discretes and Multimeter	ANTON BAKER	DEVELOPMENT OF AN ANALOGUE-DIGITAL CONVERTER ARCHITECT	
D 036146	11-nov-98	04-Audio/Video	UDO SCHILLHOF	AUDIO STEP INTERPOLATION		036146	11-nov-98	04-Audio/Video	UDO SCHILLHOF	AUDIO STEP INTERPOLATION		036146	11-nov-98	04-Audio/Video	UDO SCHILLHOF	AUDIO STEP INTERPOLATION		036146	11-nov-98	04-Audio/Video	UDO SCHILLHOF	AUDIO STEP INTERPOLATION	
N 017167	11-nov-98	09-Discretes and Multimeter	BERK JAN ADELERHOF	MR POSITION SENSOR WITH LINEAR OUTPUT		017167	11-nov-98	09-Discretes and Multimeter	BERK JAN ADELERHOF	MR POSITION SENSOR WITH LINEAR OUTPUT		017167	11-nov-98	09-Discretes and Multimeter	BERK JAN ADELERHOF	MR POSITION SENSOR WITH LINEAR OUTPUT		017167	11-nov-98	09-Discretes and Multimeter	BERK JAN ADELERHOF	MR POSITION SENSOR WITH LINEAR OUTPUT	
A 051072	6-nov-98	06-Connectivity	SWARUP ADUSUMILLI	LATERAL THIN-FILM S/DI DEVICE HAVING LATERAL DEPLETION		051072	6-nov-98	06-Connectivity	SWARUP ADUSUMILLI	LATERAL THIN-FILM S/DI DEVICE HAVING LATERAL DEPLETION		051072	6-nov-98	06-Connectivity	SWARUP ADUSUMILLI	LATERAL THIN-FILM S/DI DEVICE HAVING LATERAL DEPLETION		051072	6-nov-98	06-Connectivity	SWARUP ADUSUMILLI	LATERAL THIN-FILM S/DI DEVICE HAVING LATERAL DEPLETION	
A 023531	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		023531	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		023531	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		023531	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF	
A 051079	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		051079	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		051079	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF		051079	5-nov-98	09-Discretes and Multimeter	TED LETAUX	SECURE MEMORY MANAGEMENT UNIT WHICH USES MULTIPLE CF	
D 036140	5-nov-98	07-Identification	MARK BUER	WAFER TEST AFTER SEPARATING OF CRYSTALS		036140	5-nov-98	07-Identification	MARK BUER	WAFER TEST AFTER SEPARATING OF CRYSTALS		036140	5-nov-98	07-Identification	MARK BUER	WAFER TEST AFTER SEPARATING OF CRYSTALS		036140	5-nov-98	07-Identification	MARK BUER	WAFER TEST AFTER SEPARATING OF CRYSTALS	
A 051069	3-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		051069	3-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		051069	3-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION		051069	3-nov-98	01-CMOS	JOACHIM ANKER	DATA CARRIER WITH COVER-UP OF POWER CONSUMPTION	
F 058642	3-nov-98	03-RF Devices	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058642	3-nov-98	03-RF Devices	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058642	3-nov-98	03-RF Devices	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058642	3-nov-98	03-RF Devices	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK	
C 058246	3-nov-98	07-Identification	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058246	3-nov-98	07-Identification	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058246	3-nov-98	07-Identification	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK		058246	3-nov-98	07-Identification	PETER THURINGER	BACKGATE USED FOR COMMON MODE FEEDBACK	
A 060001	2-nov-98	09-Discretes and Multimeter	MATTHIAS MUTH	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		060001	2-nov-98	09-Discretes and Multimeter	MATTHIAS MUTH	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		060001	2-nov-98	09-Discretes and Multimeter	MATTHIAS MUTH	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		060001	2-nov-98	09-Discretes and Multimeter	MATTHIAS MUTH	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY	
C 098545	2-nov-98	09-Discretes and Multimeter	KLAUS DIETMAYER	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		098545	2-nov-98	09-Discretes and Multimeter	KLAUS DIETMAYER	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		098545	2-nov-98	09-Discretes and Multimeter	KLAUS DIETMAYER	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY		098545	2-nov-98	09-Discretes and Multimeter	KLAUS DIETMAYER	AREA OPTIM. DUPLICATION OF COMPLEX DIGITAL CIRCUITRY	
D 036138	29-oct-98	04-Automotive	AUGUST PETERSEN	DEVICE FOR MEASURING AN ANGLE OFFSET FOR ANGULAR MEASUREMENT		036138	29-oct-98	04-Automotive	AUGUST PETERSEN	DEVICE FOR MEASURING AN ANGLE OFFSET FOR ANGULAR MEASUREMENT		036138	29-oct-98	04-Automotive	AUGUST PETERSEN	DEVICE FOR MEASURING AN ANGLE OFFSET FOR ANGULAR MEASUREMENT		036138	29-oct-98	04-Automotive	AUGUST PETERSEN	DEVICE FOR MEASURING AN ANGLE OFFSET FOR ANGULAR MEASUREMENT	
D 088139	29-oct-98	09-Discretes and Multimeter	GEORG KASPERKOVITZ	ANALOGUE-DIGITAL CONVERTER ARCHITECTURE		088139	29-oct-98	09-Discretes and Multimeter	GEORG KASPERKOVITZ	ANALOGUE-DIGITAL CONVERTER ARCHITECTURE		088139	29-oct-98	09-Discretes and Multimeter	GEORG KASPERKOVITZ	ANALOGUE-DIGITAL CONVERTER ARCHITECTURE		088139	29-oct-98	09-Discretes and Multimeter	GEORG KASPERKOVITZ	ANALOGUE-DIGITAL CONVERTER ARCHITECTURE	
D 069137	25-oct-98	09-Discretes and Multimeter	PETER CHAMBERS	INTERNAL PCI AGENTS		069137	25-oct-98	09-Discretes and Multimeter	PETER CHAMBERS	INTERNAL PCI AGENTS		069137	25-oct-98	09-Discretes and Multimeter	PETER CHAMBERS	INTERNAL PCI AGENTS		069137	25-oct-98	09-Discretes and Multimeter	PETER CHAMBERS	INTERNAL PCI AGENTS	
N 017142	22-oct-98	04-Audio/Video	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017142	22-oct-98	04-Audio/Video	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017142	22-oct-98	04-Audio/Video	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017142	22-oct-98	04-Audio/Video	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE	
A 051020	22-oct-98	06-Connectivity	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		051020	22-oct-98	06-Connectivity	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		051020	22-oct-98	06-Connectivity	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		051020	22-oct-98	06-Connectivity	CHRISTHAAR MULLER	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE	
N 017134	20-oct-98	01-CMOS	ALBERT CHEN	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017134	20-oct-98	01-CMOS	ALBERT CHEN	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017134	20-oct-98	01-CMOS	ALBERT CHEN	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE		017134	20-oct-98	01-CMOS	ALBERT CHEN	LOW COMPLEXITY DESIGN RATE DETERMINATION DETECTION DE	
A 023514	16-oct-98	05-Mobile Communication	INGOLF HOLT	RATE DETECT. IN DIRECT SEQ. CODE DIVISION MULT. ACCESS S		023514	16-oct-98	05-Mobile Communication	INGOLF HOLT	RATE DETECT. IN DIRECT SEQ. CODE DIVISION MULT. ACCESS S		023514	16-oct-98	05-Mobile Communication	INGOLF HOLT	RATE DETECT. IN DIRECT SEQ. CODE DIVISION MULT. ACCESS S		023514	16-oct-98	05-Mobile Communication	INGOLF HOLT	RATE DETECT. IN DIRECT SEQ. CODE DIVISION MULT. ACCESS S	
D 069036	14-oct-98	01-CMOS	DANIEL BOUTIN	RECOVERY OF ALIGNMENT MARK AFTER CMP		069036	14-oct-98	01-CMOS	DANIEL BOUTIN	RECOVERY OF ALIGNMENT MARK AFTER CMP		069036	14-oct-98	01-CMOS	DANIEL BOUTIN	RECOVERY OF ALIGNMENT MARK AFTER CMP		069036	14-oct-98	01-CMOS	DANIEL BOUTIN	RECOVERY OF ALIGNMENT MARK AFTER CMP	
B 034291	8-oct-98	06-Connectivity	PETER BUENRING	DEVELOPMENT OF A SIGNAL PROCESSOR FOR MULTIMEDIA		034291	8-oct-98	06-Connectivity	PETER BUENRING	DEVELOPMENT OF A SIGNAL PROCESSOR FOR MULTIMEDIA		034291	8-oct-98	06-Connectivity	PETER BUENRING	DEVELOPMENT OF A SIGNAL PROCESSOR FOR MULTIMEDIA		034291	8-oct-98	06-Connectivity	PETER BUENRING	DEVELOPMENT OF A SIGNAL PROCESSOR FOR MULTIMEDIA	
A 051020	5-oct-98	04-Audio/Video	DANISH ALI	SIGNAL DELTA ADC		051020	5-oct-98	04-Audio/Video	DANISH ALI	SIGNAL DELTA ADC		051020	5-oct-98	04-Audio/Video	DANISH ALI	SIGNAL DELTA ADC		051020	5-oct-98	04-Audio/Video	DANISH ALI	S	

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No.	Inventor	Title	Pub. No.	Pub. Date	IPC Class.	IPC Class.	IPC Class.	IPC Class.
N 017053	01-CMOS and embedded in	MAKING IC'S WITH INDUCTION WITH MAGNETIC CORE	20030312015A1	98093103, 1	EP	98093103, 1	98115026	01-CMOS and embedded in
A 050919	01-CMOS and embedded in	INTEGRATED CIRCUIT DEVICE INTERCONNECTION TECHNIQUES	20030312015A1	98115026	US	98115026	98117049	01-CMOS and embedded in
D 088108	07-Identification	PASSIVE-KEYLESS-ENTRY / -KEYLESS-GO SYSTEM	20030121021A1	98117049	EP	98117049	98203054	01-CMOS and embedded in
A 023497	01-CMOS and embedded in	LATERAL BIPOLAR PNP TRANSISTOR AND METHOD OF MAKING S. U.	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
N 017085	01-CMOS and embedded in	SELF ALIGNED POLYSEED LAYER	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
N 017077	04-Audio/Video	PASSIVE DRIVING OF LCD WITH PULSE WIDTH OFF-SET	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 051005	07-Identification	METHOD AND APPARATUS FOR GENERATING ONE TIME PADS S U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
N 017073	08-Automotive	CIRCUIT ARRANGEMENT FOR BIAS FEEDING	20030016204A1	98203054	DE	98203054	98203054	01-CMOS and embedded in
D 088100	01-CMOS and embedded in	IMPROVED SOURCE/DRAIN JUNCTIONS	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 051013	01-CMOS and embedded in	MOSFET STRUCTURE HAVING IMPROVED SOURCE/DRAIN JUNCTIONS	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
N 017066	01-CMOS and embedded in	APPROXIMATELY SQUARE WAVE SIGNAL GENERATION	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 050984	02-Packaging & Testing	VAPOR DOPED BASE CONNECTION	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 050979	01-CMOS and embedded in	MODEL SIMULATION INTERFACE FOR TESTING AND VERIFYING AN AS U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
N 017072	04-Audio/Video	BLACK/WHITE STABILIZATION WITH OSD-GENERATED REFERENCE	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
N 017362	04-Audio/Video	TUNED BAND-PASS CIRCUIT	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
B 034277	05-Mobile Communication	INTELLIGENT POLYPHASE RECEIVER	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
C 098537	02-Packaging & Testing	TESTING OF ANALOG PARAMETERS OF RFID MODULES	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
B 034274	20-aug-98	DIFFERENTIAL TEMPERATURE SENSE CELLS IN POWER MOS	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 050247	01-CMOS and embedded in	SEMICONDUCTOR DEVICE WITH HIGH-TEMPERATURE-STABLE GA U S	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
A 051029	01-CMOS and embedded in	SEMICONDUCTOR HAVING SELF-ALIGNED, BURIED ETCH-STOP FC U S	20030016204A1	98203054	FR	98203054	98203054	01-CMOS and embedded in
F 098595	18-aug-98	ENHANCED PNP CHARGE-PUMP	20030016204A1	98203054	FR	98203054	98203054	01-CMOS and embedded in
A 050980	01-CMOS and embedded in	METHODS AND APPARATUS FOR EXTRACTING PARASITIC CAPACIT	20030016204A1	98203054	FR	98203054	98203054	01-CMOS and embedded in
G 050987	17-aug-98	PROGRAMMABLE ONE BIT CPU	20030016204A1	98203054	DE	98203054	98203054	01-CMOS and embedded in
C 098535	17-aug-98	TRANSISTOR WITH REDUCED POWER CONSUMPTION AT REDE EP	20030016204A1	98203054	DE	98203054	98203054	01-CMOS and embedded in
B 034272	14-aug-98	MOSFET WITH SILICON-GERMANIUM SOURCE	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
N 017058	01-CMOS and embedded in	IMPROVED TITANIUM SALICIDE PROCESS MODULE	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
N 017054	01-CMOS and embedded in	LOW VOLTAGE TRIGGERING SILICON CONTROLLED RECTIFIER EE EP	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
D 098085	31-jul-98	NEW ROBUST PHASE COMPARATOR FOR APPL. IN DELAY LOCKE	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
D 098085	31-jul-98	AUTHENTICITY CHECK OF PROGRAM EXECUTION IN SMART-VA	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
C 098532	31-jul-98	REFUSING ANALYTICAL PROCESSES FROM SOFTWARE COMPUTE	20030016204A1	98203054	DE	98203054	98203054	01-CMOS and embedded in
A 050978	24-jul-98	METHOD FOR FORMING VAS THROUGH POROUS DIELECTRIC MA U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 050976	24-jul-98	CMOS WAVESHAPING BUFFER	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
N 017041	24-jul-98	HIGH-SPEED BUS (2D)	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 023475	24-jul-98	SILICON-ON-INSULATOR (SOI) HYBRID TRANSISTOR DEVICE STRU U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 051032	23-jul-98	CAVITY DOWN HBG PACKAGE STRUCTURE	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 051017	22-jul-98	PROCESS TO IMPROVE ADHESION OF PEGD CAP LAYERS IN INTI U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 051018	22-jul-98	PROCESS TO IMPROVE ADHESION OF CAP LAYERS IN INTEGRATE U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
G 098531	21-jul-98	SWITCHING OVER BETWEEN 1 AND 2 SUBCARRIERS TRANSMISSI	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 023465	20-jul-98	HIGH-SPEED CURRENT SWITCH WITH COMPLEMENTARY STAGES U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
A 050974	17-jul-98	DOCKING SYSTEM AND METHODS FOR DETECTING AND MAN U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 050948	15-jul-98	FLOATING GATE UV-ERASER WITH HIGH TEMPERATURE	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
017069	13-jul-98	METHOD AND APPARATUS FOR IC PIN CUTTING	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
088529	13-jul-98	DATA CARRIER WITH AT LEAST TWO DEMODULATORS	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
T 098530	13-jul-98	CONVERTER CIRCUIT AND VARIABLE GAIN AMPLIFIER WITH TEMP U S	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
A 023453	13-jul-98	TRENCH MOST WITH POLYSILICON SIDEWALL SOURCE	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
034265	11-jul-98	METHOD AND APPARATUS FOR READING MULTIPLE MATCHED AD U S	20030016204A1	98203054	GB	98203054	98203054	01-CMOS and embedded in
035374	7-jul-98	DOUBLE WINDOW WITH ONE FIELD * MEN	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034248	7-jul-98	ANALOG TEST ACCESS PORT AND METHOD THEREOF	20030016204A1	98203054	EP	98203054	98203054	01-CMOS and embedded in
034246	6-jul-98	CURRENT STEERING VARIABLE GAIN AMPLIFIER WITH LINEARIZE	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034245	6-jul-98	BURST MODE TRANSFER BETWEEN SCALER AND FRAME BUFFER EP	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034244	30-jun-98	VARIABLE GAIN AMPLIFIER WITH IMPEDANCE NETWORK	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034243	30-jun-98	LOW VOLTAGE BETA LINEAR VARIABLE GAIN AMPLIFIER	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034242	30-jun-98	INTEGRATED MIRROR AND SIGMA-DELTA A/D CONVERSION FOR F	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034241	29-jun-98	CHEMICAL WET ETCH REMOVAL OF UNDERLAY MATERIAL AFTI U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034240	29-jun-98	SWITCHED-MODE POWER SUPPLY HAVING SAMPLE-AND-HOLD CI U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034239	29-jun-98	SECURE CACHE FOR INSTRUCTION AND DATA PROTECTION	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034238	28-jun-98	MEMORY CONFIGURATIONS WHICH SUPPORT MULTIPLE CRYPTO U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034237	28-jun-98	LOW POWER PLUS-IN-CARD REMOVAL DETECTION	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034236	28-jun-98	DUAL POINTER CIRCULAR QUEUE	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034235	25-jun-98	PHYSICAL LAYER SECURITY MANAGER FOR MEMORY-MAPPED SE U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in
034234	25-jun-98	SECURE DATA COMMUNICATION OVER A MEMORY-MAPPED SERU U S	20030016204A1	98203054	US	98203054	98203054	01-CMOS and embedded in

ASAMI KASEI KABUSHIKI KAISHA

Pub. No.	Pub. Date	Inventor	Class.	Title	Pub. No.	Pub. Date	Inventor	Class.	Title
A 052869	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	OPCODE-DRIVEN BUFFER MANAGER FOR USE IN DATA STORAGE	US608041	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	OPCODE-DRIVEN BUFFER MANAGER FOR USE IN DATA STORAGE
A 052870	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	NON-COHERENT MLSE-BASED EQUALIZATION FOR ISI-CHANNELS	US608042	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	NON-COHERENT MLSE-BASED EQUALIZATION FOR ISI-CHANNELS
A 052871	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SEMICONDUCTOR CALIBRATION STRUCTURES	US608043	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SEMICONDUCTOR CALIBRATION STRUCTURES
A 052872	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CHANGING DATA CODING METHOD FOR REDUCING SIDEBAND	US608044	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CHANGING DATA CODING METHOD FOR REDUCING SIDEBAND
A 052873	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CAPACITIVE DC-DC CONVERTER WITH VARIABLE VOLTAGE FACT	US608045	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CAPACITIVE DC-DC CONVERTER WITH VARIABLE VOLTAGE FACT
A 052874	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	COMMUNICATION SYSTEMS, COMMUNICATION METHODS AND A U.S. PRODUCTION OF THIN LAYERS OF SEMICONDUCTOR MATERIAL	US608046	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	COMMUNICATION SYSTEMS, COMMUNICATION METHODS AND A U.S. PRODUCTION OF THIN LAYERS OF SEMICONDUCTOR MATERIAL
A 052875	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	APPARATUS FOR ON-EAR-MODE AND FROM-EAR-MODE INTEGRATED SINGLE PIN XTAL OSCILLATOR	US608047	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	APPARATUS FOR ON-EAR-MODE AND FROM-EAR-MODE INTEGRATED SINGLE PIN XTAL OSCILLATOR
A 052876	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	OPTIMIZED CPU-MEMORY HIGH-BANDWIDTH MULTIBUS STRUCTURE	US608048	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	OPTIMIZED CPU-MEMORY HIGH-BANDWIDTH MULTIBUS STRUCTURE
A 052877	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CENTRALLY CONTROLLED INTERFACE SCHEME FOR PROMOTING BUS PERFORMANCE	US608049	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CENTRALLY CONTROLLED INTERFACE SCHEME FOR PROMOTING BUS PERFORMANCE
A 052878	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	HIGH SYNTHESIS CONSTRAINTS ALLOCATION AUTOMATING THE BUS	US608050	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	HIGH SYNTHESIS CONSTRAINTS ALLOCATION AUTOMATING THE BUS
A 052879	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SYSTEM INCLUDING ESD PROTECTION ACCURATE PITCH DETECTION	US608051	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SYSTEM INCLUDING ESD PROTECTION ACCURATE PITCH DETECTION
A 052880	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	STREAM CACHE INVALIDATION INTERFERENCE CIRCUIT WITH SLEW RATE CONTROL	US608052	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	STREAM CACHE INVALIDATION INTERFERENCE CIRCUIT WITH SLEW RATE CONTROL
A 052881	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	DEEP ULTRA VIOLET PHOTOGRAPHY ANTIREFLECTIVE COATING	US608053	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	DEEP ULTRA VIOLET PHOTOGRAPHY ANTIREFLECTIVE COATING
A 052882	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	DC OFFSET COMPENSATION FOR ZERO IF QUADRATURE DEMODULATOR	US608054	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	DC OFFSET COMPENSATION FOR ZERO IF QUADRATURE DEMODULATOR
A 052883	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRANSPONDER WITH IMPROVED VOLTAGE LIMITING MEANS	US608055	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRANSPONDER WITH IMPROVED VOLTAGE LIMITING MEANS
A 052884	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	EEPROM IN STANDARD CMOS PHASE FREQ. DETECTOR HAVING INSTANTANEOUS DIFFERENCE	US608056	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	EEPROM IN STANDARD CMOS PHASE FREQ. DETECTOR HAVING INSTANTANEOUS DIFFERENCE
A 052885	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TELETEXT WITH TRANSPARENT FUNCTION	US608057	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TELETEXT WITH TRANSPARENT FUNCTION
A 052886	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SUBSTRATE CURRENT LIMITER	US608058	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	SUBSTRATE CURRENT LIMITER
A 052887	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	ARRANGEMENT AND METHOD FOR DRAM CELL USING SHALLOW TRENCH	US608059	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	ARRANGEMENT AND METHOD FOR DRAM CELL USING SHALLOW TRENCH
A 052888	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	WIRELESS TELEPHONE SYSTEM COMPATIBLE WITH DIFFERENT MULTIBEAM ANTENNA DIVERSITY CIRCUITS	US608060	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	WIRELESS TELEPHONE SYSTEM COMPATIBLE WITH DIFFERENT MULTIBEAM ANTENNA DIVERSITY CIRCUITS
A 052889	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TESTABLE IC HAVING ANALOG AND DIGITAL CIRCUITS	US608061	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TESTABLE IC HAVING ANALOG AND DIGITAL CIRCUITS
A 052890	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	ADAPTIVE SAMPLING FOR BUILDING ACCURATE CMOS LOW POWER SCANNABLE COUNTER	US608062	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	ADAPTIVE SAMPLING FOR BUILDING ACCURATE CMOS LOW POWER SCANNABLE COUNTER
A 052891	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRENCH-GATE MOST WITH SELF-ALIGNED SPACERS	US608063	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRENCH-GATE MOST WITH SELF-ALIGNED SPACERS
A 052892	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRENCH-GATE MOST WITH COMPLIMENTARY AOP METHODS AND APPARATUS FOR CALCULATING ALIGNMENT OF TRENCH	US608064	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	TRENCH-GATE MOST WITH COMPLIMENTARY AOP METHODS AND APPARATUS FOR CALCULATING ALIGNMENT OF TRENCH
A 052893	2001-01-09	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CORDLESS RECEIVER	US608065	2000-07-12	Subhas Botthra, 21133@PF.PHILIPS.COM	01-CMOS and embedded	CORDLESS RECEIVER

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Patent No.	Class	Date	Inventor	Assignor	IPC Class	Pub No.	Pub Date	Inventor	Assignor	IPC Class	Pub No.	Pub Date
A 050921	09-Discretes and Multimark	15-apr-98	HABIB, COLNABI, 21239@PF.PHILIPS.COM	RECURSIVE LOOKAHEAD-BASED 2N-BIT SERIAL MULTIPLIERS ONE US	US	09/061820	09/06/1998					
A 050957	04-Audio/Video	15-apr-98	REMI, BUTAUD, 21142@PF.PHILIPS.COM	TRANSISTOR STACK READ ONLY MEMORY	US	09/060113	09/06/1998					
A 050990	09-Discretes and Multimark	13-apr-98	DC, SESSIONS@PHILIPS.COM	PSEUDO-DIFFERENTIAL LOGIC RECEIVER	US	09/059631	09/05/1998					
N 016646	09-Discretes and embedded pr	9-apr-98	ADAM, BROWN@PHILIPS.COM	FAST SWITCHING DIODE WITH SE SE	EP	98/201133	09/20/1998					
N 016659	05-Mobile Communication	9-apr-98	VERONIKA, SAUER@PHILIPS.COM	MULTI-STAGE COUPLED SAWTOOTH GENERATOR	DE	198/033553-A2	198/03/1998					
C 090040	09-Discretes and Multimark	9-apr-98	ED, VAN TUJUL@PHILIPS.COM	IC CARRIER FOIL FOR COMBI CARDS	DE	198/100666-B	198/10/1998					
N 016685	07-Identification	3-apr-98	MARCO, BEKOU@PHILIPS.COM	FFT-PROCESSOR WITH MULTIPLE BUTTERFLY'S IN PARALLEL	EP	98/201101,7	09/20/1998					
C 090751	02-Packaging & Testing	3-apr-98	TOMI, MASSINGILL, 21335@PF.PHILIPS.COM	PL FOR VIDEO- SOUND DEMODULATOR	DE	198/04686,2	198/04/1998					
C 090834	04-Audio/Video	31-mnt-98	THOMAS, HAFEMEISTER@PHILIPS.COM	PSEUDO-SCAN TESTING USING HARDWARE-ACCESSIBLE IC STRU US	US	09/052859	09/05/1998					
A 030975	01-CMOS and embedded pr	31-mnt-98	SUBHAS, BOTHRA, 21193@PF.PHILIPS.COM	SEMI-CONDUCTOR STRUCTURES FOR SUPPRESSING GATE OXIDE US	US	09/052796	09/05/1998					
A 030983	01-CMOS and embedded pr	31-mnt-98	LAURENT, SOUTER@PHILIPS.COM	SCAN TESTING USING HARDWARE-ACCESSIBLE IC STRU US	US	09/052900	09/05/1998					
A 050976	03-SP Devices	31-mnt-98	SUBHAS, BOTHRA, 21193@PF.PHILIPS.COM	CC AUTOMATED DESIGN OF ON-CHIP CAPACITIVE STRUCTURES FOR US	US	09/052900	09/05/1998					
F 090546	05-Mobile Communication	29-mnt-98	OLIVIER, BOZZETTO, 17229@PF.PHILIPS.COM	SHALLOW TRENCH CAPACITIVE STRUCTURES FOR SUPPRESSING US	US	09/052865	09/05/1998					
C 090510	03-RF Devices	27-mnt-98	PETER, MASSEY@PHILIPS.COM	FRANGE IMPROVING IN CTO	FR	98/03985	09/03/1998					
C 090511	07-Identification	27-mnt-98	THOMAS, RIENER@PHILIPS.COM	MULTIPLE FEED ANTENNA	GB	98/06812,9	09/06/1998					
C 090515	07-Identification	27-mnt-98	THOMAS, RIENER@PHILIPS.COM	METAL-LEADFRAME-MODUL WITH DOUBLE SIDED MOLD CAP	GB	98/06812,9	09/06/1998					
D 090825	07-Identification	24-mnt-98	WOLFGANG, EBER@PHILIPS.COM	METAL-LEADFRAME-MODUL FOR IMPLANT TECHNOLOGY	EP	98/06812,9	09/06/1998					
A 050939	04-Audio/Video	20-mnt-98	THOMAS, GIESLER@PHILIPS.COM	PASSIVE TRANSDUCER WITH CLOCK SIGNAL SUPPRESSING MEJ EP	EP	98/06812,9	09/06/1998					
F 090540	05-Mobile Communication	18-mnt-98	VISHAL, ANAND, 21109@PF.PHILIPS.COM	LIMITATION OF ANTENNA RESONANCE CIRCUIT	DE	198/13726,6	198/13/1998					
A 051286	04-Audio/Video	16-mnt-98	HENRI, COURTIER@PHILIPS.COM	METHOD OF AND SYSTEM FOR ALLOWING A COMPUTER SYSTEM US	US	09/045469	09/04/1998					
C 090911	01-CMOS and embedded pr	13-mnt-98	KEES, VAN DER SANDEN@PHILIPS.COM	LOW LEAKAGE 4T MEMORY CELL	FR	98/200266,6	09/20/1998					
A 050841	01-CMOS and embedded pr	12-mnt-98	WING, KEI AU, 21067@PF.PHILIPS.COM	METHOD OF PROTECTING QUARTZ HARDWARE FROM ETCHING US	US	09/042005	09/04/1998					
D 090822	05-Connectivity	12-mnt-98	GABRIEL, MUNGUIA, 21358@PF.PHILIPS.COM	FORMATION OF LOW RESISTIVITY TITANIUM SALICIDE	FR	98/03134	09/03/1998					
D 090820	07-Identification	11-mnt-98	THOMAS, WYER@PHILIPS.COM	SMART CARD CONTROLLER WITH "GLUE LOGIC"	DE	198/10730,7	198/10/1998					
D 090821	09-Discretes and Multimark	10-mnt-98	ROBERT, MOTES@PHILIPS.COM	CURRENT DAC OUTPUT LEVEL CONTROL	DE	198/10469,3	198/10/1998					
N 016737	08-Automotive	9-mnt-98	JOHANNES, RUNS, 06639@PF.PHILIPS.COM	BUS SYSTEM WITH AVAILABLE	DE	198/10235,6	198/10/1998					
D 090916	04-Audio/Video	7-mnt-98	OLAF, BUEHLER, 16379@PF.PHILIPS.COM	OPTICAL READING APPARATUS WITH LF AND HF CURRENT MIRROR EP	EP	98/200748,6	09/20/1998					
A 050923	05-Mobile Communication	5-mnt-98	SAMIT, SENGUPTA, 21427@PF.PHILIPS.COM	POWER SUPPLY FOR RECEIVER DURING STAND-BY-MODE	DE	198/03985,3	09/03/1998					
N 016794	01-CMOS and embedded pr	2-mnt-98	MARTIN, KOTTER@PHILIPS.COM	ETCH-RATE MONITORING USING RESONANT CRYSTALS	US	09/03572,5	09/03/1998					
N 016776	10-Other	2-mnt-98	RONALD, DEKKER@PHILIPS.COM	SOA DEVICE WITH LOW ER LAYER	EP	98/200645,4	09/20/1998					
B 052426	09-Discretes and Multimark	28-feb-98	HOLGER, SCHLICHTENHORST@PHILIPS.COM	PNP POWER TRANSISTORS & POWER DIODES ON P-SUBSTRATE EP	EP	98/200644,7	09/20/1998					
A 050977	04-Audio/Video	26-feb-98	KARL, WITTE@PHILIPS.COM	UNIVERSAL INTERFACE BETWEEN PERIPHERALS & P-BUS	GB	98/031698	09/03/1998					
B 050978	01-CMOS and embedded pr	26-feb-98	XI, WELIN, 21319@PF.PHILIPS.COM	SYSTEM FOR SHARING RESOURCES IN A DIGITAL FILTER	US	09/031698	09/03/1998					
B 050979	01-CMOS and embedded pr	26-feb-98	XI, WELIN, 21319@PF.PHILIPS.COM	METHOD OF MINIMIZING DISHING DURING CHEMICAL MECHANICAL US	US	09/031698	09/03/1998					
N 016783	05-Mobile Communication	25-feb-98	JONATHAN, WATSON, 16341@PF.PHILIPS.COM	CLOCK RECOVERY CIRCUIT FOR PAGER	GB	98/200721	09/20/1998					
N 016780	01-CMOS and embedded pr	25-feb-98	PAUL, GRADENWITZ, 14017@PF.PHILIPS.COM	UNKNOWA COMPACT GATE PROTECTION DIODE	EP	98/200597,7	09/20/1998					
N 016772	05-Mobile Communication	25-feb-98	CORNEEL, SCHUIJER, 02632@PF.PHILIPS.COM	COMMUNICATION DEVICE WITH PHASE CONTINUOUS SYNCHRON EP	EP	98/200595,1	09/20/1998					
A 050682	05-Mobile Communication	24-feb-98	CORNEEL, SCHUIJER, 02632@PF.PHILIPS.COM	UNIVERSAL INTERFACE BETWEEN PERIPHERALS & P-BUS	EP	98/200596,9	09/20/1998					
B 050686	05-Mobile Communication	20-feb-98	DAVID, AUER, 21115@PF.PHILIPS.COM	PROGRAMMABLE DELAY PATH CIRCUIT AND OPERATING POINT FUS EP	EP	09/022687	09/02/1998					
N 016779	05-Mobile Communication	20-feb-98	PHILIPPE, GAGLIONE, 21223@PF.PHILIPS.COM	VOICE RECORDING AND PLAYBACK MODE USING THE G.728 HALF US	US	09/022684	09/02/1998					
N 016780	05-Mobile Communication	20-feb-98	LOTHAR, CLAUSNER, 17995@PF.PHILIPS.COM	HYBRID AMPLIFIER	DE	198/07074,8	198/07/1998					
N 016781	05-Mobile Communication	20-feb-98	ERIC, LAURENT, 18110@PF.PHILIPS.COM	POWER CONSUMPTION REDUCTION IN A MSI	EP	98/400421,8	09/40/1998					
C 050966	05-Mobile Communication	18-feb-98	ERIC, LAURENT, 18110@PF.PHILIPS.COM	POWER CONSUMPTION REDUCTION IN A MS II	EP	98/400422,6	09/40/1998					
N 050967	01-CMOS and embedded pr	18-feb-98	ED, NOWAK, 21373@PF.PHILIPS.COM	PROTECTION CIRCUITS AND METHODS OF PROTECTING A SEMI-CONDUCTOR EP	EP	98/400423,4	09/40/1998					
N 050968	05-Mobile Communication	17-feb-98	DERWIN, MATTOZ, 21081@PF.PHILIPS.COM	METHODS OF PROTECTING A SEMI-CONDUCTOR DEVICE	US	09/025429	09/02/1998					
N 050969	05-Mobile Communication	17-feb-98	ERWALD, FRASL, @PHILIPS.COM	MEMBRANE WITH AXIAL WAVESHAPED SUPPORTING ZONE	EP	98/090944,5	09/09/1998					
N 050970	07-Connectivity	17-feb-98	ERWALD, FRASL, @PHILIPS.COM	MEMBRANE WITH U-SHAPED COIL SUPPORTING ZONE	EP	98/090945,2	09/09/1998					
N 050971	07-Connectivity	17-feb-98	RONALD, LANGE, 21302@PF.PHILIPS.COM	SELECTIVE DATA READ-AHEAD IN BUS-TO-BUS BRIDGE ARCHITECT EP	EP	09/025020	09/02/1998					
N 050972	04-Audio/Video	16-feb-98	SIGI, ARNO@PHILIPS.COM	TRANSPOUNDER-READER WITH MATCHED RECEIVER MEANS	EP	98/090046	09/09/1998					
N 050973	01-CMOS and embedded pr	16-feb-98	FONS, VAN AMESF, ODRIT@PHILIPS.COM	SATELLITE RECEIVER	US	09/023834,5	09/02/1998					
N 050974	01-CMOS and embedded pr	16-feb-98	S, GENDET, 17110@PF.PHILIPS.COM	METHOD FOR CLEANING SUBSTRATES WATERIZONE I	US	09/023963	09/02/1998					
N 050975	07-Identification	12-feb-98	CHARLES, DRILL, 21190@PF.PHILIPS.COM	SYSTEM FOR DISCOVERING BY-PRODUCT AGGLOMERATIONS FROM US	US	09/022847	09/02/1998					
N 050976	05-Mobile Communication	11-feb-98	MARK, SUER, 21198@PF.PHILIPS.COM	PIPELINE HARDWARE IMPLEMENTATION OF A WASHING ALGORITHM US	US	09/021735	09/02/1998					
N 050977	04-Audio/Video	10-feb-98	VENKAT, NAGASWAMI, 17798@PF.PHILIPS.COM	EXTENDED COMMON ACCESS CHANNEL LOW POWER USAGE WITH US	US	98/200413,7	09/20/1998					
N 050978	05-Mobile Communication	9-feb-98	FRANCOIS, NIOT, 20925@PF.PHILIPS.COM	IC MANUFACTURING PROCESS CONTROL USING DEFECTS	EP	09/021679	09/02/1998					
N 050979	09-Discretes and Multimark	9-feb-98	PETER, HURKES@PHILIPS.COM	MEMORY INTERFACES, METHODS OF EXEC US	US	09/022850	09/02/1998					
N 050980	09-Discretes and Multimark	8-feb-98	FRED, HURKES@PHILIPS.COM	SYSTEM TO DETECT A POWER MANAGEMENT SYSTEM RESUME E US	US	98/200394,9	09/20/1998					
N 050981	09-Discretes and Multimark	8-feb-98	RENE, ZINGGG@PHILIPS.COM	PRECISE LATERAL RESURF DOPING PROFILE	EP	98/200396,4	09/20/1998					
N 050982	07-Identification	8-feb-98	THORWALD, RABELER, 06521@PF.PHILIPS.COM	SMART CARD WITH ENHANCED SECURITY	DE	15804784,3	15/80/1998					
N 050984	01-CMOS and embedded pr	5-feb-98	MARK, HALEY, 21248@PF.PHILIPS.COM	MULTIPLE POLE ELECTROSTATIC CHUCK WITH SELF HEALING ME US	US	09/019458	09/01/1998					

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N 016751	02-Packaging & Testing	FRANS DE JONG@PHILIPS.COM	ONE-TERMINAL BIT		982203338.5	US6232765
A 050949	04-Audio/Video	JAMES CUNNINGHAM.21174@PF.PHILIPS.COM	DATA STORAGE CIRCUIT USING SHARED BIT LINE AND METHOD T	EP	15520349348.41	US5972713
A 050952	04-Audio/Video	JAMES CUNNINGHAM.21174@PF.PHILIPS.COM	DATA STORAGE CIRCUIT USING SHARED BIT LINE AF	US		US5972713
N 016756	09-Discretics and Multimatks	ARNOLDUS VENE.15082@PF.PHILIPS.COM	CIRCUIT COMPRISING INTERCONNECT TEST UNIT	EP	15220409856.52	US5654258
N 017203	02-Packaging & Testing	FRANS DE JONG@PHILIPS.COM	CIRCUIT COMPRISING INTERCONNECT TEST UNIT	EP	15220409856.52	US5654258
A 050969	01-CMOS and embedded in	RADJANAPRAGADA.21109@PF.PHILIPS.COM	AUTOCLAVE WITH IMPROVED HEATING AND ACCESS	US	15220409856.52	US5661954
A 050981	02-Packaging & Testing	RYOUNG KIM.21352@PF.PHILIPS.COM	FLEXIBLE PIN COUNT PACKAGE FOR SEMICONDUCTOR DEVICE	US	15220409856.52	US5661954
N 016754	04-Audio/Video	KAVE, KIANUSH.03280@PF.PHILIPS.COM	GLOBAL TUNER	EP	15220409856.52	US5639001
A 051269	05-Mobile Communication	KM. JASPER@NETNET.NL	GREEN UNIFORMITY RESTORATION	EP	15220409856.52	US5639001
A 050972	09-Discretics and Multimatks	JAMES SHIFFER.21438@PF.PHILIPS.COM	COA FIVE VOLT TOLERANT I/O BUFFER	US	15220409856.52	US5639001
A 050904	03-RF Devices	SUBHAS BOTTHRA.21133@PF.PHILIPS.COM	MICRO-ELECTROMECHANICAL VOLTAGE SHIFTER	US	15220409856.52	US5639001
A 050901	06-Connectivity	DAVE EVOY@PHILIPS.COM	SYSTEM HAVING PROCESSOR MONITORING INFORMATION CAPABILITY OF AN IN LUS	US	15220409856.52	US5639001
A 050901	07-Identification	ADVAN DEN ENDE@PHILIPS.COM	SYSTEM AND METHOD FOR ACCESSING INFORMATION DECRYPTED	US	15220409856.52	US5639001
A 050982	05-Mobile Communication	RAY.GROWER@PHILIPS.COM	LOW COMPLEXITY, LOW BANDWIDTH BANDPASS FILTER	US	15220409856.52	US5639001
B 034225	02-Packaging & Testing	MARK BUER.21138@PF.PHILIPS.COM	HIGH-SPEED MODULAR DRIVER ASSEMBLY	EP	15220409856.52	US5639001
A 050796	06-Connectivity	HERMANN JAGS@PHILIPS.COM	DIPISTORTION BRIDGE DRIVER ASSEMBLY	US	15220409856.52	US5639001
D 098007	10-Chips	PETER CHAMBERS.21153@PF.PHILIPS.COM	CIRCUIT ARRANGEMENT FOR AN ADJUSTABLE HIGH-PASS FILTER	US	15220409856.52	US5639001
A 050889	04-Audio/Video	PETER CHAMBERS.21153@PF.PHILIPS.COM	CIRCUIT ARRANGEMENT FOR AN ADJUSTABLE HIGH-PASS FILTER	US	15220409856.52	US5639001
A 050893	20-Jan-98	JOHN CICCONE.21164@PF.PHILIPS.COM	CACHE BASED SCAN MATRIX KEYBOARD CONTROLLER	US	15220409856.52	US5639001
A 051001	05-Mobile Communication	JOHN CICCONE.21164@PF.PHILIPS.COM	CACHE BASED SCAN MATRIX KEYBOARD CONTROLLER	US	15220409856.52	US5639001
A 050893	15-Jan-98	JOHN CICCONE.21164@PF.PHILIPS.COM	POWER-ON-RESET CIRCUIT HAVING REDUCED SIZED CHARGING	US	15220409856.52	US5639001
A 050954	01-CMOS and embedded in	DAVID ZIGER@PHILIPS.COM	SEMICONDUCTOR PROCESSING METHODS AND STRUCTURES FO	US	15220409856.52	US5639001
A 050974	14-Jan-98	DAVID ZIGER@PHILIPS.COM	SEMICONDUCTOR PROCESSING METHODS AND STRUCTURES FO	US	15220409856.52	US5639001
A 050953	13-Jan-98	XI-WEI LIN.21310@PF.PHILIPS.COM	METHOD OF SELECTIVELY APPLYING DOPANTS TO AN INTEGRATE	US	15220409856.52	US5639001
A 050953	04-Audio/Video	REG ERHMANN@PHILIPS.COM	BINARY MEMORY DESIGN WITH DATA STORED IN LOW-POWER SE	US	15220409856.52	US5639001
A 050953	04-Audio/Video	VISHAL AMAND.21108@PF.PHILIPS.COM	BINARY MEMORY DESIGN WITH DATA STORED IN LOW-POWER SE	US	15220409856.52	US5639001
E 058001	04-Audio/Video	AXEL KATTNER@PHILIPS.COM	INTEGRATOR FILTER CIRCUIT WITH A REFERENCE FILTER	DE	15220409856.52	US5639001
A 050906	08-Connectivity	CARL KNUDSEN@PHILIPS.COM	A SMART RETRY SYSTEM THAT REDUCES WASTED BUS TRANSAC	US	15220409856.52	US5639001
D 098004	09-Automotive	PETER BUEHRING@PHILIPS.COM	DISCONNECTING UNIT	DE	15220409856.52	US5639001
A 050989	09-Discretics and Multimatks	TIM PONTIUS@PHILIPS.COM	BINARY COUNTER SYSTEM USING Q GREATER THAN OR EQUAL 1US	DE	15220409856.52	US5639001
A 050985	30-Dec-97	JOHN WEEMS@PHILIPS.COM	METHOD FOR DETECTING A LOCATION OF CONTAMINANT ENTRY	US	15220409856.52	US5639001
A 050947	29-Dec-97	PHILIPPE GAGLIONE.21223@PF.PHILIPS.COM	DATA COMMUNICATION DEVICES, PERSONAL HANDY-PHONE SYS	US	15220409856.52	US5639001
A 023337	24-Dec-97	PHILIPPE GAGLIONE.21223@PF.PHILIPS.COM	DATA COMMUNICATION DEVICES, PERSONAL HANDY-PHONE SYS	US	15220409856.52	US5639001
A 050982	23-Dec-97	KAMRAN MANTTEGI.21394@PF.PHILIPS.COM	THIN FILM SILICON-ON-INSULATOR HIGH-VOLTAGE STRUCTURE	US	15220409856.52	US5639001
A 001322	23-Dec-97	KAMRAN MANTTEGI.21394@PF.PHILIPS.COM	THIN FILM SILICON-ON-INSULATOR HIGH-VOLTAGE STRUCTURE	US	15220409856.52	US5639001
A 023335	23-Dec-97	FAROOD KAMGAR.18438@PF.PHILIPS.COM	OXIDE WIRE BOND INSULATION IN SEMICONDUCTOR ASSEMBLIES	US	15220409856.52	US5639001
N 016980	22-Dec-97	FAROOD KAMGAR.18438@PF.PHILIPS.COM	OXIDE WIRE BOND INSULATION IN SEMICONDUCTOR ASSEMBLIES	US	15220409856.52	US5639001
A 050927	22-Dec-97	STEFAN LABRAN@PHILIPS.COM	SELF CALIBRATING CURRENT SLEW RATE CONTROL FOR CMOS E	US	15220409856.52	US5639001
A 050954	22-Dec-97	SUBHAS BOTTHRA.21133@PF.PHILIPS.COM	SEMICONDUCTOR PRESSURE TRANSDUCER STRUCTURES AND N	US	15220409856.52	US5639001
A 050993	22-Dec-97	SUBHAS BOTTHRA.21133@PF.PHILIPS.COM	SEMICONDUCTOR PRESSURE TRANSDUCER STRUCTURES AND N	US	15220409856.52	US5639001
A 001319	22-Dec-97	SUBHAS BOTTHRA.21133@PF.PHILIPS.COM	SEMICONDUCTOR PRESSURE TRANSDUCER STRUCTURES AND N	US	15220409856.52	US5639001
A 050936	22-Dec-97	SLOBODAN SIMOVICH.17227@PF.PHILIPS.COM	PHILIP EXTRA REGISTER MINIMIZES CPU IDLE CYCLES DURING CACHE	US	15220409856.52	US5639001
A 001318	22-Dec-97	SLOBODAN SIMOVICH.17227@PF.PHILIPS.COM	PHILIP EXTRA REGISTER MINIMIZES CPU IDLE CYCLES DURING CACHE	US	15220409856.52	US5639001
D 097148	22-Dec-97	DANIEL LINESBERGER.05903@PF.PHILIPS.COM	PHASE LOCKED LOOP LOCK CONDITION DETECTOR	US	15220409856.52	US5639001
D 097163	22-Dec-97	THOMAS TRODGEN.15903@PF.PHILIPS.COM	PHASE LOCKED LOOP LOCK CONDITION DETECTOR	US	15220409856.52	US5639001
N 017170	18-Dec-97	HANS LUCHMANN.18747@PF.PHILIPS.COM	SENSOR DEVICE HAVING ERROR DETECTION MEANS	DE	15220409856.52	US5639001
A 050883	18-Dec-97	WILFRID BIRTH@PHILIPS.COM	PA-LOAD SWITCHING FOR MOBILE RADIO TRANSMITTER	DE	15220409856.52	US5639001
A 001317	17-Dec-97	PHILIPPE GAGLIONE.21223@PF.PHILIPS.COM	VORRUCHTUNG ZUM HIERARCHISCHEN VERBINDEIN EINER MEHRZ	DE	15220409856.52	US5639001
F 097628	15-Dec-97	ALMA ANDERSON@PHILIPS.COM	LOW POWER CLOCK SQUARER WITH TIGHT DUTY CYCLE CONTR	US	15220409856.52	US5639001
050911	12-Dec-97	DIERE CHARRON.16983@PF.PHILIPS.COM	EEPROM PROM CLOMING PROTECTION	FR	15220409856.52	US5639001
001318	12-Dec-97	TIM LEHLER.21202@PF.PHILIPS.COM	CIRCUIT SLOPE TIMING ANALYSIS AND NON-LINEAR DELAY TABLE	US	15220409856.52	US5639001
001318	12-Dec-97	OLAF HIRSCH@PHILIPS.COM	CIRCUIT SLOPE TIMING ANALYSIS AND NON-LINEAR DELAY TABLE	US	15220409856.52	US5639001
001318	12-Dec-97	HENKJAN BERGVELD@PHILIPS.COM	RF-POWER CONTROLS IN COMMUNICATION RECEIVER	EP	15220409856.52	US5639001
001318	12-Dec-97	SRDJAN KORIC@PHILIPS.COM	RF-POWER CONTROLS IN COMMUNICATION RECEIVER	EP	15220409856.52	US5639001
001318	12-Dec-97	ALANNA EMILYUENTE.NL	MULTI-STAGE VOLTAGE REFERENCE	EP	15220409856.52	US5639001
001318	12-Dec-97	ALANNA EMILYUENTE.NL	MULTI-STAGE VOLTAGE REFERENCE	EP	15220409856.52	US5639001
001318	12-Dec-97	AXEL KATTNER@PHILIPS.COM	VIA ALIGNMENT, ETCH COMPLETION AND CRITICAL DIMENSION	US	15220409856.52	US5639001
001318	12-Dec-97	ARJEN KUIJER.17640@PF.PHILIPS.COM	METHOD AND DEVICE FOR PASTING SOA DEVICES	DE	15220409856.52	US5639001
001318	12-Dec-97	ERIC BERNASCONI@PHILIPS.COM	METHOD AND DEVICE FOR PASTING SOA DEVICES	DE	15220409856.52	US5639001
001318	12-Dec-97	PETER ZAWILSKI.21489@PF.PHILIPS.COM	AN INTEGRATED CIRCUIT INCLUDING PATCHING CIRCUITRY TO	US	15220409856.52	US5639001
001318	12-Dec-97	JAYARAMA SHENOY.21434@PF.PHILIPS.COM	METHOD AND APPARATUS FOR EQUILIBRIUM SIGNAL PARAMETERS	US	15220409856.52	US5639001
001318	12-Dec-97	LOUIS PRAAMSA@PHILIPS.COM	MOSFET WITH SHORT METALLIZED GATE	EP	15220409856.52	US5639001
001318	12-Dec-97	PHILIPPE GAGLIONE.21223@PF.PHILIPS.COM	CONFERENCE CALL ON A PERSONAL HANDY PHONE SYSTEM	US	15220409856.52	US5639001
001318	12-Dec-97	JOHN CICCONE.21164@PF.PHILIPS.COM	HIGH-SPEED LOGIC EMBODIED DIFFERENTIAL DYNAMIC CMOS	US	15220409856.52	US5639001
001318	12-Dec-97	DANIEL BAKER.21117@PF.PHILIPS.COM	METHOD FOR ALIGNMENT USING MULTIPLE WAVELENGTHS OF	US	15220409856.52	US5639001
001318	12-Dec-97	DANIEL BAKER.21117@PF.PHILIPS.COM	METHOD FOR ALIGNMENT USING MULTIPLE WAVELENGTHS OF	US	15220409856.52	US5639001
001318	12-Dec-97	DANIEL BAKER.21117@PF.PHILIPS.COM	METHOD AND APPARATUS FOR DETECTING MISALIGNMENTS IN	US	15220409856.52	US5639001
001318	12-Dec-97	DANIEL BAKER.21117@PF.PHILIPS.COM	METHOD AND APPARATUS FOR DETECTING MISALIGNMENTS IN	US	15220409856.52	US5639001

IPTLA Annex 1 Transfer Filings - PIRAL\_020806.xls

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Patent No.	Inventor	Title	Pub No.	Date
A 040787	MORGAN, DENISE V.	04-Audio/Video	US040787A1	06/09/97
A 050793	MORGAN, DENISE V.	04-Audio/Video	US050793A1	06/09/97
D 097114	GORDON, CICHON	04-Audio/Video	US097114D1	18/11/97
D 097115	GORDON, CICHON	04-Audio/Video	US097115D1	18/11/97
A 052853	KAMRAN, IRAVANI	05-Mobile Communication	US052853A1	18/11/97
A 052855	KAMRAN, IRAVANI	05-Mobile Communication	US052855A1	18/11/97
A 052190	KAMRAN, IRAVANI	05-Mobile Communication	US052190A1	14/11/97
A 001131A	CHRIS, WALSH	10-Other	US001131A1	13/11/97
A 050859	VISHAL, ANAND	04-Audio/Video	US050859A1	13/11/97
A 050866	RICHARD, BURBRIDGE	05-Mobile Communication	US050866A1	12/11/97
B 034208	MICHEL, MAIETTA	02-Other	US034208B1	12/11/97
A 023305	CHRIS, WALSH	10-Other	US023305A1	12/11/97
A 023306	CHRIS, WALSH	10-Other	US023306A1	12/11/97
A 023301	MICHAEL, ANG	05-Mobile Communication	US023301A1	10/11/97
N 016815	FABRIZIO, BARBERI	05-Mobile Communication	US016815N1	10/11/97
A 023299	ALEX, HERRERA	04-Audio/Video	US023299A1	4/11/97
A 050869	ROBERT, PAYNE	01-CMOS and embedded pr	US050869A1	01/08/97
A 050870	ROBERT, PAYNE	01-CMOS and embedded pr	US050870A1	31/08/97
N 016610	HANS, DINGEMANSE	02-Packaging & Testing	US016610N1	31/08/97
A 023296	EINO, JACOBS	04-Audio/Video	US023296A1	31/08/97
A 050958	ROBERT, PAYNE	04-Audio/Video	US050958A1	31/08/97
N 016595	ROBERT, PAYNE	04-Audio/Video	US016595N1	31/08/97
A 050875	JOHN, GICCONE	05-Mobile Communication	US050875A1	31/08/97
N 016679	EVERETT, BOSMA	05-Mobile Communication	US016679N1	30/08/97
A 050829	KARL, MAURITZ	05-Mobile Communication	US050829A1	29/08/97
A 050861	PETER, CHAMBERS	05-Mobile Communication	US050861A1	28/08/97
N 016582	DAVID, SPANIO	05-Mobile Communication	US016582N1	28/08/97
A 050866	DEV, ALOK	05-Mobile Communication	US050866A1	28/08/97
A 050867	DEV, ALOK	05-Mobile Communication	US050867A1	28/08/97
C 097114	PAUL, LEVY	02-Packaging & Testing	US097114C1	25/08/97
A 050798	PAUL, LEVY	05-Mobile Communication	US050798A1	25/08/97
B 034206	HAROLD, N. BAUER	07-Identification	US034206B1	24/08/97
C 097128	JOHN, GICCONE	07-Identification	US097128C1	22/08/97
A 036871	DOMINIK, BERGER	07-Identification	US036871A1	21/08/97
N 016584	PIERRE, LEROUX	04-Audio/Video	US016584N1	21/08/97
A 050887	ADMIN, TIMMER	04-Audio/Video	US050887A1	21/08/97
B 034205	KAMRAN, IRAVANI	05-Mobile Communication	US034205B1	17/08/97
016555	RICHARD, BARKER	05-Mobile Communication	US016555A1	16/08/97
050862	PIERRE, LEROUX	06-Connectivity	US050862A1	14/08/97
097801	CARL, KNUDSEN	07-Identification	US097801A1	9/08/97
050822	FRANCOIS, LAUNAY	07-Identification	US050822A1	8/08/97
050820	IAN, HARVEY	01-CMOS and embedded pr	US050820A1	6/08/97
097996	IAN, HARVEY	01-CMOS and embedded pr	US097996A1	1/08/97
097997	IAN, HARVEY	01-CMOS and embedded pr	US097997A1	30/07/97
097998	IAN, HARVEY	01-CMOS and embedded pr	US097998A1	30/07/97
097999	IAN, HARVEY	01-CMOS and embedded pr	US097999A1	30/07/97
098000	IAN, HARVEY	01-CMOS and embedded pr	US098000A1	30/07/97
098001	IAN, HARVEY	01-CMOS and embedded pr	US098001A1	30/07/97
098002	IAN, HARVEY	01-CMOS and embedded pr	US098002A1	30/07/97
098003	IAN, HARVEY	01-CMOS and embedded pr	US098003A1	30/07/97
098004	IAN, HARVEY	01-CMOS and embedded pr	US098004A1	30/07/97
098005	IAN, HARVEY	01-CMOS and embedded pr	US098005A1	30/07/97
098006	IAN, HARVEY	01-CMOS and embedded pr	US098006A1	30/07/97
098007	IAN, HARVEY	01-CMOS and embedded pr	US098007A1	30/07/97
098008	IAN, HARVEY	01-CMOS and embedded pr	US098008A1	30/07/97
098009	IAN, HARVEY	01-CMOS and embedded pr	US098009A1	30/07/97
098010	IAN, HARVEY	01-CMOS and embedded pr	US098010A1	30/07/97
098011	IAN, HARVEY	01-CMOS and embedded pr	US098011A1	30/07/97
098012	IAN, HARVEY	01-CMOS and embedded pr	US098012A1	30/07/97
098013	IAN, HARVEY	01-CMOS and embedded pr	US098013A1	30/07/97
098014	IAN, HARVEY	01-CMOS and embedded pr	US098014A1	30/07/97
098015	IAN, HARVEY	01-CMOS and embedded pr	US098015A1	30/07/97
098016	IAN, HARVEY	01-CMOS and embedded pr	US098016A1	30/07/97
098017	IAN, HARVEY	01-CMOS and embedded pr	US098017A1	30/07/97
098018	IAN, HARVEY	01-CMOS and embedded pr	US098018A1	30/07/97
098019	IAN, HARVEY	01-CMOS and embedded pr	US098019A1	30/07/97
098020	IAN, HARVEY	01-CMOS and embedded pr	US098020A1	30/07/97
098021	IAN, HARVEY	01-CMOS and embedded pr	US098021A1	30/07/97
098022	IAN, HARVEY	01-CMOS and embedded pr	US098022A1	30/07/97
098023	IAN, HARVEY	01-CMOS and embedded pr	US098023A1	30/07/97
098024	IAN, HARVEY	01-CMOS and embedded pr	US098024A1	30/07/97
098025	IAN, HARVEY	01-CMOS and embedded pr	US098025A1	30/07/97
098026	IAN, HARVEY	01-CMOS and embedded pr	US098026A1	30/07/97
098027	IAN, HARVEY	01-CMOS and embedded pr	US098027A1	30/07/97
098028	IAN, HARVEY	01-CMOS and embedded pr	US098028A1	30/07/97
098029	IAN, HARVEY	01-CMOS and embedded pr	US098029A1	30/07/97
098030	IAN, HARVEY	01-CMOS and embedded pr	US098030A1	30/07/97
098031	IAN, HARVEY	01-CMOS and embedded pr	US098031A1	30/07/97
098032	IAN, HARVEY	01-CMOS and embedded pr	US098032A1	30/07/97
098033	IAN, HARVEY	01-CMOS and embedded pr	US098033A1	30/07/97
098034	IAN, HARVEY	01-CMOS and embedded pr	US098034A1	30/07/97
098035	IAN, HARVEY	01-CMOS and embedded pr	US098035A1	30/07/97
098036	IAN, HARVEY	01-CMOS and embedded pr	US098036A1	30/07/97
098037	IAN, HARVEY	01-CMOS and embedded pr	US098037A1	30/07/97
098038	IAN, HARVEY	01-CMOS and embedded pr	US098038A1	30/07/97
098039	IAN, HARVEY	01-CMOS and embedded pr	US098039A1	30/07/97
098040	IAN, HARVEY	01-CMOS and embedded pr	US098040A1	30/07/97
098041	IAN, HARVEY	01-CMOS and embedded pr	US098041A1	30/07/97
098042	IAN, HARVEY	01-CMOS and embedded pr	US098042A1	30/07/97
098043	IAN, HARVEY	01-CMOS and embedded pr	US098043A1	30/07/97
098044	IAN, HARVEY	01-CMOS and embedded pr	US098044A1	30/07/97
098045	IAN, HARVEY	01-CMOS and embedded pr	US098045A1	30/07/97
098046	IAN, HARVEY	01-CMOS and embedded pr	US098046A1	30/07/97
098047	IAN, HARVEY	01-CMOS and embedded pr	US098047A1	30/07/97
098048	IAN, HARVEY	01-CMOS and embedded pr	US098048A1	30/07/97
098049	IAN, HARVEY	01-CMOS and embedded pr	US098049A1	30/07/97
098050	IAN, HARVEY	01-CMOS and embedded pr	US098050A1	30/07/97
098051	IAN, HARVEY	01-CMOS and embedded pr	US098051A1	30/07/97
098052	IAN, HARVEY	01-CMOS and embedded pr	US098052A1	30/07/97
098053	IAN, HARVEY	01-CMOS and embedded pr	US098053A1	30/07/97
098054	IAN, HARVEY	01-CMOS and embedded pr	US098054A1	30/07/97
098055	IAN, HARVEY	01-CMOS and embedded pr	US098055A1	30/07/97
098056	IAN, HARVEY	01-CMOS and embedded pr	US098056A1	30/07/97
098057	IAN, HARVEY	01-CMOS and embedded pr	US098057A1	30/07/97
098058	IAN, HARVEY	01-CMOS and embedded pr	US098058A1	30/07/97
098059	IAN, HARVEY	01-CMOS and embedded pr	US098059A1	30/07/97
098060	IAN, HARVEY	01-CMOS and embedded pr	US098060A1	30/07/97
098061	IAN, HARVEY	01-CMOS and embedded pr	US098061A1	30/07/97
098062	IAN, HARVEY	01-CMOS and embedded pr	US098062A1	30/07/97
098063	IAN, HARVEY	01-CMOS and embedded pr	US098063A1	30/07/97
098064	IAN, HARVEY	01-CMOS and embedded pr	US098064A1	30/07/97
098065	IAN, HARVEY	01-CMOS and embedded pr	US098065A1	30/07/97
098066	IAN, HARVEY	01-CMOS and embedded pr	US098066A1	30/07/97
098067	IAN, HARVEY	01-CMOS and embedded pr	US098067A1	30/07/97
098068	IAN, HARVEY	01-CMOS and embedded pr	US098068A1	30/07/97
098069	IAN, HARVEY	01-CMOS and embedded pr	US098069A1	30/07/97
098070	IAN, HARVEY	01-CMOS and embedded pr	US098070A1	30/07/97
098071	IAN, HARVEY	01-CMOS and embedded pr	US098071A1	30/07/97
098072	IAN, HARVEY	01-CMOS and embedded pr	US098072A1	30/07/97
098073	IAN, HARVEY	01-CMOS and embedded pr	US098073A1	30/07/97
098074	IAN, HARVEY	01-CMOS and embedded pr	US098074A1	30/07/97
098075	IAN, HARVEY	01-CMOS and embedded pr	US098075A1	30/07/97
098076	IAN, HARVEY	01-CMOS and embedded pr	US098076A1	30/07/97
098077	IAN, HARVEY	01-CMOS and embedded pr	US098077A1	30/07/97
098078	IAN, HARVEY	01-CMOS and embedded pr	US098078A1	30/07/97
098079	IAN, HARVEY	01-CMOS and embedded pr	US098079A1	30/07/97
098080	IAN, HARVEY	01-CMOS and embedded pr	US098080A1	30/07/97
098081	IAN, HARVEY	01-CMOS and embedded pr	US098081A1	30/07/97
098082	IAN, HARVEY	01-CMOS and embedded pr	US098082A1	30/07/97
098083	IAN, HARVEY	01-CMOS and embedded pr	US098083A1	30/07/97
098084	IAN, HARVEY	01-CMOS and embedded pr	US098084A1	30/07/97
098085	IAN, HARVEY	01-CMOS and embedded pr	US098085A1	30/07/97
098086	IAN, HARVEY	01-CMOS and embedded pr	US098086A1	30/07/97
098087	IAN, HARVEY	01-CMOS and embedded pr	US098087A1	30/07/97
098088	IAN, HARVEY	01-CMOS and embedded pr	US098088A1	30/07/97
098089	IAN, HARVEY	01-CMOS and embedded pr	US098089A1	30/07/97
098090	IAN, HARVEY	01-CMOS and embedded pr	US098090A1	30/07/97
098091	IAN, HARVEY	01-CMOS and embedded pr	US098091A1	30/07/97
098092	IAN, HARVEY	01-CMOS and embedded pr	US098092A1	30/07/97
098093	IAN, HARVEY	01-CMOS and embedded pr	US098093A1	30/07/97
098094	IAN, HARVEY	01-CMOS and embedded pr	US098094A1	30/07/97
098095	IAN, HARVEY	01-CMOS and embedded pr	US098095A1	30/07/97
098096	IAN, HARVEY	01-CMOS and embedded pr	US098096A1	30/07/97
098097	IAN, HARVEY	01-CMOS and embedded pr	US098097A1	30/07/97
098098	IAN, HARVEY	01-CMOS and embedded pr	US098098A1	30/07/97
098099	IAN, HARVEY	01-CMOS and embedded pr	US098099A1	30/07/97
098100	IAN, HARVEY	01-CMOS and embedded pr	US098100A1	30/07/97



Pub No.	Class	Inventor	Title	Pub No.	Class	Inventor	Title
A 050689	01-CMOS and embedded pr	XI-WEILIN, 2131@PF.PHILIPS.COM	SELF-ALIGNED SILICIDATION STRUCTURE AND METHOD OF FORMUS	US 6933739	08/927,479		
A 050791	01-CMOS and embedded pr	SUBHAS, BOTHRA, 21133@PF.PHILIPS.COM	PHOTO ALIGNMENT STRUCTURE	US 6825040	08/92540		
A 050848	01-CMOS and embedded pr	DANIEL BAKER, 21117@PF.PHILIPS.COM	METHOD AND APPARATUS FOR IMPROVING ALIGNMENT FOR MET U.S.	US 6923106	08/923106		
N 016508	01-CMOS and embedded pr	ADAM BROWN@PHILIPS.COM	FABRICATION METHOD FOR SCROTTY DIODES	EP 97202717.A			
A 050732	01-CMOS and embedded pr	JEAR, FERNANDEZ, 21213@PF.PHILIPS.COM	CIRCUIT DESIGN FOR AUTHENTICITY CHECK OF CIRCUIT	US 6921361	08/921361		
D 097116	09-Discreties and Multimar	ERIC, ZWAN, 08289@PF.PHILIPS.COM	SIGMA DELTA AD CONVERTER WITH IMPROVED GAIN ACCURACY	EP 97202667.9			
N 016498	01-CMOS and embedded pr	KOUROU, CHANDERHARI, 21230@PF.PHILIPS.COM	PHILI SEMICONDUCTOR ANTIREFLECTIVE COATING	US 6851648	08/51648		
A 050980	04-Audio/Video	M. VLOT@PHILIPS.COM	MULTI-STACK PROCESSOR	US 69519911	08/519911		
F 097589	09-Discreties and Multimar	ERIC, DESBONNETS@PHILIPS.COM	INTEGRATED PLL-DEMODULATOR FOR FM APPLICATIONS	EP 97202638.8			
D 097113	05-Mobile Communication	MICHAEL PIETRZYK@PHILIPS.COM	RINGOSCILLATOR	FR 9710687			
A 050824	05-Mobile Communication	DAVID SPANGL, 21447@PF.PHILIPS.COM	LOGIC IMPLEMENTATION OF CONTROL SIGNALS FOR ON-SILICON U.S.	US 6936857.3			
C 097522	12-aug-97	STEFAN OTT, 10238@PF.PHILIPS.COM	METHOD AND SYSTEM FOR USING DATA DECOMPRESSION ON (US	US 6918596	08/918596		
N 016493	09-Discreties and Multimar	DOMINIK BERGER, 16638@PF.PHILIPS.COM	CHIP CARD WITH POWER MANAGEMENT FOR REMOTE ACCESS	US 6918543	08/918543		
B 034174	09-Discreties and Multimar	JOHAN VAN DEN HOMBURG@PHILIPS.COM	POWER SUPPLY WITH SYNCHRONIZED RECTIFIER	EP 97890161.9			
D 097106	03-RF Devices	RAUF WENDEL, 16291@PF.PHILIPS.COM	POWER SUPPLY WITH SYNCHRONIZED RECTIFIER	EP 97202483.6			
D 097105	03-RF Devices	RAUF WENDEL, 16291@PF.PHILIPS.COM	POWER SUPPLY WITH SYNCHRONIZED RECTIFIER	EP 97168539.9			
N 016480	09-Discreties and Multimar	ENGEL, ROZA@PHILIPS.COM	MICROWAVE ELEMENT	US 6934087.1			
A 050755	01-CMOS and embedded pr	CALVIN GABRIEL, 20438@PF.PHILIPS.COM	ADV. CONV. GENERATING ADDRESS CODES	US 6929291	08/929291		
A 030772	05-Mobile Communication	PHILIPPE GAGLIONE, 21223@PF.PHILIPS.COM	SHALLOW TRENCH OXIDE FOR REDUCED REI US	US 692447.5			
A 030773	05-Mobile Communication	PHILIPPE GAGLIONE, 21223@PF.PHILIPS.COM	METHOD OF SYNCHRONIZATION A PERSONAL HANDPHONE SYS US	US 69308331			
C 097521	05-Mobile Communication	FRICH, KLEIN@PHILIPS.COM	METHODS OF ANALYZING A RADIO SIGNAL AND METHODS OF AN US	US 69308331			
N 016687	05-Mobile Communication	ERICH, KLEIN@PHILIPS.COM	LOUDSPEAKER WITH DIRECTIONAL EFFECT TO BE BUILT-IN IN SE EP	US 6930155.1			
D 097096	02-aug-97	HELMUT SALBAUM, 12858@PF.PHILIPS.COM	POWER SUPPLY WITH SYNCHRONIZED RECTIFIER	EP 97204072.9			
D 097104	05-Mobile Communication	HELMUT SALBAUM, 12858@PF.PHILIPS.COM	POWER SUPPLY WITH SYNCHRONIZED RECTIFIER	EP 9735327.6			
A 050741	02-Packaging & Testing	HERALD N. BAUER@PHILIPS.COM	COMMUNICATION SYSTEM WITH DMA-UNIT	US 6935306.6			
A 023264	05-Mobile Communication	PETER CHAMBERS, 21153@PF.PHILIPS.COM	REGISTER-BASED PROGRAMMABLE POST-SILICON SYSTEM TO PR US	US 691804818			
A 023265	05-Mobile Communication	NAVIED, MAJID, 13957@PF.PHILIPS.COM	LOW POWER STAND-BY FOR SWITCHED-MODE POWER SUPPLY C US	US 69303779			
A 030774	05-Mobile Communication	PHILIPPE GAGLIONE, 21223@PF.PHILIPS.COM	FUNCTIONAL ONOFF SWITCH FOR SWITCHED-MODE POWER SUP US	US 69303774			
A 030775	05-Mobile Communication	PHILIPPE GAGLIONE, 21223@PF.PHILIPS.COM	HARDWARE PCH CHECKING FOR PERSONAL HANDPHONE SYST US	US 69302302			
A 050776	05-Mobile Communication	PHILIPPE GAGLIONE, 21223@PF.PHILIPS.COM	PERSONAL HANDPHONE SYSTEM HARDWARE CHECKING OF SA US	US 69302189			
A 030816	06-Connectivity	PETER CHAMBERS, 21153@PF.PHILIPS.COM	PERSONAL HANDPHONE SYSTEM HARDWARE CHECKING OF BR US	US 69302091			
D 097083	01-CMOS and embedded pr	HANSLOCHMANN, 16747@PF.PHILIPS.COM	METHOD AND SYSTEM FOR ACCURATE TEMPORAL DETERMINATI US	US 6917465			
A 030876	01-CMOS and embedded pr	SUBHAS, BOTHRA, 21133@PF.PHILIPS.COM	SELECTABLE INTERCONNECT VIA STRUCTURES AND METHODS FOR US	US 69400601			
A 030877	02-Packaging & Testing	KOICHI NOMURA, 21371@PF.PHILIPS.COM	TEST DATA INPUT BUS TO SUPPLY TEST DATA TO U.S	US 6910590			
N 016444	04-Audio/Video	AD SEMPEL@PHILIPS.COM	GLOBAL ASYMMETRY COMPENSATION AFTER DEMODULATION EP	EP 97202586.3			
D 097091	02-aug-97	RUDOLF PLASSCHKE, 02295@PF.PHILIPS.COM	GLOBAL ASYMMETRY COMPENSATION AFTER DEMODULATION EP	EP 97202586.3			
N 016453	04-Audio/Video	ECKART RZITTKA@PHILIPS.COM	TWO-CELL ZEROFORM FUSE PROGRAMMED OPPOSITE	US 6927632			
N 016457	04-Audio/Video	BERT DE KONING@PHILIPS.COM	FREQUENCY TRIMMING IN HEARING INSTRUMENTS	US 6927632			
N 016458	04-Audio/Video	HENK DERKS@PHILIPS.COM	STEP DOWN CONVERTER WITH CONSTANT INPUT IMPEDANCE FC EP	EP 97401699			
D 097094	05-Mobile Communication	KIM JASPERS@HETNET.NL	PARALLEL COLOR/CONTROL SIGNAL GENERATION	EP 97401701.6			
N 016466	05-Mobile Communication	KIM JASPERS@HETNET.NL	SMART GREEN COLOR SAMPLE INTERPOLATION	EP 97202164.8			
050802	05-Mobile Communication	STEFAN OTT, 10238@PF.PHILIPS.COM	ORGANIC ELASTIC BUFFER TO INTERFACE DIGITAL SYSTEMS	US 69692302			
A 050838	06-Connectivity	STEFAN OTT, 10238@PF.PHILIPS.COM	SAMPLE RATE FREQUENCY ADJUSTMENT FOR SAMPLI US	US 69692302			
097575	04-Audio/Video	BRUNO PUTZEYS@PHILIPS.COM	INTERFERENCE SUPPRESSION IN CLASS-D AMPLIFIERS	FR 97202140.2			
097575	04-Audio/Video	GILLES CHEVALLIER, 06224@PF.PHILIPS.COM	SAMPLE & HOLD BASED ON TWO TIME CONSTANTS	FR 97202140.2			
097575	04-Audio/Video	WALT HUBERD, 21267@PF.PHILIPS.COM	SYSTEM FOR MONITORING OPTICAL PROPERTIES OF PHOTOLITH U.S	US 69391402			
097575	04-Audio/Video	WIEBE DE BOER@PHILIPS.COM	MEMORY DEVICE WITH H.S. CAPACITOR ELECTRODE	EP 97401830.5			
097575	04-Audio/Video	STEVE LABRUM@PHILIPS.COM	CMOS I/O CURRENT SLEW-RATE CONTROL AND TRAILING EDGES EP	EP 97401830.5			
097575	04-Audio/Video	LEO RUIJTENBURG, 2@PHILIPS.COM	TV-TUNER WITH SWITCHABLE OUTPUTS	DE 19726315.2			
097575	04-Audio/Video	KIM BECH-ANDERSEN, 17794@PF.PHILIPS.COM	FM-MODULATION FOR PLL	FR 9709289			
097575	04-Audio/Video	XI-WEILIN, 21318@PF.PHILIPS.COM	SELF-ALIGNED SILICIDATION TECHNIQUE TO INDEPENDENTLY FOI US	US 69885382			
097575	04-Audio/Video	XI-WEILIN, 21318@PF.PHILIPS.COM	SELF-ALIGNED ETCH-STOP LAYER FORMATION FOR SEMICONDUCTI US	US 69885741			
097575	04-Audio/Video	XI-WEILIN, 21318@PF.PHILIPS.COM	SELF-ALIGNED PROCESSING OF SEMICONDUCTOR DEVICE FEAT U.S	US 69881770			
097575	04-Audio/Video	XI-WEILIN, 21318@PF.PHILIPS.COM	SELECTIVE EXCLUSION OF SILICIDE FORMATION TO MAKE POLYS US	US 69885378			
097575	04-Audio/Video	XI-WEILIN, 21318@PF.PHILIPS.COM	METALLIZATION TECHNIQUE FOR GATE ELECTRODES AND LOCAL US	US 69885740			
097575	04-Audio/Video	JAMES SHIFFER, 21435@PF.PHILIPS.COM	DIGITAL INTEGRATED CIRCUIT BUFFER DIGITAL DEVICE AND METI US	US 69880052			
097575	04-Audio/Video	CHRISTOPHE LORIEAU, 17588@PF.PHILIPS.COM	IMPROVED STAND-BY DURATION FOR DECT HANDSETS	FR 9708034			
097575	04-Audio/Video	JOHN LAAK, 17424@PF.PHILIPS.COM	LOW-DISTO HF AMPLIFIER WITH CD CAPACITANCE	EP 97201857.4			
097575	04-Audio/Video	SUBHAS, BOTHRA, 21133@PF.PHILIPS.COM	HOLE STRUCTURE INCLUDING CVD TUNGSTEN SILICIDE BARR US	US 69681614			
097575	04-Audio/Video	PETER BALTUS@PHILIPS.COM	DIFFERENTIAL AMPLIFIER HAVING SATURATION SENSING CIRCUIT EP	EP 97201844.2			
097575	04-Audio/Video	MAN HARVEY, 21255@PF.PHILIPS.COM	OXIDE ETCH STOP TECHNIQUES FOR UNIFORM DAMASCENE TREI US	US 69880590			

Pub No	App No	Inventor	IPC Class	Pub No	App No	Inventor	IPC Class
B 034166	04-Audio/Video	TREVOR HALL@PHILIPS.COM	01-CMOS and embedded in	US 6252320	US 6252320	US 6252320	US 6252320
N 016240	05-Mobile Communication	ANDRE VAN BEZOUJEN@PHILIPS.COM	01-CMOS and embedded in	US 6252321	US 6252321	US 6252321	US 6252321
A 050613	01-CMOS and embedded in	DANNY ECHTLE 21197@PHILIPS.COM	01-CMOS and embedded in	US 6252322	US 6252322	US 6252322	US 6252322
D 050707	01-CMOS and embedded in	WILFANG VOLLE 17678@PHILIPS.COM	01-CMOS and embedded in	US 6252323	US 6252323	US 6252323	US 6252323
N 016415	02-Packaging & Testing	KAMRAN MANTEHIZI 21334@PHILIPS.COM	02-Packaging & Testing	US 6252324	US 6252324	US 6252324	US 6252324
D 090706	05-Mobile Communication	FRANK THUS@PHILIPS.COM	05-Mobile Communication	US 6252325	US 6252325	US 6252325	US 6252325
N 016401	08-Discretes and Multichip	BURKHARD DICK@PHILIPS.COM	08-Discretes and Multichip	US 6252326	US 6252326	US 6252326	US 6252326
A 050838	09-Discretes and Multichip	MARC VAN DELDEN@PHILIPS.COM	09-Discretes and Multichip	US 6252327	US 6252327	US 6252327	US 6252327
Q 097022	01-CMOS and embedded in	PETER CHAMBERS 21153@PHILIPS.COM	01-CMOS and embedded in	US 6252328	US 6252328	US 6252328	US 6252328
Q 097023	08-Automotive	REINHARD SCHMID 19033@PHILIPS.COM	08-Automotive	US 6252329	US 6252329	US 6252329	US 6252329
N 016350	04-Audio/Video	ROLF GUTSMANN 17302@PHILIPS.COM	04-Audio/Video	US 6252330	US 6252330	US 6252330	US 6252330
A 050764	01-CMOS and embedded in	FRANCOIS NICOT 20895@PHILIPS.COM	01-CMOS and embedded in	US 6252331	US 6252331	US 6252331	US 6252331
A 050759	07-Identification	JOHN CHICCO 21184@PHILIPS.COM	07-Identification	US 6252332	US 6252332	US 6252332	US 6252332
N 016382	06-Connectivity	WEL-JIN TANG 18803@PHILIPS.COM	06-Connectivity	US 6252333	US 6252333	US 6252333	US 6252333
N 016378	02-Packaging & Testing	JACK BAKKER 00068@PHILIPS.COM	02-Packaging & Testing	US 6252334	US 6252334	US 6252334	US 6252334
B 034158	01-CMOS and embedded in	HERMANN PEEK 02231@PHILIPS.COM	01-CMOS and embedded in	US 6252335	US 6252335	US 6252335	US 6252335
N 016348	04-Audio/Video	ELISA AUROUX 17216@PHILIPS.COM	04-Audio/Video	US 6252336	US 6252336	US 6252336	US 6252336
F 097654	05-Mobile Communication	EDWIN DILLING@PHILIPS.COM	05-Mobile Communication	US 6252337	US 6252337	US 6252337	US 6252337
N 016357	09-Discretes and Multichip	RIK JOS@PHILIPS.COM	09-Discretes and Multichip	US 6252338	US 6252338	US 6252338	US 6252338
A 050798	01-CMOS and embedded in	MILIND WELING 21487@PHILIPS.COM	01-CMOS and embedded in	US 6252339	US 6252339	US 6252339	US 6252339
A 050732	04-Audio/Video	LAWRENCE LETHAM 21310@PHILIPS.COM	04-Audio/Video	US 6252340	US 6252340	US 6252340	US 6252340
N 016358	08-Discretes and Multichip	ADAM BROWN@PHILIPS.COM	08-Discretes and Multichip	US 6252341	US 6252341	US 6252341	US 6252341
Q 097516	02-Packaging & Testing	DOMINIK BERGER 16638@PHILIPS.COM	02-Packaging & Testing	US 6252342	US 6252342	US 6252342	US 6252342
A 050811	01-CMOS and embedded in	SUBHAS BOTHERA 21133@PHILIPS.COM	01-CMOS and embedded in	US 6252343	US 6252343	US 6252343	US 6252343
N 016353	02-Packaging & Testing	TACO ZWIEMSTRA 13801@PHILIPS.COM	02-Packaging & Testing	US 6252344	US 6252344	US 6252344	US 6252344
N 016359	07-Identification	MARK BUER 21138@PHILIPS.COM	07-Identification	US 6252345	US 6252345	US 6252345	US 6252345
A 050787	01-CMOS and embedded in	MARK BUER 21138@PHILIPS.COM	01-CMOS and embedded in	US 6252346	US 6252346	US 6252346	US 6252346
A 001308	09-Discretes and Multichip	RICHARD WOMACK 17413@PHILIPS.COM	09-Discretes and Multichip	US 6252347	US 6252347	US 6252347	US 6252347
A 050792	01-CMOS and embedded in	SUBHAS BOTHERA 21133@PHILIPS.COM	01-CMOS and embedded in	US 6252348	US 6252348	US 6252348	US 6252348
A 050744	04-Audio/Video	SANDRO BEDARIDA 21320@PHILIPS.COM	04-Audio/Video	US 6252349	US 6252349	US 6252349	US 6252349
A 050736	06-Connectivity	MARK BUER 21138@PHILIPS.COM	06-Connectivity	US 6252350	US 6252350	US 6252350	US 6252350
A 050795	09-Discretes and Multichip	MARK BUER 21138@PHILIPS.COM	09-Discretes and Multichip	US 6252351	US 6252351	US 6252351	US 6252351
060813	01-CMOS and embedded in	CLIVE TAYLOR 20741@PHILIPS.COM	01-CMOS and embedded in	US 6252352	US 6252352	US 6252352	US 6252352
060729	05-Mobile Communication	STEFAN OTT 10235@PHILIPS.COM	05-Mobile Communication	US 6252353	US 6252353	US 6252353	US 6252353
060716	04-Audio/Video	ALLEN PAGE 21383@PHILIPS.COM	04-Audio/Video	US 6252354	US 6252354	US 6252354	US 6252354
060710	01-CMOS and embedded in	PETER CHAMBERS 21153@PHILIPS.COM	01-CMOS and embedded in	US 6252355	US 6252355	US 6252355	US 6252355
022342	07-Identification	MARIAN WEBSTER@PHILIPS.COM	07-Identification	US 6252356	US 6252356	US 6252356	US 6252356
022343	08-Discretes and Multichip	WOLFRAM PRESCHER@PHILIPS.COM	08-Discretes and Multichip	US 6252357	US 6252357	US 6252357	US 6252357
022344	05-Mobile Communication	NAVEED MAJID 13557@PHILIPS.COM	05-Mobile Communication	US 6252358	US 6252358	US 6252358	US 6252358
022345	06-Connectivity	ERWIN SEINEN@PHILIPS.COM	06-Connectivity	US 6252359	US 6252359	US 6252359	US 6252359
022346	07-Identification	WOLF GANG EBER@PHILIPS.COM	07-Identification	US 6252360	US 6252360	US 6252360	US 6252360
022347	01-CMOS and embedded in	SUBHAS BOTHERA 21153@PHILIPS.COM	01-CMOS and embedded in	US 6252361	US 6252361	US 6252361	US 6252361
022348	04-Audio/Video	PETER CHAMBERS 21153@PHILIPS.COM	04-Audio/Video	US 6252362	US 6252362	US 6252362	US 6252362
022349	05-Mobile Communication	ROEL VAN DER TUIN@PHILIPS.COM	05-Mobile Communication	US 6252363	US 6252363	US 6252363	US 6252363
022350	06-Connectivity	KEN JARAMILLO 21275@PHILIPS.COM	06-Connectivity	US 6252364	US 6252364	US 6252364	US 6252364
022351	07-Identification	ELLE KHOURY@PHILIPS.COM	07-Identification	US 6252365	US 6252365	US 6252365	US 6252365
022352	08-Discretes and Multichip	ENGEL KOZAG@PHILIPS.COM	08-Discretes and Multichip	US 6252366	US 6252366	US 6252366	US 6252366
022353	09-Discretes and Multichip	DAVID CANARO@PHILIPS.COM	09-Discretes and Multichip	US 6252367	US 6252367	US 6252367	US 6252367
022354	01-CMOS and embedded in	WILLIAM THIES 17415@PHILIPS.COM	01-CMOS and embedded in	US 6252368	US 6252368	US 6252368	US 6252368
022355	05-Mobile Communication	OLIVIER LAPARRA 21303@PHILIPS.COM	05-Mobile Communication	US 6252369	US 6252369	US 6252369	US 6252369
022356	06-Connectivity	PIERRE WOLKES@PHILIPS.COM	06-Connectivity	US 6252370	US 6252370	US 6252370	US 6252370
022357	07-Identification	EDUARD STIKVDOORT@PHILIPS.COM	07-Identification	US 6252371	US 6252371	US 6252371	US 6252371
022358	08-Discretes and Multichip	OMER WEHUNT 21038@PHILIPS.COM	08-Discretes and Multichip	US 6252372	US 6252372	US 6252372	US 6252372
022359	09-Discretes and Multichip	NERVE MARIE@PHILIPS.COM	09-Discretes and Multichip	US 6252373	US 6252373	US 6252373	US 6252373
022360	01-CMOS and embedded in	WILLIAM THIES 17415@PHILIPS.COM	01-CMOS and embedded in	US 6252374	US 6252374	US 6252374	US 6252374
022361	05-Mobile Communication	OLIVIER LAPARRA 21303@PHILIPS.COM	05-Mobile Communication	US 6252375	US 6252375	US 6252375	US 6252375
022362	06-Connectivity	PIERRE WOLKES@PHILIPS.COM	06-Connectivity	US 6252376	US 6252376	US 6252376	US 6252376
022363	07-Identification	EDUARD STIKVDOORT@PHILIPS.COM	07-Identification	US 6252377	US 6252377	US 6252377	US 6252377
022364	08-Discretes and Multichip	OMER WEHUNT 21038@PHILIPS.COM	08-Discretes and Multichip	US 6252378	US 6252378	US 6252378	US 6252378
022365	09-Discretes and Multichip	NERVE MARIE@PHILIPS.COM	09-Discretes and Multichip	US 6252379	US 6252379	US 6252379	US 6252379
022366	01-CMOS and embedded in	WILLIAM THIES 17415@PHILIPS.COM	01-CMOS and embedded in	US 6252380	US 6252380	US 6252380	US 6252380
022367	05-Mobile Communication	OLIVIER LAPARRA 21303@PHILIPS.COM	05-Mobile Communication	US 6252381	US 6252381	US 6252381	US 6252381
022368	06-Connectivity	PIERRE WOLKES@PHILIPS.COM	06-Connectivity	US 6252382	US 6252382	US 6252382	US 6252382
022369	07-Identification	EDUARD STIKVDOORT@PHILIPS.COM	07-Identification	US 6252383	US 6252383	US 6252383	US 6252383
022370	08-Discretes and Multichip	OMER WEHUNT 21038@PHILIPS.COM	08-Discretes and Multichip	US 6252384	US 6252384	US 6252384	US 6252384
022371	09-Discretes and Multichip	NERVE MARIE@PHILIPS.COM	09-Discretes and Multichip	US 6252385	US 6252385	US 6252385	US 6252385



Pub No	IPC Class	IPC Class	Inventor	Pub No	IPC Class	IPC Class	Inventor
N 016390	3-sec-97	01-CMOS and embedded pr	ROB VERHAAR@PHILIPS.COM	US6065203			
A 050725	27-mnt-97	01-CMOS and embedded pr	PIERRE LEROUX@PHILIPS.COM	US6065204			
A 051263	27-mnt-97	01-CMOS and embedded pr	CHARLES DRILL 21190@PF.PHILIPS.COM	US6065205			
N 016281	27-mnt-97	01-CMOS and embedded pr	KOEN APPELTANS 14268@PF.PHILIPS.COM	US6065206			
N 016276	21-mnt-97	02-Packaging & Testing	MANOJ SACHDEV 14350@PF.PHILIPS.COM	US6065207			
N 016306	21-mnt-97	05-Mobile Communication	HADEH KHORRAMBADI 37936@PF.PHILIPS.COM	US6065208			
N 016283	21-mnt-97	05-Mobile Communication	FRANS PANISIER@PHILIPS.COM	US6065209			
N 016286	21-mnt-97	05-Mobile Communication	FRANS PANISIER@PHILIPS.COM	US6065210			
D 087834	20-mnt-97	09-Discrete and Multimed	EDWIN DILLING@PHILIPS.COM	US6065211			
A 050757	18-mnt-97	01-CMOS and embedded pr	FRANK VOLMAR 16872@PF.PHILIPS.COM	US6065212			
F 087533	18-mnt-97	09-Discrete and Multimed	SUBHAS BOHRA 21193@PF.PHILIPS.COM	US6065213			
F 087534	18-mnt-97	09-Discrete and Multimed	RICHARD MORISSON 12200@PF.PHILIPS.COM	US6065214			
A 050719	14-mnt-97	07-Identification	MARK BUER 21139@PF.PHILIPS.COM	US6065215			
A 050988	12-mnt-97	04-Audio/Video	SANDRO BEDARIDA 21120@PF.PHILIPS.COM	US6065216			
N 016262	11-mnt-97	04-Audio/Video	KURT MUHELMANN 02082@PF.PHILIPS.COM	US6065217			
A 050763	10-mnt-97	04-Audio/Video	CHRISTIAN PONTE 20951@PF.PHILIPS.COM	US6065218			
A 050983	10-mnt-97	08-Connectivity	CHRISTIAN PONTE 20951@PF.PHILIPS.COM	US6065219			
D 087027	6-mnt-97	07-Identification	RAUF MALZAHN@PHILIPS.COM	US6065220			
N 016256	5-mnt-97	05-Mobile Communication	JOHAN JACOBS 01345@PF.PHILIPS.COM	US6065221			
F 087526	4-mnt-97	04-Audio/Video	CHRISTIAN PONTE 20951@PF.PHILIPS.COM	US6065222			
A 050932	28-feb-97	02-Packaging & Testing	BURKHARD HEINKE 16647@PF.PHILIPS.COM	US6065223			
D 087022	28-feb-97	04-Audio/Video	ROEL VANBERTULING@PHILIPS.COM	US6065224			
A 050766	28-feb-97	05-Mobile Communication	EWARD FRASL@PHILIPS.COM	US6065225			
D 097598	28-feb-97	05-Mobile Communication	PETER BLEIM@PHILIPS.COM	US6065226			
C 097510	28-feb-97	05-Mobile Communication	CHRISTIAN PONTE 20951@PF.PHILIPS.COM	US6065227			
A 050684	27-feb-97	01-CMOS and embedded pr	CRAIG BELLOW 21122@PF.PHILIPS.COM	US6065228			
N 016236	27-feb-97	01-CMOS and embedded pr	FRANS WIDDERSHOVEN@PHILIPS.COM	US6065229			
N 016250	27-feb-97	04-Audio/Video	DURK VAN LEEUWEN@PHILIPS.COM	US6065230			
A 050756	25-feb-97	05-Mobile Communication	MICHEL LEEUWEN 21200@PF.PHILIPS.COM	US6065231			
F 087524	24-feb-97	03-Mobile Communication	MARC GOOAS 17276@PF.PHILIPS.COM	US6065232			
A 050967	24-feb-97	01-CMOS and embedded pr	JERRY HARVEY 21246@PF.PHILIPS.COM	US6065233			
D 087020	22-feb-97	03-RF Devices	RALF WENDEL 16281@PF.PHILIPS.COM	US6065234			
A 050853	21-feb-97	03-RF Devices	JOSEF PLETL 21392@PF.PHILIPS.COM	US6065235			
A 050471	21-feb-97	18-Other	SANDRO BEDARIDA 21120@PF.PHILIPS.COM	US6065236			
A 050738	19-feb-97	01-CMOS and embedded pr	RAMIRO SOLIS 21145@PF.PHILIPS.COM	US6065237			
B 054179	19-feb-97	09-Discrete and Multimed	DAVID OHEN 21159@PF.PHILIPS.COM	US6065238			
B 054180	19-feb-97	09-Discrete and Multimed	BRENDAN KELLY@PHILIPS.COM	US6065239			
F 087522	18-feb-97	05-Mobile Communication	BRENDAN KELLY@PHILIPS.COM	US6065240			
B 034136	15-feb-97	04-Audio/Video	STEVEN VAN LERBERGHE@PHILIPS.COM	US6065241			
F 087517	14-feb-97	03-RF Devices	JOHN KINCHORN@PHILIPS.COM	US6065242			
N 050719	11-feb-97	05-Mobile Communication	AXEL MEYER 16847@PF.PHILIPS.COM	US6065243			
N 050715	10-feb-97	05-Mobile Communication	DAVE EVOY@PHILIPS.COM	US6065244			
N 016213	7-feb-97	05-Mobile Communication	ROBERT MORES 15653@PF.PHILIPS.COM	US6065245			
N 016214	7-feb-97	05-Mobile Communication	HENK DEKKS@PHILIPS.COM	US6065246			
D 050882	31-jan-97	02-Packaging & Testing	HENK VISSER@PHILIPS.COM	US6065247			
D 050671	31-jan-97	02-Packaging & Testing	ROBERT PAYNE@PHILIPS.COM	US6065248			
D 050716	30-jan-97	06-Connectivity	MORGAN DEMPSEY 21183@PF.PHILIPS.COM	US6065249			
D 050704	28-jan-97	05-Mobile Communication	HERMAN SCHUTTE 07348@PF.PHILIPS.COM	US6065250			
D 050706	28-jan-97	06-Connectivity	PATRICK BAILLY 08145@PF.PHILIPS.COM	US6065251			
D 050707	28-jan-97	06-Connectivity	SUNG-HUN OH 21378@PF.PHILIPS.COM	US6065252			
D 050708	28-jan-97	02-Packaging & Testing	PAUL GORSSSEN@PHILIPS.COM	US6065253			
D 050709	28-jan-97	02-Packaging & Testing	GUIDO PLANGGER@PHILIPS.COM	US6065254			
D 050710	28-jan-97	03-Automotive	ROBERT MORES 15653@PF.PHILIPS.COM	US6065255			
D 050711	21-jan-97	03-RF Devices	BERNHARD SPIESS@PHILIPS.COM	US6065256			
D 050712	21-jan-97	03-RF Devices	KONRAD BAUMANN 16843@PF.PHILIPS.COM	US6065257			
D 050713	21-jan-97	03-RF Devices	MARTIN APISCHNER 16135@PF.PHILIPS.COM	US6065258			
D 050714	21-jan-97	07-Identification	SIGI ARNOLD@PHILIPS.COM	US6065259			
D 050715	21-jan-97	07-Identification	WERNER JAMESCH@PHILIPS.COM	US6065260			
D 050716	15-jan-97	01-CMOS and embedded pr	OLIVIER LAPARRA 21303@PF.PHILIPS.COM	US6065261			
D 050717	15-jan-97	04-Audio/Video	HANS BREKELMANS@PHILIPS.COM	US6065262			
D 050718	9-jan-97	09-Discrete and Multimed	ELIE KHOURY@PHILIPS.COM	US6065263			
D 050719	9-jan-97	01-CMOS and embedded pr	CALVIN GABRIEL 20439@PF.PHILIPS.COM	US6065264			
D 050720	31-dec-96	05-Mobile Communication	DAVE EVOY@PHILIPS.COM	US6065265			



Patent No.	IPC Class.	Inventor	Applicant	Pub. No.	Pub. Date	Pub. Type	Pub. Country	Pub. Title	Pub. Title Transl.	Pub. Title Transl.	Pub. Title Transl.
0158186	04-Audio/Video	HARTMUT HARLOS	PHILIPS.COM	19930229	1993-02-29	EP	DE	PROCESOR INSTRUCTIONS WITH RESTORE - NOT RESTORE BIT	PROCESOR INSTRUCTIONS WITH RESTORE - NOT RESTORE BIT	PROCESOR INSTRUCTIONS WITH RESTORE - NOT RESTORE BIT	19930229.9
0158187	04-Audio/Video	GRAHAM THOMSON	PHILIPS.COM	19930229	1993-02-29	EP	EP	SELF-TUNED RECEIVER CALIBRATION	SELF-TUNED RECEIVER CALIBRATION	SELF-TUNED RECEIVER CALIBRATION	19930229.7
0158188	05-Mobile Communication	KAVE KIANUSH	PHILIPS.COM	19930229	1993-02-29	EP	EP	CURRENT DISTRIBUTION CIRCUIT FOR PHONE	CURRENT DISTRIBUTION CIRCUIT FOR PHONE	CURRENT DISTRIBUTION CIRCUIT FOR PHONE	19930229.5
0158189	05-Mobile Communication	JACOB MULDER	PHILIPS.COM	19930229	1993-02-29	EP	EP	ADAPTIVE SLOPE POWER SUPPLY FOR PHONE	ADAPTIVE SLOPE POWER SUPPLY FOR PHONE	ADAPTIVE SLOPE POWER SUPPLY FOR PHONE	19930229.6
0158190	06-Connectivity	RONALD LANGE	PHILIPS.COM	19930229	1993-02-29	EP	EP	SELECTIVE LATENCY REDUCTION IN BRIDGE SYSTEM BETWEEN T	SELECTIVE LATENCY REDUCTION IN BRIDGE SYSTEM BETWEEN T	SELECTIVE LATENCY REDUCTION IN BRIDGE SYSTEM BETWEEN T	19930229.8
0158191	06-Connectivity	RONALD LANGE	PHILIPS.COM	19930229	1993-02-29	EP	EP	ARRANGEMENT AND METHOD FOR ALLOWING SEQUENCE-INDEPHUS	ARRANGEMENT AND METHOD FOR ALLOWING SEQUENCE-INDEPHUS	ARRANGEMENT AND METHOD FOR ALLOWING SEQUENCE-INDEPHUS	19930229.9
0158192	06-Connectivity	RONALD LANGE	PHILIPS.COM	19930229	1993-02-29	EP	EP	ADDRESS AND METHOD FOR EFFICIENTLY IMPLEMENTING COM	ADDRESS AND METHOD FOR EFFICIENTLY IMPLEMENTING COM	ADDRESS AND METHOD FOR EFFICIENTLY IMPLEMENTING COM	19930229.10
0158193	06-Connectivity	KEN DOCKNER	PHILIPS.COM	19930229	1993-02-29	EP	EP	METHOD AND APPARATUS FOR RADIO MOBILES	METHOD AND APPARATUS FOR RADIO MOBILES	METHOD AND APPARATUS FOR RADIO MOBILES	19930229.11
0158194	01-CMOS and embedded p	DAVID DUPERRAY	PHILIPS.COM	19930229	1993-02-29	EP	FR	TEMPERATURE CONTROLLED PROCESS SWITCH (GRACEFUL D.)	TEMPERATURE CONTROLLED PROCESS SWITCH (GRACEFUL D.)	TEMPERATURE CONTROLLED PROCESS SWITCH (GRACEFUL D.)	19930229.12
0158195	03-RF Devices	MICHAEL ANG	PHILIPS.COM	19930229	1993-02-29	EP	US	READ CROSSBAR ELIMINATION IN A VLIW PROCESSOR	READ CROSSBAR ELIMINATION IN A VLIW PROCESSOR	READ CROSSBAR ELIMINATION IN A VLIW PROCESSOR	19930229.13
0158196	04-Audio/Video	ERNO JACOBS	PHILIPS.COM	19930229	1993-02-29	EP	US	DATA TRANSFER BETWEEN 2 CLOCK DOMAINS	DATA TRANSFER BETWEEN 2 CLOCK DOMAINS	DATA TRANSFER BETWEEN 2 CLOCK DOMAINS	19930229.14
0158197	06-Packaging & Testing	DOUG KAY	PHILIPS.COM	19930229	1993-02-29	EP	US	SCAN FLIP-FLOP AND METHODS FOR CONTROLLING THE	SCAN FLIP-FLOP AND METHODS FOR CONTROLLING THE	SCAN FLIP-FLOP AND METHODS FOR CONTROLLING THE	19930229.15
0158198	03-RF Devices	ROBERT LATHAM	PHILIPS.COM	19930229	1993-02-29	EP	US	SHIELDED CONDUCTORS FOR MIXED-SIGNAL CHIP	SHIELDED CONDUCTORS FOR MIXED-SIGNAL CHIP	SHIELDED CONDUCTORS FOR MIXED-SIGNAL CHIP	19930229.16
0158199	02-Packaging & Testing	ROBERT LATHAM	PHILIPS.COM	19930229	1993-02-29	EP	US	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUME	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUME	TELECOMMUNICATION DEVICE WITH REDUCED POWER CONSUME	19930229.17
0158200	04-Audio/Video	KAMRAN MANTEGHI	PHILIPS.COM	19930229	1993-02-29	EP	DE	HIGH DENSITY LEADED BALL-GRID ARRAY PACKAGE	HIGH DENSITY LEADED BALL-GRID ARRAY PACKAGE	HIGH DENSITY LEADED BALL-GRID ARRAY PACKAGE	19930229.18
0158201	04-Audio/Video	JOSE OLIVEIRA	PHILIPS.COM	19930229	1993-02-29	EP	US	INTERRUPT SYSTEM FOR MIPS PROCESSORS	INTERRUPT SYSTEM FOR MIPS PROCESSORS	INTERRUPT SYSTEM FOR MIPS PROCESSORS	19930229.19
0158202	10-Other	DAVE EVOY	PHILIPS.COM	19930229	1993-02-29	EP	EP	POWER MANAGEMENT SYSTEM FOR A COMPUTER	POWER MANAGEMENT SYSTEM FOR A COMPUTER	POWER MANAGEMENT SYSTEM FOR A COMPUTER	19930229.20
0158203	02-Packaging & Testing	SCOTT HARRON	PHILIPS.COM	19930229	1993-02-29	EP	US	INTERRUPT BASED POSITIONING SYSTEM FOR JOYSTICKS AND M	INTERRUPT BASED POSITIONING SYSTEM FOR JOYSTICKS AND M	INTERRUPT BASED POSITIONING SYSTEM FOR JOYSTICKS AND M	19930229.21
0158204	04-Audio/Video	KAMRAN MANTEGHI	PHILIPS.COM	19930229	1993-02-29	EP	US	WEIGHTED BUS ARBITRATION	WEIGHTED BUS ARBITRATION	WEIGHTED BUS ARBITRATION	19930229.22
0158205	04-Audio/Video	ERNO JACOBS	PHILIPS.COM	19930229	1993-02-29	EP	US	UNKI INTERFERENCE DETECTION CIRCUIT	UNKI INTERFERENCE DETECTION CIRCUIT	UNKI INTERFERENCE DETECTION CIRCUIT	19930229.23
0158206	04-Audio/Video	DAVID CHEN	PHILIPS.COM	19930229	1993-02-29	EP	US	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	19930229.24
0158207	04-Audio/Video	GEORG KASPERKOWITZ	PHILIPS.COM	19930229	1993-02-29	EP	EP	BUFFER AND METHOD FOR TRANSFERRING DATA THEREIN	BUFFER AND METHOD FOR TRANSFERRING DATA THEREIN	BUFFER AND METHOD FOR TRANSFERRING DATA THEREIN	19930229.25
0158208	05-Mobile Communication	STEVE LEUNG	PHILIPS.COM	19930229	1993-02-29	EP	US	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	MODULAR SCALABLE MULTI-PROCESSOR ARCHITECTURE	19930229.26
0158209	10-Other	PETER CHAMBERS	PHILIPS.COM	19930229	1993-02-29	EP	US	WAVELENGTH ADDRESS CACHE TO REDUCE ACCESSSES OVER A	WAVELENGTH ADDRESS CACHE TO REDUCE ACCESSSES OVER A	WAVELENGTH ADDRESS CACHE TO REDUCE ACCESSSES OVER A	19930229.27
0158210	03-RF Devices	RAUF WENDEL	PHILIPS.COM	19930229	1993-02-29	EP	DE	DUAL GATE OXIDE PROCESS WITH INCREASED RELIABILITY	DUAL GATE OXIDE PROCESS WITH INCREASED RELIABILITY	DUAL GATE OXIDE PROCESS WITH INCREASED RELIABILITY	19930229.28
0158211	03-RF Devices	RAUF WENDEL	PHILIPS.COM	19930229	1993-02-29	EP	DE	DC-OFFSET CORRECTION IN ZERO - IF RECEIVER	DC-OFFSET CORRECTION IN ZERO - IF RECEIVER	DC-OFFSET CORRECTION IN ZERO - IF RECEIVER	19930229.29
0158212	01-CMOS and embedded p	SUBHASH NARIANI	PHILIPS.COM	19930229	1993-02-29	EP	EP	TELECOMMUNICATION DEVICE WITH A SWITCHING RING CIRCUIT	TELECOMMUNICATION DEVICE WITH A SWITCHING RING CIRCUIT	TELECOMMUNICATION DEVICE WITH A SWITCHING RING CIRCUIT	19930229.30
0158213	03-RF Devices	JOACHIM LANGE	PHILIPS.COM	19930229	1993-02-29	EP	DE	FREQUENCY INDEPENDENT VOLTAGE DIVIDER	FREQUENCY INDEPENDENT VOLTAGE DIVIDER	FREQUENCY INDEPENDENT VOLTAGE DIVIDER	19930229.31
0158214	03-RF Devices	ANDRE VAN BEZOUJEN	PHILIPS.COM	19930229	1993-02-29	EP	WO	IMPROVE TRAPPING OF NO INSTRUCTIOCKERS IN	IMPROVE TRAPPING OF NO INSTRUCTIOCKERS IN	IMPROVE TRAPPING OF NO INSTRUCTIOCKERS IN	19930229.32
0158215	03-RF Devices	BARRY DAVIS	PHILIPS.COM	19930229	1993-02-29	EP	US	METHOD AND APPARATUS FOR MAINTAINING COHERENCY IN	METHOD AND APPARATUS FOR MAINTAINING COHERENCY IN	METHOD AND APPARATUS FOR MAINTAINING COHERENCY IN	19930229.33
0158216	03-RF Devices	SWAROOP ADUSUMILLI	PHILIPS.COM	19930229	1993-02-29	EP	US	DEADLOCK RESOLUTION AND APPARATUS FOR INTERFACING CO	DEADLOCK RESOLUTION AND APPARATUS FOR INTERFACING CO	DEADLOCK RESOLUTION AND APPARATUS FOR INTERFACING CO	19930229.34
0158217	03-RF Devices	WOLFGANG BUHR	PHILIPS.COM	19930229	1993-02-29	EP	DE	SECURE PERSONALIZATION OF SPARE ELECTRONIC KEYS	SECURE PERSONALIZATION OF SPARE ELECTRONIC KEYS	SECURE PERSONALIZATION OF SPARE ELECTRONIC KEYS	19930229.35
0158218	01-CMOS and embedded p	IAN HARVEY	PHILIPS.COM	19930229	1993-02-29	EP	DE	CUSTOM LASER CONDUCTOR LINKAGE FOR INTEGRATED CIRCUIT	CUSTOM LASER CONDUCTOR LINKAGE FOR INTEGRATED CIRCUIT	CUSTOM LASER CONDUCTOR LINKAGE FOR INTEGRATED CIRCUIT	19930229.36
0158219	01-CMOS and embedded p	HAROLD FISCHER	PHILIPS.COM	19930229	1993-02-29	EP	US	ELECTRICAL DEVICE WITH ENERGY STORAGE	ELECTRICAL DEVICE WITH ENERGY STORAGE	ELECTRICAL DEVICE WITH ENERGY STORAGE	19930229.37
0158220	01-CMOS and embedded p	YU-PIN HAN	PHILIPS.COM	19930229	1993-02-29	EP	DE	APPARATUS AND METHOD FOR PROGRAMMING ANTIFUSE STRUC	APPARATUS AND METHOD FOR PROGRAMMING ANTIFUSE STRUC	APPARATUS AND METHOD FOR PROGRAMMING ANTIFUSE STRUC	19930229.38
0158221	04-Audio/Video	IVAN SANCHEZ	PHILIPS.COM	19930229	1993-02-29	EP	US	METHOD OF MAKING DOPED ANTIFUSE STRUCTURES	METHOD OF MAKING DOPED ANTIFUSE STRUCTURES	METHOD OF MAKING DOPED ANTIFUSE STRUCTURES	19930229.39
0158222	05-Mobile Communication	JAN PAUL HUYSER	PHILIPS.COM	19930229	1993-02-29	EP	US	LOAD DETECTION IN BRIDGE AMPLIFIER	LOAD DETECTION IN BRIDGE AMPLIFIER	LOAD DETECTION IN BRIDGE AMPLIFIER	19930229.40
0158223	02-Packaging & Testing	THOMAS G.J. RICHTER	PHILIPS.COM	19930229	1993-02-29	EP	EP	MOBILE WITH SYNCHRONIZATION DELAY	MOBILE WITH SYNCHRONIZATION DELAY	MOBILE WITH SYNCHRONIZATION DELAY	19930229.41
0158224	02-Packaging & Testing	KESHAVA SATISH	PHILIPS.COM	19930229	1993-02-29	EP	DE	LIMITED PROBE DEVICE TESTING FOR HIGH PIN COUNT DIGITAL	LIMITED PROBE DEVICE TESTING FOR HIGH PIN COUNT DIGITAL	LIMITED PROBE DEVICE TESTING FOR HIGH PIN COUNT DIGITAL	19930229.42
0158225	07-Identification	LONNIE GOFF	PHILIPS.COM	19930229	1993-02-29	EP	US	CLOCK CLAMPING CIRCUIT THAT PREVENTS CLOCK GLITCHING A	CLOCK CLAMPING CIRCUIT THAT PREVENTS CLOCK GLITCHING A	CLOCK CLAMPING CIRCUIT THAT PREVENTS CLOCK GLITCHING A	19930229.43
0158226	06-Discretizes and Multimat	THOMAS WILLE	PHILIPS.COM	19930229	1993-02-29	EP	US	MICROPROCESSOR ARCHITECTURE FOR CRYPTO-SMART-CARDS	MICROPROCESSOR ARCHITECTURE FOR CRYPTO-SMART-CARDS	MICROPROCESSOR ARCHITECTURE FOR CRYPTO-SMART-CARDS	19930229.44
0158227	03-RF Devices	MAREIKE KLEE	PHILIPS.COM	19930229	1993-02-29	EP	DE	ANTENNA DIVERSITY IN PORTABLE TRANSCIEVER	ANTENNA DIVERSITY IN PORTABLE TRANSCIEVER	ANTENNA DIVERSITY IN PORTABLE TRANSCIEVER	19930229.45
0158228	04-Audio/Video	DAVID CASSETTI	PHILIPS.COM	19930229	1993-02-29	EP	FR	PLL LOCK-ON DETECTOR	PLL LOCK-ON DETECTOR	PLL LOCK-ON DETECTOR	19930229.46
0158229	04-Audio/Video	ALAIN VIGNE	PHILIPS.COM	19930229	1993-02-29	EP	FR	SHOCK PIPE FOR CD PLAYER	SHOCK PIPE FOR CD PLAYER	SHOCK PIPE FOR CD PLAYER	19930229.47
0158230	09-Discretizes and Multimat	HANS VOORMAN	PHILIPS.COM	19930229	1993-02-29	EP	FR	CAPACITIVE SENSING ARRAY WITH OXIDISED ELECTRODES	CAPACITIVE SENSING ARRAY WITH OXIDISED ELECTRODES	CAPACITIVE SENSING ARRAY WITH OXIDISED ELECTRODES	19930229.48
0158231	04-Audio/Video	JOHN HAROLD BARRY	PHILIPS.COM	19930229	1993-02-29	EP	GB	SIGNAL PROCESSOR WITH ASH-STORAGE	SIGNAL PROCESSOR WITH ASH-STORAGE	SIGNAL PROCESSOR WITH ASH-STORAGE	19930229.49
0158232	04-Audio/Video	MIGEL YOUNG	PHILIPS.COM	19930229	1993-02-29	EP	GB	FREQUENCY CONVERTER FOR COLOR SIGNALS	FREQUENCY CONVERTER FOR COLOR SIGNALS	FREQUENCY CONVERTER FOR COLOR SIGNALS	19930229.50
0158233	05-Mobile Communication	HARALD N. BAUER	PHILIPS.COM	19930229	1993-02-29	EP	WO	DISCRETE BIPOLAR TRANSISTOR WITH DOUBLE EM. CONNECTION	DISCRETE BIPOLAR TRANSISTOR WITH DOUBLE EM. CONNECTION	DISCRETE BIPOLAR TRANSISTOR WITH DOUBLE EM. CONNECTION	19930229.51
0158234	04-Audio/Video	MARKUS FRANZ	PHILIPS.COM	19930229	1993-02-29	EP	DE	PATTERNED FILLED LAYERS FOR INTEGRATED CIRCUIT MANUFAC	PATTERNED FILLED LAYERS FOR INTEGRATED CIRCUIT MANUFAC	PATTERNED FILLED LAYERS FOR INTEGRATED CIRCUIT MANUFAC	19930229.52
0158235	04-Audio/Video	RONALD DEKKER	PHILIPS.COM	19930229	1993-02-29	EP	DE	ASYNCHRONOUS SAMPLING PEAK DETECTOR	ASYNCHRONOUS SAMPLING PEAK DETECTOR	ASYNCHRONOUS SAMPLING PEAK DETECTOR	19930229.53
0158236	04-Audio/Video	BILL FINDLEY	PHILIPS.COM	19930229	1993-02-29	EP	GB	SETTING-UP CARTESIAN LOOP TRANSMITTER	SETTING-UP CARTESIAN LOOP TRANSMITTER	SETTING-UP CARTESIAN LOOP TRANSMITTER	19930229.54
0158237	05-Mobile Communication	NICK SWALES	PHILIPS.COM	19930229	1993-02-29	EP	EP	MONITORING THE LOOP OF A CARTESIAN LOOP	MONITORING THE LOOP OF A CARTESIAN LOOP	MONITORING THE LOOP OF A CARTESIAN LOOP	19930229.55
0158238	05-Mobile Communication	GEORF SPARKS	PHILIPS.COM	19930229	1993-02-29	EP	EP	POWER CONTROL OF TRANSMITTER POWER AMPLIFIERS	POWER CONTROL OF TRANSMITTER POWER AMPLIFIERS	POWER CONTROL OF TRANSMITTER POWER AMPLIFIERS	19930229.56
0158239	05-Mobile Communication	GEORF SPARKS	PHILIPS.COM	19930229	1993-02-29	EP	EP	SYSTEM FOR SUPPORTING (DMA) INPUT / OUTPUT (IO) DEVICES	SYSTEM FOR SUPPORTING (DMA) INPUT / OUTPUT (IO) DEVICES	SYSTEM FOR SUPPORTING (DMA) INPUT / OUTPUT (IO) DEVICES	19930229.57
0158240	05-Mobile Communication	JAMES JIRGAL	PHILIPS.COM	19930229	1993-02-29	EP	EP	SIGNAL PROCESSOR	SIGNAL PROCESSOR	SIGNAL PROCESSOR	19930229.58
0158241	05-Mobile Communication	HARALD N. BAUER	PHILIPS.COM	19930229	1993-02-29	EP	DE	LOW DISSIPATION BUFFER CIRCUIT	LOW DISSIPATION BUFFER CIRCUIT	LOW DISSIPATION BUFFER CIRCUIT	19930229.59
0158242	05-Mobile Communication	JEAN PERRAUD	PHILIPS.COM	19930229	1993-02-29	EP	FR	LOW DISSIPATION BUFFER CIRCUIT	LOW DISSIPATION BUFFER CIRCUIT	LOW DISSIPATION BUFFER CIRCUIT	19930229.60

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AUSTRIA CARD PLASTIKARTI



N	12-nrt-96	10-Other	HARRIE, MAAS@PHILIPS.COM	SOA DEVICES WITH INTEGRATED SPACER	EP	98200673	US5698122
N 015716	12-nrt-96	10-Other	RONALD DEKKER@PHILIPS.COM	MAKING HYBRIDE SDA DEVICES	EP	98200674	US5706520
A 050545	11-nrt-96	04-CMOS and embedded pr	DAVID ZIGER@PHILIPS.COM	THIN FILM THICKNESS AND OPTIMAL FOCUS MEASURING	US	086713932	US5829466
D 096030	8-nrt-96	01-Audio/Video	SCOTT HARROW 21250@PF.PHILIPS.COM	STARTING CIRCUIT FOR HIGH PASS FILTERS	ENGLIS	086713151	US5924226
D 096029	8-nrt-96	05-Mobile Communication	BURKHARD DICK@PHILIPS.COM	LIN-LOG-CONVERTER	DE	1969890547	US5914870
B 034051	7-nrt-96	05-Mobile Communication	KNUD HOLTVOETH@PHILIPS.COM	PHILIPS C STEPWISE BIAS CURRENT REDUCTION FOR RF AMPLIFIERS	DE	1860886615	US5914870
N 015711	6-nrt-96	09-Discrete and Multimarke	LUDIK@NATLAB.RESEARCH.PHILIPS.COM	PCI BUS MASTER WITH CASCADED PCI ARBITRATION	EP	982001814	US5929482
A 052166	5-nrt-96	10-Other	FRANK STORY 21454@PF.PHILIPS.COM	QUADRATURE DEMOD. AND DIGIT. POSTPROCESSING	US	086711784	US5930492
D 096024	5-nrt-96	07-Identification	WALTER EINFELDT@PHILIPS.COM	TEMPERATURE COMPENSATED REFERENCE VOLTAGE SOURCE	DE	196934512	US5931560
O 096025	2-nrt-96	07-Identification	MANFRED BIEHL 08115@PF.PHILIPS.COM	IMPROVING METASTABLE RESOLVING TIME	EP	982001518	US5931560
N 015097	28-feb-96	09-Discrete and Multimarke	PETER PRILLER 16387@PF.PHILIPS.COM	IF-DEMULATOR WITH AGC-AMPLIFIER	DE	196930661	US5931560
F 095216	27-feb-96	05-Discrete and Multimarke	BRAMMEL SE 06508@PF.PHILIPS.COM	HALF-DUPLEX BIDIIRECTIONAL DATA LINE	EP	982001517	US5931560
F 095217	27-feb-96	05-Discrete and Multimarke	RONALD CLINE 07509@PF.PHILIPS.COM	WELL RESISTOR AS A BALLAST RESISTOR FOR OUTPUT MOSFET	EP	982001517	US5931560
A 050414	14-feb-96	04-Audio/Video	MATHEILICLEVER 16106@PF.PHILIPS.COM	CHARGE PUMP ADDRESSING	US	196940495	US5931560
A 050415	14-feb-96	04-Audio/Video	KEN DOCKSER 21187@PF.PHILIPS.COM	METHOD FOR IMPROVING THE MANUFACTURABILITY OF THE SPIA US	EP	086503900	US5931560
D 096016	10-feb-96	01-CMOS and embedded pr	CALVIN GABRIEL 20439@PF.PHILIPS.COM	BLACK LEVEL SETTING THRU CONTROL + REF VALUES RATIOS	EP	982001951	US5931560
N 015882	7-feb-96	04-Audio/Video	SOENKE STRUCK 15056@PF.PHILIPS.COM	IF-DEMULATOR WITH AGC-AMPLIFIER	FR	982001951	US5931560
N 015873	5-feb-96	04-Audio/Video	PIET VAN DER ZEE@PHILIPS.COM	FOULDED CASCADE IN BEAM CURRENT MEASUREMENT	US	086501437	US5931560
Q 026014	5-feb-96	07-Identification	HENRICUS TULDER 05241@PF.PHILIPS.COM	MULTI-STANDARD IF WITH SWITCHABLE PARALLEL FILTER	DE	196048298	US5931560
A 050608	31-jan-96	01-CMOS and embedded pr	DOMINIK BERGER 18639@PF.PHILIPS.COM	PHILIPS CC OPTIMIZED UNDERLAYER STRUCTURES FOR MAINTAINING CHEMI US	DE	196040495	US5931560
A 050563	30-jan-96	01-CMOS and embedded pr	SUBHAS BOTHRA 21133@PF.PHILIPS.COM	BLACK LEVEL SETTING THRU CONTROL + REF VALUES RATIOS	US	086503900	US5931560
A 050560	30-jan-96	01-CMOS and embedded pr	SUBHAS BOTHRA 21133@PF.PHILIPS.COM	BLACK LEVEL SETTING THRU CONTROL + REF VALUES RATIOS	US	086503900	US5931560
N 015662	29-jan-96	04-Audio/Video	DICK VAN DEN BROEKE@PHILIPS.COM	IF-DEMULATOR WITH AGC-AMPLIFIER	EP	982001951	US5931560
D 096039	29-jan-96	04-Audio/Video	JOACHIM BRILKA@PHILIPS.COM	IF-DEMULATOR WITH AGC-AMPLIFIER	EP	982001951	US5931560
F 098506	15-jan-96	07-Identification	PHILIPPE MAUGARS@PHILIPS.COM	HALF-DUPLEX BIDIIRECTIONAL DATA LINE	DE	196921871	US5931560
A 050578	15-jan-96	01-CMOS and embedded pr	CHUN JIANG 21275@PF.PHILIPS.COM	WELL RESISTOR AS A BALLAST RESISTOR FOR OUTPUT MOSFET	FR	982001951	US5931560
A 050687	15-jan-96	09-Discrete and Multimarke	JOHN CALLAHAN 21146@PF.PHILIPS.COM	CHARGE PUMP ADDRESSING	US	086506304	US5931560
A 050540	29-dec-95	01-CMOS and embedded pr	PAUL FINDLEY 21216@PF.PHILIPS.COM	METHOD FOR MAKING ANTIFUSE STRUCTURES USING IMPLANTATION US	US	027728300	US5931560
A 050527	28-dec-95	01-CMOS and embedded pr	YU-PIN LIAH 21249@PF.PHILIPS.COM	METHOD FOR VERIFYING AN AVERAGE TOPOGRAPHY HEIGHT FUJIS	US	086502844	US5931560
A 050558	28-dec-95	01-CMOS and embedded pr	PIERRE LEROUX@PHILIPS.COM	METHOD AND APPARATUS FOR FABRICATING ANTI-FUSE DEVICES US	US	08579824	US5931560
A 050571	28-dec-95	02-Packaging & Testing	MIGUEL DELGADO 21162@PF.PHILIPS.COM	ELECTRICALLY ENHANCED POWER QUAD FLAT PACK ARRANGEM US	US	08681294	US5931560
A 050694	28-dec-95	04-Audio/Video	KAMRAN MANTEGHI 21334@PF.PHILIPS.COM	SYSTEM AND METHOD FOR PROGRAMMING VROM LINKS	US	08581946	US5931560
A 023382	28-dec-95	05-Mobile Communication	JOHN D'ICCONO 21164@PF.PHILIPS.COM	POWER MOSFET WITH METAL GATE IN SOI	US	086509408	US5931560
A 023063	28-dec-95	05-Mobile Communication	MANJUN KIM 03821@PF.PHILIPS.COM	TRANSISTOR IN SOI WITH MULTIPLE BASE CONTACT	US	086579703	US5931560
A 050579	27-dec-95	01-CMOS and embedded pr	DAVE EVOY@PHILIPS.COM	SYSTEM AND METHOD FOR ALTERING BUS SPEED BASED ON BUS US	US	08579489	US5931560
A 052195	27-dec-95	01-CMOS and embedded pr	PIERRE LEROUX@PHILIPS.COM	WAFER WITH FOCUS EXPOSURE MATRIX	US	09579172	US5931560
A 050225	27-dec-95	04-Audio/Video	TERESA TROWBRIDGE 21466@PF.PHILIPS.COM	ELECTRICALLY CONDUCTIVE INTERCONNECTS FOR INTEGRATED US	US	086578102	US5931560
A 050531	27-dec-95	04-Audio/Video	KEN DOCKSER 21187@PF.PHILIPS.COM	GSM AUTO GAIN CONTROL	US	086578102	US5931560
A 050537	27-dec-95	05-Mobile Communication	LAURENT SOUPE@PHILIPS.COM	GSM AUTO GAIN CONTROL	US	086578102	US5931560
N 050480	27-dec-95	09-Discrete and Multimarke	JOHN CALLAHAN 21145@PF.PHILIPS.COM	PHILIPS COI HIGH VOLTAGE DETECT CIRCUIT WITH INCREASED LONG TERM R US	US	086578490	US5931560
N 050543	26-dec-95	01-CMOS and embedded pr	PETER CHAMBERS 21155@PF.PHILIPS.COM	OPTIMIZED STRUCTURES FOR DUMMY FILL MASK DESIGN	US	08679685	US5931560
N 050544	26-dec-95	01-CMOS and embedded pr	FRECK HUISMAN 03881@PF.PHILIPS.COM	WRAPPED-LINE CACHE FOR MICROPROCESSOR SYSTEM	EP	85203583	US5931560
A 050549	20-dec-95	04-Audio/Video	DOLG BAUMANN 21629@PF.PHILIPS.COM	SMART CARD DETECTION SYSTEM	US	086575765	US5931560
F 055597	20-dec-95	07-Identification	SIEGFRIED RITTER 16008@PF.PHILIPS.COM	SMART CARD DETECTION SYSTEM	DE	19547694	US5931560
N 050604	18-dec-95	01-CMOS and embedded pr	PHILIPPE MAUGARS@PHILIPS.COM	WAFER EDGE SEALING	FR	95151778	US5931560
N 050605	15-dec-95	02-Packaging & Testing	JUDY GALLOWAY 21223@PF.PHILIPS.COM	MOST WITH BURIED DIFFUSION STOPPER OF SIZE	US	086574059	US5931560
N 050606	14-dec-95	02-Packaging & Testing	J.SCHMITZ@UTWENTE.NL	MOST WITH BURIED DIFFUSION STOPPER OF SIZE	EP	952039572	US5931560
N 050607	13-dec-95	02-Packaging & Testing	REINDER GAAL 14826@PF.PHILIPS.COM	SDI PACKAGE FOR OSF DIODE WITH CONTACT PIN	EP	952039572	US5931560
N 050608	11-dec-95	09-Discrete and Multimarke	STEVEN MERCHANT 05092@PF.PHILIPS.COM	MOLD FLOW REGULATING DAM RING	EP	086571488	US5931560
N 050609	8-dec-95	01-CMOS and embedded pr	SANG LEE 21306@PF.PHILIPS.COM	SOI PACKAGING ASSEMBLIES FOR ENCAPSULATED INTEGRATED CIRCU US	US	086570707	US5931560
N 050610	8-dec-95	02-Packaging & Testing	CHE-YUAN CHIEN 21198@PF.PHILIPS.COM	SOI PACKAGING ASSEMBLIES FOR ENCAPSULATED INTEGRATED CIRCU US	US	086570707	US5931560
N 050611	1-dec-95	02-Packaging & Testing	MARCEL AALMERS@PHILIPS.COM	503.3 VOLT BUS INTERFACE / LATCH	EP	952033314	US5931560
N 050612	30-nov-95	05-Mobile Communication	BRIAN MARTIN 14564@PF.PHILIPS.COM	CURRENT ONE-SHOT CIRCUIT	EP	086565886	US5931560
N 050613	30-nov-95	05-Mobile Communication	BRIAN MARTIN 14564@PF.PHILIPS.COM	CURRENT ONE-SHOT CIRCUIT	US	086565886	US5931560
N 015379	29-nov-95	09-Discrete and Multimarke	HENK DERKS@PHILIPS.COM	LOW VOLTAGE LOGIC CIRCUIT	US	952032266	US5931560
N 015558	7-nov-95	02-Packaging & Testing	DOUGLAS GRANT 14863@PF.PHILIPS.COM	A SINGLE CLASS D-AMPLIFIER FOR RINGING AND SPEECH	EP	952032266	US5931560
N 050545	7-nov-95	02-Packaging & Testing	KAMRAN MANTEGHI 21334@PF.PHILIPS.COM	LEADFRAME BALL GRID ARRAY PACKAGE	US	952032266	US5931560
N 050566	7-nov-95	01-CMOS and embedded pr	KAMRAN MANTEGHI 21334@PF.PHILIPS.COM	TECHNIQUE FOR IMPROVING BONDING STRENGTH OF LEADFRAM US	US	086533214	US5931560
N 050530	3-nov-95	02-Packaging & Testing	GREG EHMANN@PHILIPS.COM	MOLDED LEADFRAME BALL GRID ARRAY	US	086533214	US5931560
A 051281	31-dkt-95	01-CMOS and embedded pr	MOAZEM HOSSAIN 22140@PF.PHILIPS.COM	GTI INPUT RECEIVER WITH HYSTERSIS	US	086550848	US5931560

Handwritten mark resembling a stylized '2' or similar symbol.

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Invorder

Pub. No.	Pub. Date	Inventor/Assignor	Title	IPC Class.	Pub. No.	Pub. Date	Inventor/Assignor	Title	IPC Class.
F 045582	3-04-95	04-Audio/Video	PIERRE, CHANTEAU, DORVILLE @PF PHILIPS, C	04-500000	US 5920311	1997-01-14	US 5920311	1997-01-14	04-500000
D 095117	28-04-95	05-Discretes and Multimatx	JOHANNES, RABOVSKY @PHILIPS, C	05-000000	US 5920312	1997-01-14	US 5920312	1997-01-14	05-000000
A 023043	25-04-95	05-Mobile Communication	SEON SUP KIM, 16683 @PF PHILIPS, C	05-000000	US 5920313	1997-01-14	US 5920313	1997-01-14	05-000000
N 015512	20-04-95	01-CMOS and embedded m	WIEBE, DE BDER @PHILIPS, C	01-000000	US 5920314	1997-01-14	US 5920314	1997-01-14	01-000000
N 015527	20-04-95	02-Packaging & Testing	MATHIAS MURIS, 08022 @PF PHILIPS, C	02-000000	US 5920315	1997-01-14	US 5920315	1997-01-14	02-000000
D 095111	17-04-95	04-Audio/Video	LEO RUTENBURG, 2 @PHILIPS, C	04-500000	US 5920316	1997-01-14	US 5920316	1997-01-14	04-500000
A 050319	13-04-95	01-CMOS and embedded m	JEAN FERNANDEZ, 21313 @PF PHILIPS, C	01-000000	US 5920317	1997-01-14	US 5920317	1997-01-14	01-000000
A 001294	13-04-95	05-Mobile Communication	NASHOLLAH NAVID, 08007 @PF PHILIPS, C	05-000000	US 5920318	1997-01-14	US 5920318	1997-01-14	05-000000
A 080577	13-04-95	05-Mobile Communication	DAVE EVOY @PHILIPS, C	05-000000	US 5920319	1997-01-14	US 5920319	1997-01-14	05-000000
A 050396	13-04-95	05-Mobile Communication	SOEL VANDERTONG @PHILIPS, C	05-000000	US 5920320	1997-01-14	US 5920320	1997-01-14	05-000000
A 080516	12-04-95	02-Packaging & Testing	SAM MCCRACKEN, 21344 @PF PHILIPS, C	02-000000	US 5920321	1997-01-14	US 5920321	1997-01-14	02-000000
A 080516	12-04-95	05-Mobile Communication	DAVE EVOY @PHILIPS, C	05-000000	US 5920322	1997-01-14	US 5920322	1997-01-14	05-000000
A 001291	8-04-95	09-Discretes and Multimatx	FAROOQ BEHBAHANI, 16167 @PF PHILIPS, C	09-000000	US 5920323	1997-01-14	US 5920323	1997-01-14	09-000000
F 095365	29-04-95	05-Mobile Communication	FRANS, SCHOFFERS @PHILIPS, C	05-000000	US 5920324	1997-01-14	US 5920324	1997-01-14	05-000000
A 050482	29-04-95	07-Identification	NEIL SHEA, 21433 @PF PHILIPS, C	07-000000	US 5920325	1997-01-14	US 5920325	1997-01-14	07-000000
A 023036	27-04-95	04-Audio/Video	MICHAEL ANG, 15560 @PF PHILIPS, C	04-500000	US 5920326	1997-01-14	US 5920326	1997-01-14	04-500000
N 015470	25-04-95	05-Mobile Communication	BRAMMELSE, 06608 @PF PHILIPS, C	05-000000	US 5920327	1997-01-14	US 5920327	1997-01-14	05-000000
A 050420	22-04-95	06-Connectivity	HUZFA, OUTLERYWALA, 21175 @PF PHILIPS, C	06-000000	US 5920328	1997-01-14	US 5920328	1997-01-14	06-000000
N 015453	18-04-95	09-Discretes and Multimatx	GREGG LAHTI, 21308 @PF PHILIPS, C	09-000000	US 5920329	1997-01-14	US 5920329	1997-01-14	09-000000
A 050469	14-04-95	01-CMOS and embedded m	FREK, HUISMAN, 0388 @PF PHILIPS, C	01-000000	US 5920330	1997-01-14	US 5920330	1997-01-14	01-000000
A 050383	8-04-95	05-Mobile Communication	JEAN FERNANDEZ, 21113 @PF PHILIPS, C	05-000000	US 5920331	1997-01-14	US 5920331	1997-01-14	05-000000
N 015509	8-04-95	09-Discretes and Multimatx	MICHAEL CHENG, 21160 @PF PHILIPS, C	09-000000	US 5920332	1997-01-14	US 5920332	1997-01-14	09-000000
N 015981	8-04-95	09-Discretes and Multimatx	PHILIPPE, SILVESTRE, 21439 @PF PHILIPS, C	09-000000	US 5920333	1997-01-14	US 5920333	1997-01-14	09-000000
N 015444	7-04-95	05-Mobile Communication	PIETER VORENKAMP, 05683 @PF PHILIPS, C	05-000000	US 5920334	1997-01-14	US 5920334	1997-01-14	05-000000
A 023009	1-04-95	04-Audio/Video	RUDO, PLESSCHER, 02295 @PF PHILIPS, C	04-500000	US 5920335	1997-01-14	US 5920335	1997-01-14	04-500000
A 023038	1-04-95	04-Audio/Video	MARCEL PLEHROM @PHILIPS, C	04-500000	US 5920336	1997-01-14	US 5920336	1997-01-14	04-500000
N 015450	31-03-95	04-Audio/Video	GERRIT SLAVENBURG @PHILIPS, C	04-500000	US 5920337	1997-01-14	US 5920337	1997-01-14	04-500000
N 015428	28-03-95	09-Discretes and Multimatx	GERRIT SLAVENBURG @PHILIPS, C	09-000000	US 5920338	1997-01-14	US 5920338	1997-01-14	09-000000
C 095017	26-03-95	05-Mobile Communication	PIETER VORENKAMP, 05683 @PF PHILIPS, C	05-000000	US 5920339	1997-01-14	US 5920339	1997-01-14	05-000000
D 095085	24-03-95	04-Audio/Video	LEO WARMDAM @PHILIPS, C	04-500000	US 5920340	1997-01-14	US 5920340	1997-01-14	04-500000
A 001277	23-03-95	05-Mobile Communication	VIKTOR, GORNSTEIN @PHILIPS, C	05-000000	US 5920341	1997-01-14	US 5920341	1997-01-14	05-000000
B 030308	18-03-95	08-Identification	DOMINIK BERGER, 16638 @PF PHILIPS, C	08-000000	US 5920342	1997-01-14	US 5920342	1997-01-14	08-000000
B 030306	4-03-95	09-Discretes and Multimatx	WALLACE, KOU, 24303 @PF PHILIPS, C	09-000000	US 5920343	1997-01-14	US 5920343	1997-01-14	09-000000
A 050441	1-03-95	04-Audio/Video	HOLSER, GEHRT @PHILIPS, C	04-500000	US 5920344	1997-01-14	US 5920344	1997-01-14	04-500000
C 095020	1-03-95	07-Identification	NASHOLLAH NAVID, 08007 @PF PHILIPS, C	07-000000	US 5920345	1997-01-14	US 5920345	1997-01-14	07-000000
F 095073	27-02-95	02-Packaging & Testing	MARKUS PRANCO, 16629 @PF PHILIPS, C	02-000000	US 5920346	1997-01-14	US 5920346	1997-01-14	02-000000
N 015533	27-02-95	02-Packaging & Testing	HEIKO BACKER, 16296 @PF PHILIPS, C	02-000000	US 5920347	1997-01-14	US 5920347	1997-01-14	02-000000
N 015397	21-02-95	04-Audio/Video	WOLFGANG BAREITHER, 15931 @PF PHILIPS, C	04-500000	US 5920348	1997-01-14	US 5920348	1997-01-14	04-500000
A 050489	20-02-95	05-Mobile Communication	FONS DE LANGE @PHILIPS, C	05-000000	US 5920349	1997-01-14	US 5920349	1997-01-14	05-000000
A 050513	19-02-95	02-Packaging & Testing	DANIEL JANSEN, 01370 @PF PHILIPS, C	02-000000	US 5920350	1997-01-14	US 5920350	1997-01-14	02-000000
A 050536	19-02-95	05-Mobile Communication	CRAIG STAAB, 21481 @PF PHILIPS, C	05-000000	US 5920351	1997-01-14	US 5920351	1997-01-14	05-000000
US 590665	15-02-95	01-CMOS and embedded m	DAVE EVOY @PHILIPS, C	01-000000	US 5920352	1997-01-14	US 5920352	1997-01-14	01-000000
US 590666	10-02-95	04-Audio/Video	SUBHAS BROTHER, 21133 @PF PHILIPS, C	04-500000	US 5920353	1997-01-14	US 5920353	1997-01-14	04-500000
US 590667	7-02-95	09-Discretes and Multimatx	KARL BUCKENMAYER, 16489 @PF PHILIPS, C	09-000000	US 5920354	1997-01-14	US 5920354	1997-01-14	09-000000
US 590668	7-02-95	04-Audio/Video	HEINRICH BARTHEL, 21334 @PF PHILIPS, C	04-500000	US 5920355	1997-01-14	US 5920355	1997-01-14	04-500000
US 590669	7-02-95	02-Packaging & Testing	LOUIS PANDULA, 21384 @PF PHILIPS, C	02-000000	US 5920356	1997-01-14	US 5920356	1997-01-14	02-000000
US 590670	29-01-95	04-Audio/Video	HERBERT HEINEMANN, 08312 @PF PHILIPS, C	04-500000	US 5920357	1997-01-14	US 5920357	1997-01-14	04-500000
US 590671	27-01-95	04-Audio/Video	MANDU SACHDEV, 14350 @PF PHILIPS, C	04-500000	US 5920358	1997-01-14	US 5920358	1997-01-14	04-500000
US 590672	26-01-95	06-Connectivity	GERRIT SLAVENBURG @PHILIPS, C	06-000000	US 5920359	1997-01-14	US 5920359	1997-01-14	06-000000
US 590673	26-01-95	06-Connectivity	STEFAN NÖTJE @PHILIPS, C	06-000000	US 5920360	1997-01-14	US 5920360	1997-01-14	06-000000
US 590674	21-01-95	06-Connectivity	RALPH HERON, 21239 @PF PHILIPS, C	06-000000	US 5920361	1997-01-14	US 5920361	1997-01-14	06-000000
US 590675	20-01-95	02-Packaging & Testing	IAN CLARKE, 15069 @PF PHILIPS, C	02-000000	US 5920362	1997-01-14	US 5920362	1997-01-14	02-000000
US 590676	14-01-95	04-Audio/Video	KAMRAN JAMAL, 21272 @PF PHILIPS, C	04-500000	US 5920363	1997-01-14	US 5920363	1997-01-14	04-500000
US 590677	12-01-95	01-CMOS and embedded m	PHILIPPE KLAEYER, 15694 @PF PHILIPS, C	01-000000	US 5920364	1997-01-14	US 5920364	1997-01-14	01-000000
US 590678	9-01-95	10-Other	LIY DING, 21198 @PF PHILIPS, C	10-000000	US 5920365	1997-01-14	US 5920365	1997-01-14	10-000000
US 590679	8-01-95	06-Connectivity	LÖWME, GÖFF, 21237 @PF PHILIPS, C	06-000000	US 5920366	1997-01-14	US 5920366	1997-01-14	06-000000
US 590680	6-01-95	01-CMOS and embedded m	HARALD EISELE, 10273 @PF PHILIPS, C	01-000000	US 5920367	1997-01-14	US 5920367	1997-01-14	01-000000
US 590681	6-01-95	06-Connectivity	FELIX FUJISHIRO, 21211 @PF PHILIPS, C	06-000000	US 5920368	1997-01-14	US 5920368	1997-01-14	06-000000
US 590682	5-01-95	06-Connectivity	MICHAEL CREWS, 21170 @PF PHILIPS, C	06-000000	US 5920369	1997-01-14	US 5920369	1997-01-14	06-000000

SIEMENS AKTIENGESELLSCHAFT



No.	Inventor(s)	Title	IPC Class	Pub No.	Pub Date	App No.	App Date	App Inventor(s)	App Title	App IPC Class	App Pub No.	App Pub Date	App Inventor(s)	App Title	App IPC Class	App Pub No.	App Pub Date	App Inventor(s)	App Title	App IPC Class	App Pub No.	App Pub Date			
D 095050	JOACHIM BRILKA@PHILIPS.COM	OSCILLATOR FOR USE ON IC	04-Audio/Video		18520244.9			JOACHIM BRILKA@PHILIPS.COM	OSCILLATOR FOR USE ON IC	H03B	18520244.9		JOACHIM BRILKA@PHILIPS.COM	OSCILLATOR FOR USE ON IC	H03B	18520244.9		JOACHIM BRILKA@PHILIPS.COM	OSCILLATOR FOR USE ON IC	H03B	18520244.9				
C 095513	DOMINIK BERGER, 16638@PF.PHILIPS.CO	COMBINED CONTACTLESS CHIPCARD	07-Identification	2-Jun-95				DOMINIK BERGER, 16638@PF.PHILIPS.CO	COMBINED CONTACTLESS CHIPCARD	G06K	18501690.63		DOMINIK BERGER, 16638@PF.PHILIPS.CO	COMBINED CONTACTLESS CHIPCARD	G06K	18501690.63		DOMINIK BERGER, 16638@PF.PHILIPS.CO	COMBINED CONTACTLESS CHIPCARD	G06K	18501690.63				
N 015340	MARCEL VAN DE WETERLO@PHILIPS.COM	CATY MODULE BIAS VIA INPUT TRANSFORMER	06-Connectivity	31-mar-95				MARCEL VAN DE WETERLO@PHILIPS.COM	CATY MODULE BIAS VIA INPUT TRANSFORMER	H01L	18500908.05		MARCEL VAN DE WETERLO@PHILIPS.COM	CATY MODULE BIAS VIA INPUT TRANSFORMER	H01L	18500908.05		MARCEL VAN DE WETERLO@PHILIPS.COM	CATY MODULE BIAS VIA INPUT TRANSFORMER	H01L	18500908.05				
Q 095512	GERALD HOLMFG, 16616@PF.PHILIPS.CO	POWER SUPPLY CIRCUIT FOR CONTACTLESS CARD	07-Identification	31-mar-95				GERALD HOLMFG, 16616@PF.PHILIPS.CO	POWER SUPPLY CIRCUIT FOR CONTACTLESS CARD	H01L	18500908.05		GERALD HOLMFG, 16616@PF.PHILIPS.CO	POWER SUPPLY CIRCUIT FOR CONTACTLESS CARD	H01L	18500908.05		GERALD HOLMFG, 16616@PF.PHILIPS.CO	POWER SUPPLY CIRCUIT FOR CONTACTLESS CARD	H01L	18500908.05				
A 0950562	CHRISTOPHER ARCUS, 21111@PF.PHILIPS.CO	LOW NOISE, LOW VOLTAGE PHASE LOCK LOOP	05-Mobile Communication	17-mar-95				CHRISTOPHER ARCUS, 21111@PF.PHILIPS.CO	LOW NOISE, LOW VOLTAGE PHASE LOCK LOOP	H03H	18500103.11		CHRISTOPHER ARCUS, 21111@PF.PHILIPS.CO	LOW NOISE, LOW VOLTAGE PHASE LOCK LOOP	H03H	18500103.11		CHRISTOPHER ARCUS, 21111@PF.PHILIPS.CO	LOW NOISE, LOW VOLTAGE PHASE LOCK LOOP	H03H	18500103.11				
N 015307	KLAAS SANDERS, 14828@PF.PHILIPS.CO	SMD DIODE PACKAGE WITH CONDUCTIVE STRIPS	02-Packaging & Testing	12-mar-95				KLAAS SANDERS, 14828@PF.PHILIPS.CO	SMD DIODE PACKAGE WITH CONDUCTIVE STRIPS	H01L	18500380.15		KLAAS SANDERS, 14828@PF.PHILIPS.CO	SMD DIODE PACKAGE WITH CONDUCTIVE STRIPS	H01L	18500380.15		KLAAS SANDERS, 14828@PF.PHILIPS.CO	SMD DIODE PACKAGE WITH CONDUCTIVE STRIPS	H01L	18500380.15				
N 015302	ERIC KARLHAIN@PHILIPS.COM	RGB SCAN CONVERTER	04-Audio/Video	12-mar-95				ERIC KARLHAIN@PHILIPS.COM	RGB SCAN CONVERTER	H03C	18500380.15		ERIC KARLHAIN@PHILIPS.COM	RGB SCAN CONVERTER	H03C	18500380.15		ERIC KARLHAIN@PHILIPS.COM	RGB SCAN CONVERTER	H03C	18500380.15				
A 001217	HARRIE MAAS@PHILIPS.COM	MAKING S.O.A. DEVICES WITH THROUGH HOLES	10-Other	9-mar-95				HARRIE MAAS@PHILIPS.COM	MAKING S.O.A. DEVICES WITH THROUGH HOLES	B22D	18500380.15		HARRIE MAAS@PHILIPS.COM	MAKING S.O.A. DEVICES WITH THROUGH HOLES	B22D	18500380.15		HARRIE MAAS@PHILIPS.COM	MAKING S.O.A. DEVICES WITH THROUGH HOLES	B22D	18500380.15				
A 050437	RENDOCKSER, 21187@PF.PHILIPS.COM	FEAR DETECTOR WITH IMPROVED SPEED	04-Discretes and Multimeters	4-mar-95				RENDOCKSER, 21187@PF.PHILIPS.COM	FEAR DETECTOR WITH IMPROVED SPEED	G06M	18500380.15		RENDOCKSER, 21187@PF.PHILIPS.COM	FEAR DETECTOR WITH IMPROVED SPEED	G06M	18500380.15		RENDOCKSER, 21187@PF.PHILIPS.COM	FEAR DETECTOR WITH IMPROVED SPEED	G06M	18500380.15				
B 033982	SIMON BRAMWELL@PHILIPS.COM	DYNAMIC SWITCHING OF DATA SLICING ALGORITHM	04-Audio/Video	22-apr-95				SIMON BRAMWELL@PHILIPS.COM	DYNAMIC SWITCHING OF DATA SLICING ALGORITHM	H03C	18500380.15		SIMON BRAMWELL@PHILIPS.COM	DYNAMIC SWITCHING OF DATA SLICING ALGORITHM	H03C	18500380.15		SIMON BRAMWELL@PHILIPS.COM	DYNAMIC SWITCHING OF DATA SLICING ALGORITHM	H03C	18500380.15				
F 095520	YVES DUFOUR, 06670@PF.PHILIPS.CO	LOW VOLTAGE LOGIC CELL	08-Discretes and Multimeters	10-apr-95				YVES DUFOUR, 06670@PF.PHILIPS.CO	LOW VOLTAGE LOGIC CELL	H03C	18500380.15		YVES DUFOUR, 06670@PF.PHILIPS.CO	LOW VOLTAGE LOGIC CELL	H03C	18500380.15		YVES DUFOUR, 06670@PF.PHILIPS.CO	LOW VOLTAGE LOGIC CELL	H03C	18500380.15				
A 050470	ROBERT FRONEN, 06584@PF.PHILIPS.CO	FAST LEVEL-SHIFTER FOR HIGH SIDE DRIVER	04-Discretes and Multimeters	6-apr-95				ROBERT FRONEN, 06584@PF.PHILIPS.CO	FAST LEVEL-SHIFTER FOR HIGH SIDE DRIVER	H03C	18500380.15		ROBERT FRONEN, 06584@PF.PHILIPS.CO	FAST LEVEL-SHIFTER FOR HIGH SIDE DRIVER	H03C	18500380.15		ROBERT FRONEN, 06584@PF.PHILIPS.CO	FAST LEVEL-SHIFTER FOR HIGH SIDE DRIVER	H03C	18500380.15				
A 052175	FR CHRISTOPHE GAUTHRON, 21229@PF.PHILIPS.CO	METHOD FOR ELIMINATING FALSE CRITICAL PATH IN A LOGIC CIRCUIT	01-CMOS and embedded	3-apr-95				FR CHRISTOPHE GAUTHRON, 21229@PF.PHILIPS.CO	METHOD FOR ELIMINATING FALSE CRITICAL PATH IN A LOGIC CIRCUIT	G06F	18500380.15		FR CHRISTOPHE GAUTHRON, 21229@PF.PHILIPS.CO	METHOD FOR ELIMINATING FALSE CRITICAL PATH IN A LOGIC CIRCUIT	G06F	18500380.15		FR CHRISTOPHE GAUTHRON, 21229@PF.PHILIPS.CO	METHOD FOR ELIMINATING FALSE CRITICAL PATH IN A LOGIC CIRCUIT	G06F	18500380.15				
N 015328	FR YING LOH, 21328@PF.PHILIPS.COM	PHASE LOCK LOOP WITH IMPROVED SPEED	01-CMOS and embedded	13-mar-95				FR YING LOH, 21328@PF.PHILIPS.COM	PHASE LOCK LOOP WITH IMPROVED SPEED	H03C	18500380.15		FR YING LOH, 21328@PF.PHILIPS.COM	PHASE LOCK LOOP WITH IMPROVED SPEED	H03C	18500380.15		FR YING LOH, 21328@PF.PHILIPS.COM	PHASE LOCK LOOP WITH IMPROVED SPEED	H03C	18500380.15				
N 015342	EDUARD STIKVOORT@PHILIPS.COM	SIGNAL DELTA MODULATOR WITH POLYPHASE FILTER	05-Mobile Communication	27-feb-95				EDUARD STIKVOORT@PHILIPS.COM	SIGNAL DELTA MODULATOR WITH POLYPHASE FILTER	H03H	18500380.15		EDUARD STIKVOORT@PHILIPS.COM	SIGNAL DELTA MODULATOR WITH POLYPHASE FILTER	H03H	18500380.15		EDUARD STIKVOORT@PHILIPS.COM	SIGNAL DELTA MODULATOR WITH POLYPHASE FILTER	H03H	18500380.15				
F 095507	A. M. NELLISSE@PHILIPS.COM	SHADOWED DEPOSITION OF MULTILAYER COMPONENTS	07-Identification	22-feb-95				A. M. NELLISSE@PHILIPS.COM	SHADOWED DEPOSITION OF MULTILAYER COMPONENTS	B22D	18500380.15		A. M. NELLISSE@PHILIPS.COM	SHADOWED DEPOSITION OF MULTILAYER COMPONENTS	B22D	18500380.15		A. M. NELLISSE@PHILIPS.COM	SHADOWED DEPOSITION OF MULTILAYER COMPONENTS	B22D	18500380.15				
A 052184	CHARLES LINSLEY, 21319@PF.PHILIPS.CO	SLIP BUFFER FOR SYNCHRONIZING DATA TRANSFER BETWEEN T AND D	09-Discretes and Multimeters	17-feb-95				CHARLES LINSLEY, 21319@PF.PHILIPS.CO	SLIP BUFFER FOR SYNCHRONIZING DATA TRANSFER BETWEEN T AND D	H03C	18500380.15		CHARLES LINSLEY, 21319@PF.PHILIPS.CO	SLIP BUFFER FOR SYNCHRONIZING DATA TRANSFER BETWEEN T AND D	H03C	18500380.15		CHARLES LINSLEY, 21319@PF.PHILIPS.CO	SLIP BUFFER FOR SYNCHRONIZING DATA TRANSFER BETWEEN T AND D	H03C	18500380.15				
B 032972	DIODE USED AS NEGATIVE CAPACITANCE	04-Discretes and Multimeters	16-feb-95					DIODE USED AS NEGATIVE CAPACITANCE	H03C	18500380.15		DIODE USED AS NEGATIVE CAPACITANCE	H03C	18500380.15		DIODE USED AS NEGATIVE CAPACITANCE		DIODE USED AS NEGATIVE CAPACITANCE	H03C	18500380.15					
A 051280	YANNICK DEVILLE, 06544@PF.PHILIPS.CO	MULTIFUNCTIONAL THIN-FILM MEMORY	05-Mobile Communication	16-feb-95				YANNICK DEVILLE, 06544@PF.PHILIPS.CO	MULTIFUNCTIONAL THIN-FILM MEMORY	H03C	18500380.15		YANNICK DEVILLE, 06544@PF.PHILIPS.CO	MULTIFUNCTIONAL THIN-FILM MEMORY	H03C	18500380.15		YANNICK DEVILLE, 06544@PF.PHILIPS.CO	MULTIFUNCTIONAL THIN-FILM MEMORY	H03C	18500380.15				
D 095012	KEN DOCKSER, 21187@PF.PHILIPS.COM	MICROPROCESSOR WITH PROGRAMMABLE INSTRUCTION TRAINING	05-Mobile Communication	14-feb-95				KEN DOCKSER, 21187@PF.PHILIPS.COM	MICROPROCESSOR WITH PROGRAMMABLE INSTRUCTION TRAINING	G06F	18500380.15		KEN DOCKSER, 21187@PF.PHILIPS.COM	MICROPROCESSOR WITH PROGRAMMABLE INSTRUCTION TRAINING	G06F	18500380.15		KEN DOCKSER, 21187@PF.PHILIPS.COM	MICROPROCESSOR WITH PROGRAMMABLE INSTRUCTION TRAINING	G06F	18500380.15				
A 052211	LAURA SIMMONS, 21448@PF.PHILIPS.CO	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS	04-Audio/Video	13-feb-95				LAURA SIMMONS, 21448@PF.PHILIPS.CO	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS	H03C	18500380.15		LAURA SIMMONS, 21448@PF.PHILIPS.CO	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS	H03C	18500380.15		LAURA SIMMONS, 21448@PF.PHILIPS.CO	IMPROVEMENTS IN OR RELATING TO ZERO-IF RECEIVERS	H03C	18500380.15				
A 050495	HANS-JUERGEN KUEHNIG@PHILIPS.COM	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION	05-Mobile Communication	3-feb-95				HANS-JUERGEN KUEHNIG@PHILIPS.COM	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION	H03C	18500380.15		HANS-JUERGEN KUEHNIG@PHILIPS.COM	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION	H03C	18500380.15		HANS-JUERGEN KUEHNIG@PHILIPS.COM	METHOD AND APPARATUS FOR REDUCING POWER CONSUMPTION	H03C	18500380.15				
A 050486	DAVID CASSETTI, 21148@PF.PHILIPS.CO	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	05-Mobile Communication	3-feb-95				DAVID CASSETTI, 21148@PF.PHILIPS.CO	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15		DAVID CASSETTI, 21148@PF.PHILIPS.CO	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15		DAVID CASSETTI, 21148@PF.PHILIPS.CO	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15				
A 050487	DAVE EVOY@PHILIPS.COM	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	05-Mobile Communication	23-jan-95				DAVE EVOY@PHILIPS.COM	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15		DAVE EVOY@PHILIPS.COM	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15		DAVE EVOY@PHILIPS.COM	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15				
A 050506	DAVE EVOY@PHILIPS.COM	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	05-Mobile Communication	13-jan-95				DAVE EVOY@PHILIPS.COM	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15		DAVE EVOY@PHILIPS.COM	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15		DAVE EVOY@PHILIPS.COM	CONTROLLER FOR FREQUENCY ADJUSTABLE PLL CLOCK GENERATION FOR A PLL	H03C	18500380.15				
A 050427	CHRISTOPHER KINGSLEY, 21286@PF.PHILIPS.CO	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	02-Packaging & Testing	13-jan-95				CHRISTOPHER KINGSLEY, 21286@PF.PHILIPS.CO	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15		CHRISTOPHER KINGSLEY, 21286@PF.PHILIPS.CO	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15		CHRISTOPHER KINGSLEY, 21286@PF.PHILIPS.CO	APPARATUS FOR MONITORING DISTRIBUTED I/O DEVICE BY PROV	H03C	18500380.15				
A 050488	JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	07-Identification	12-jan-95				JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15		JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15		JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15				
A 050491	ANDRE ROSIERE, 21184@PF.PHILIPS.CO	ANTI-FUSE ROM PROGRAMMING CIRCUIT	05-Mobile Communication	6-jan-95				ANDRE ROSIERE, 21184@PF.PHILIPS.CO	ANTI-FUSE ROM PROGRAMMING CIRCUIT	H03C	18500380.15		ANDRE ROSIERE, 21184@PF.PHILIPS.CO	ANTI-FUSE ROM PROGRAMMING CIRCUIT	H03C	18500380.15		ANDRE ROSIERE, 21184@PF.PHILIPS.CO	ANTI-FUSE ROM PROGRAMMING CIRCUIT	H03C	18500380.15				
A 050525	RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	07-Identification	8-jan-95				RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15		RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15		RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15				
A 050452	RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	01-CMOS and embedded	30-dec-94				RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15		RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15		RICHARD TAKAHASHI, 21458@PF.PHILIPS.CO	DUAL PURPOSE SECURITY ARCHITECTURE WITH PROTECTED I/O	H03C	18500380.15				
A 050157	ATHAN SPYROU, 21448@PF.PHILIPS.CO	METHOD AND APPARATUS FOR DETERMINING THE TIMING SPECIF	02-Packaging & Testing	30-dec-94				ATHAN SPYROU, 21448@PF.PHILIPS.CO	METHOD AND APPARATUS FOR DETERMINING THE TIMING SPECIF	H03C	18500380.15		ATHAN SPYROU, 21448@PF.PHILIPS.CO	METHOD AND APPARATUS FOR DETERMINING THE TIMING SPECIF	H03C	18500380.15		ATHAN SPYROU, 21448@PF.PHILIPS.CO	METHOD AND APPARATUS FOR DETERMINING THE TIMING SPECIF	H03C	18500380.15				
A 050470	BILL SHU, 21436@PF.PHILIPS.CO	MULTI-PORT REGISTER FILE	04-Audio/Video	30-dec-94				BILL SHU, 21436@PF.PHILIPS.CO	MULTI-PORT REGISTER FILE	H03C	18500380.15		BILL SHU, 21436@PF.PHILIPS.CO	MULTI-PORT REGISTER FILE	H03C	18500380.15		BILL SHU, 21436@PF.PHILIPS.CO	MULTI-PORT REGISTER FILE	H03C	18500380.15				
A 050480	VIJAY MEHRA, 14098@PF.PHILIPS.CO	DYNAMIC SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	04-Audio/Video	30-dec-94				VIJAY MEHRA, 14098@PF.PHILIPS.CO	DYNAMIC SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15		VIJAY MEHRA, 14098@PF.PHILIPS.CO	DYNAMIC SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15		VIJAY MEHRA, 14098@PF.PHILIPS.CO	DYNAMIC SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15				
N 015146	JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	04-Audio/Video	23-dec-94				JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15		JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15		JOHN CALLAHAN, 21145@PF.PHILIPS.CO	TEMPERATURE MONITORING OF AN I/O	H03C	18500380.15				
A 050495	RONALD DEKKER@PHILIPS.COM	EXCEPTION RECOVERY IN DP SYSTEM	01-CMOS and embedded	22-dec-94				RONALD DEKKER@PHILIPS.COM	EXCEPTION RECOVERY IN DP SYSTEM	G06F	18500380.15		RONALD DEKKER@PHILIPS.COM	EXCEPTION RECOVERY IN DP SYSTEM	G06F	18500380.15		RONALD DEKKER@PHILIPS.COM	EXCEPTION RECOVERY IN DP SYSTEM	G06F	18500380.15				
050472	JOHN SHANNON@PHILIPS.COM	MIXING SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	01-CMOS and embedded	21-dec-94				JOHN SHANNON@PHILIPS.COM	MIXING SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15		JOHN SHANNON@PHILIPS.COM	MIXING SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15		JOHN SHANNON@PHILIPS.COM	MIXING SINGLE ENDED SENSE AMP IMPROVEMENT WITH CHARGE	H03C	18500380.15				
050435	JEAN-CHARLES GOMI, 21286@PF.PHILIPS.CO	WIREBOND LEAD SYSTEM WITH IMPROVED WIRE SEPARATION	01-CMOS and embedded	21-dec-94				JEAN-CHARLES GOMI, 21286@PF.PHILIPS.CO	WIREBOND LEAD SYSTEM WITH IMPROVED WIRE SEPARATION	H03C	18500380.15		JEAN-CHARLES GOMI, 21286@PF.PHILIPS.CO	WIREBOND LEAD SYSTEM WITH IMPROVED WIRE SEPARATION	H03C	18500380.15		JEAN-CHARLES GOMI, 21286@PF.PHILIPS.CO	WIREBOND LEAD SYSTEM WITH IMPROVED WIRE SEPARATION	H03C	18500380.15				
050436	RICHARD GROOVER, 21241@PF.PHILIPS.CO	MULTIPLICATION MULTIPLICATION-ACCUMULATION METHOD AND	02-Packaging & Testing																						

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Pub No.	App No.	Inventor	IPC Class	Pub No.	App No.	Inventor	IPC Class
N 015068	04-Audio/Video	RUDOLF PLASSCHKE	02-2120	US 2002/013886 A2	94203139 A	EP	02-2120
A 050331	02-Packaging & Testing	GEORGE FUJIMOTO	02-2120	US 6632263 B2	94203139 A	EP	02-2120
F 084952	05-Mobile Communication	PATRICK BAILEY	09-1418	US 6632263 B2	94203139 A	EP	09-1418
N 015049	01-CMOS and embedded pr	SUNIL KNOTTNER	211114	US 6420361 A1	54203611.5	FR	211114
A 050408	01-CMOS and embedded pr	MARTIN KNOTTNER	211114	US 6420361 A1	54203611.5	FR	211114
B 033930	09-Discretes and Multimat	BRENDAN KELLY	01-1444	US 6832404 B	0832404	US	01-1444
N 015053	05-Mobile Communication	HENDRIK BOEZEN	01-1444	US 6832404 B	0832404	US	01-1444
A 050479	01-CMOS and embedded pr	TIAO HUANG	212686	US 6832404 B	0832404	US	212686
A 050452	04-Audio/Video	KEN DOKKER	211876	US 64202925.7	94202925.7	EP	211876
N 015031	04-Audio/Video	SEDRIG KASPERKOWITZ	01-1481	US 6831631 B3	08316313	US	01-1481
A 050403	06-Connectivity	ART SOBEL	214444	US 6831631 B3	08316313	US	214444
A 052177	02-Packaging & Testing	AL PLATT	222650	US 6831631 B3	08316313	US	222650
A 050417	06-Connectivity	GREGG LAHTI	213000	US 6831631 B3	08316313	US	213000
A 001252	04-Audio/Video	GRI. MIZRAHI-SHALOM	158150	US 6831631 B3	08316313	US	158150
A 001253	04-Audio/Video	JOHN WANG	01-1503	US 6831631 B3	08316313	US	01-1503
A 001257	04-Audio/Video	THOMAS TRODDEN	158030	US 6831631 B3	08316313	US	158030
A 001251	09-Discretes and Multimat	FARELL OSTLER	038140	US 6831631 B3	08316313	US	038140
A 001258	09-Discretes and Multimat	GRI. MIZRAHI-SHALOM	158150	US 6831631 B3	08316313	US	158150
A 001261	09-Discretes and Multimat	ROY SANTANU	158110	US 6831631 B3	08316313	US	158110
A 001262	09-Discretes and Multimat	KEVIN SHOLANDER	158190	US 6831631 B3	08316313	US	158190
N 014998	04-Audio/Video	ROB WOULDSEMA	01-1444	US 6831631 B3	08316313	US	01-1444
F 094622	03-Mobile Communication	JEAN-SULLAUD	149907	US 6831631 B3	08316313	EP	149907
D 094110	3-sep-94	ERNST KILIAN	063988	US 6831631 B3	08316313	DE	063988
A 050454	04-Audio/Video	WALT POTTS	219399	US 6831631 B3	08316313	US	219399
A 050474	04-Audio/Video	DAVID CASSETTI	211448	US 6831631 B3	08316313	US	211448
N 014988	04-Audio/Video	DICK TEULUNG	01-1444	US 6831631 B3	08316313	US	01-1444
A 050438	04-Audio/Video	PIM WILUJIT	213242	US 6831631 B3	08316313	US	213242
A 021808	05-Mobile Communication	LEO WARMERDAM	01-1444	US 6831631 B3	08316313	US	01-1444
A 052173	01-CMOS and embedded pr	TIAO HUANG	212686	US 6831631 B3	08316313	US	212686
A 052176	01-CMOS and embedded pr	TIAO HUANG	212686	US 6831631 B3	08316313	US	212686
A 052193	01-CMOS and embedded pr	DIPANKAR PRAMANIK	215120	US 6831631 B3	08316313	US	215120
A 052200	05-Mobile Communication	FRIEDHELM HEINKE	084654	US 6831631 B3	08316313	US	084654
D 094039	09-Discretes and Multimat	SNAUT AELTWEINTE	NL	US 6831631 B3	08316313	DE	
N 014924	02-Packaging & Testing	ARNOLDUS ARNOUDUS	159626	US 6831631 B3	08316313	EP	159626
A 050429	02-Packaging & Testing	GARY SMALL	214427	US 6831631 B3	08316313	US	214427
A 050432	05-Mobile Communication	JOSE GARCIA	140650	US 6831631 B3	08316313	US	140650
A 050447	09-Identification	PAUL LEVY	213193	US 6831631 B3	08316313	US	213193
A 021892	09-Discretes and Multimat	SUNG-HUN CH	213756	US 6831631 B3	08316313	US	213756
D 094083	05-Mobile Communication	STEPHEN WONG	038200	US 6831631 B3	08316313	DE	038200
N 014921	05-Mobile Communication	PETER JOHNSON	184655	US 6831631 B3	08316313	EP	184655
033912	05-Mobile Communication	MANJIN KIM	038210	US 6831631 B3	08316313	US	038210
021880	17-Jun-94	IAN CLARKE	150666	US 6831631 B3	08316313	GB	150666
A 050426	04-Audio/Video	MICHAEL SCHMIDT	146532	US 6831631 B3	08316313	US	146532
N 014921	05-Mobile Communication	HOWARD PEIN	022399	US 6831631 B3	08316313	US	022399
021887	14-Jan-94	MANJIN KIM	038210	US 6831631 B3	08316313	US	038210
A 052180	10-Jun-94	SANG LEE	213006	US 6831631 B3	08316313	US	213006
A 050426	05-Mobile Communication	LOUIS PANDELL	01-1394	US 6831631 B3	08316313	US	01-1394
N 014968	05-Mobile Communication	KOEN APPELTANS	142888	US 6831631 B3	08316313	EP	142888
001250	24-mei-94	SCHUYLER SHIMANEK	152711	US 6831631 B3	08316313	EP	152711
014958	10-Jun-94	RONALD DEKKER	01-1444	US 6831631 B3	08316313	US	01-1444
001251	11-mei-94	OSMAN AKASU	211020	US 6831631 B3	08316313	US	211020
001252	6-mei-94	KUANG-YEH CHANG	211550	US 6831631 B3	08316313	US	211550
001253	4-mei-94	YVES DURFUR	085507	US 6831631 B3	08316313	US	085507
001254	21-apr-94	LEO RUTENBURG	20-01-1444	US 6831631 B3	08316313	US	20-01-1444
001255	15-apr-94	A.J.M. NELLISSEN	PHILIPS.COM	US 6831631 B3	08316313	US	PHILIPS.COM
001256	14-apr-94	JOACHIM BRILKA	PHILIPS.COM	US 6831631 B3	08316313	US	PHILIPS.COM
001257	8-apr-94	BRAM NELSE	066908	US 6831631 B3	08316313	US	066908
001258	7-apr-94	BILL SHU	214366	US 6831631 B3	08316313	US	214366
001259	31-mrt-94	YU-PIN HAN	212449	US 6831631 B3	08316313	US	212449
001260	31-mrt-94	KEN RAVINDRAN	221336	US 6831631 B3	08316313	US	221336
001261	31-mrt-94	LOUIS LIANG	213144	US 6831631 B3	08316313	US	213144
001262	25-mrt-94	BRIAN MARTIN	146940	US 6831631 B3	08316313	US	146940
001263	25-mrt-94	SANG LEE	213068	US 6831631 B3	08316313	US	213068
001264	25-mrt-94	TOM PARKINSON	151683	US 6831631 B3	08316313	US	151683



Patent No.	Inventor	IPC Class.	Title	Pub. No.	Pub. Date	Pub. Title	IPC Class.	Inventor
A 050406	TOM MASSINGILL, 21939@PF.PHILIPS.COM	02-Packaging & Testing	GRID ARRAY PACKAGE BASED ON WIRE US	08102630	08/10/2006	08102630	08102630	US5620080
N 014529	PETER KLAPPROTH@PHILIPS.COM	02-Packaging & Testing	SUPPORT FACILITIES FOR SPARC CHIP DEVELOPMENT	93202228	09/22/2006	93202228	93202228	US5620081
A 060286	YASUMASA KOSAKA, 21283@PF.PHILIPS.COM	09-Audio/Video	PAD STRUCTURE WITH PARASTIC MOS TRANSISTOR	08097417	08/09/2006	08097417	08097417	US5620082
N 014651	GRAFAM, THOMASON@PHILIPS.COM	07-Identification	CONTROLER WITH NON-UNIFORM RESOLUTION LUT	93202062	09/22/2006	93202062	93202062	US5620083
N 014652	WOLFGANG TOBERGTE@PHILIPS.COM	07-Identification	TERMINAL AND CARD SHARE POWER TRANSMISSION LOAD	P4323530	04/30/2006	P4323530	P4323530	US5620084
N 014653	JAN HAISMA, 01050@PF.PHILIPS.COM	09-Discretes and Multimar	DIRECT BONDING OF ORGANIC MATERIALS	93202150	09/22/2006	93202150	93202150	US5620085
N 014654	MANOJ SACHDEV, 14350@PF.PHILIPS.COM	02-Packaging & Testing	HIGH VOLTAGE MOST WITH STRIPED DRAIN EXTENSION	93202078	09/22/2006	93202078	93202078	US5620086
N 014655	MANOJ SACHDEV, 14350@PF.PHILIPS.COM	02-Packaging & Testing	COMBINATORIAL TESTING OF SEQUENTIAL CIRCUIT	93202027	09/22/2006	93202027	93202027	US5620087
B 033954	PAUL MOORE@PHILIPS.COM	06-Mobile Communication	TEMP. COMP. LOCO. AMPLIFIER	93200012	09/22/2006	93200012	93200012	US5620088
B 033955	PAUL MOORE@PHILIPS.COM	06-Mobile Communication	TEMP. COMP. LOCO. AMPLIFIER	93200012	09/22/2006	93200012	93200012	US5620089
F 082349	LAURA SIMMONS, 21440@PF.PHILIPS.COM	06-Connectivity	NEGATIVE GATE INPUT PROTECTION	93139402	09/13/2006	93139402	93139402	US5620090
F 083001	MARC BOURN, 14298@PF.PHILIPS.COM	07-Identification	STATUS REGISTER WITH ASYNCHRONOUS READ AND RESET	08106639	08/10/2006	08106639	08106639	US5620091
F 083002	CHRISTINUS VALBURG, 09015@PF.PHILIPS.COM	06-Discretes and Multimar	INTERFACE CIRCUIT FOR VOLTAGE BOOSTER CIRCUIT	93200004	09/22/2006	93200004	93200004	US5620092
A 050352	YOUNG LOH, 21228@PF.PHILIPS.COM	01-CMOS and embedded p	METHOD FOR CREATING SELF-ALIGNED BICMOS-C US	08081761	08/08/2006	08081761	08081761	US5620093
A 050360	JOHN LEAKIN, 21194@PF.PHILIPS.COM	01-CMOS and embedded p	METHOD FOR REDUCING RESISTANCE AT INTERFACE OF SINGLE US	08082119	08/08/2006	08082119	08082119	US5620094
A 050363	ED NOWAK, 21373@PF.PHILIPS.COM	01-CMOS and embedded p	A METHOD FOR SELF-ALIGNED PUNCHTHROUGH IMPLANT USING US	08082123	08/08/2006	08082123	08082123	US5620095
A 050364	AHMAD HAMZEHDOSTI, 21248@PF.PHILIPS.COM	02-Packaging & Testing	PLASTIC ENCAPSULATED INTEGRATED CIRCUIT PACKAGE HAVIN US	08082117	08/08/2006	08082117	08082117	US5620096
B 033956	PAUL LEVY, 21313@PF.PHILIPS.COM	04-Audio/Video	CONSOLE TEST APPARATUS FOR INTEGRATED CIRCUIT CHIPS	08082124	08/08/2006	08082124	08082124	US5620097
N 014657	CHUN JIANG, 21276@PF.PHILIPS.COM	09-Discretes and Multimar	AC DRAIN VOLTAGE CHARGING SOURCE FOR PROM DEVICES	9407443	09/07/2006	9407443	9407443	US5620098
N 014658	BRENDAN KELLY@PHILIPS.COM	09-Discretes and Multimar	REDUCTION OF TD AND TROFF IN LOW-SIDE SWITCH	93201794	09/22/2006	93201794	93201794	US5620099
N 014659	BRENDAN KELLY@PHILIPS.COM	09-Discretes and Multimar	CMOS OUTPUT STAGE: LOW GROUND BOUNCE	08078719	08/07/2006	08078719	08078719	US5620100
A 050356	ROBERT BLAUSCH, 00241@PF.PHILIPS.COM	01-CMOS and embedded p	LOGIC FAMILY WITH MIXED SINGLE-DRAWN ENDED GATES	08078529	08/07/2006	08078529	08078529	US5620101
A 050358	DAVID GAUPOL, 21228@PF.PHILIPS.COM	01-CMOS and embedded p	CMOS OUTPUT STAGE: LOW GROUND BOUNCE	08078529	08/07/2006	08078529	08078529	US5620102
A 050359	GERHARD WISCHERMANN, 10239@PF.PHILIPS.COM	01-CMOS and embedded p	CMOS OUTPUT STAGE: LOW GROUND BOUNCE	08078529	08/07/2006	08078529	08078529	US5620103
A 050361	DAVE EVOY@PHILIPS.COM	01-CMOS and embedded p	MULTIPLY ADDRESS DATA BUS WITH MULTIPLEX SYSTEM COMPT US	08078076	08/07/2006	08078076	08078076	US5620104
A 050362	YI-HEN WEI, 21486@PF.PHILIPS.COM	01-CMOS and embedded p	LATERALLY GRADED DEPOSIT-TYPE EMITTER FOR BIPOLAR TRAJ US	08072279	08/07/2006	08072279	08072279	US5620105
A 050368	VIVEK JAIN, 21271@PF.PHILIPS.COM	05-Mobile Communication	METHOD ENHANCING PLANARIZATION ETC BACK MARGIN, RELIA US	93201607	09/22/2006	93201607	93201607	US5620106
N 014661	JACOB MULDER, 02068@PF.PHILIPS.COM	09-Discretes and Multimar	ACCURATE INTEGRATED AMPLIFIER	93201586	09/22/2006	93201586	93201586	US5620107
N 014662	J.H.HULSHUIS@EWI.TUDELFT.NL	09-Discretes and Multimar	MIXED ANALOG/DIGITAL TWO-LINE BUS SYSTEM	93201584	09/22/2006	93201584	93201584	US5620108
N 014663	CHENG-TUNG CHIEN, 14337@PF.PHILIPS.COM	02-Packaging & Testing	POWER SAVING METHOD FOR HANDSETS	08066557	08/06/2006	08066557	08066557	US5620109
F 083540	NAI-CHIEE LIU, 01730@PF.PHILIPS.COM	02-Packaging & Testing	THREE-WAY SWITCHES ROUTING ANALOG TEST SIGNALS	93200896	09/22/2006	93200896	93200896	US5620110
F 083541	PATRYCE GAMAUND@PHILIPS.COM	05-Mobile Communication	FLIP-CHIP MIMC	9305897	09/05/2006	9305897	9305897	US5620111
F 083538	PATRYCE GAMAUND@PHILIPS.COM	05-Mobile Communication	FLIP-CHIP MIMC	9305897	09/05/2006	9305897	9305897	US5620112
D 083082	YANNICK DEVILLE, 08544@PF.PHILIPS.COM	04-Audio/Video	MIMC SEMICONDUCTOR DEVICE	9305716	09/05/2006	9305716	9305716	US5620113
D 083083	YANNICK DEVILLE, 08544@PF.PHILIPS.COM	04-Audio/Video	MIMC SEMICONDUCTOR DEVICE	9305716	09/05/2006	9305716	9305716	US5620114
D 083084	YANNICK DEVILLE, 08544@PF.PHILIPS.COM	04-Audio/Video	MIMC SEMICONDUCTOR DEVICE	9305716	09/05/2006	9305716	9305716	US5620115
D 083085	YANNICK DEVILLE, 08544@PF.PHILIPS.COM	04-Audio/Video	MIMC SEMICONDUCTOR DEVICE	9305716	09/05/2006	9305716	9305716	US5620116
A 021816	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	P4314424	04/30/2006	P4314424	P4314424	US5620117
F 083540	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620118
F 083541	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620119
F 083542	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620120
F 083543	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620121
F 083544	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620122
F 083545	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620123
F 083546	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620124
F 083547	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620125
F 083548	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620126
F 083549	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620127
F 083550	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620128
F 083551	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620129
F 083552	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620130
F 083553	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620131
F 083554	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620132
F 083555	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620133
F 083556	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620134
F 083557	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620135
F 083558	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620136
F 083559	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620137
F 083560	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620138
F 083561	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620139
F 083562	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620140
F 083563	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620141
F 083564	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620142
F 083565	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620143
F 083566	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620144
F 083567	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620145
F 083568	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620146
F 083569	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620147
F 083570	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620148
F 083571	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620149
F 083572	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620150
F 083573	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620151
F 083574	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620152
F 083575	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620153
F 083576	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620154
F 083577	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620155
F 083578	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620156
F 083579	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620157
F 083580	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620158
F 083581	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620159
F 083582	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620160
F 083583	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620161
F 083584	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620162
F 083585	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620163
F 083586	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	SOUND CARRIER FROM IF IN MULTI-STANDARD TV	93201046	09/22/2006	93201046	93201046	US5620164
F 083587	JOACHIM BRILKA@PHILIPS.COM	04-Discretes and Multimar	S					



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Main table with columns for Patent Number, Class, Inventor Name, Title, Date, and Status. Includes entries for Nico Benschop, Jean-Perraud, and others.

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Pub. No.	Pub. Date	App. No.	App. Date	Inventor	Assignor	IPC Class.	Abstract	Pub. No.	Pub. Date	App. No.	App. Date	Inventor	Assignor	IPC Class.	Abstract
B 053790	1-Jul-91	08-Discretes and Multimarks	BRENDAN KELLY @PHILIPS.COM	PHILIPS	G01R 31/00	POWER DEVICE WITH OVERVOLTAGE PROTECTION	9207869	US 5241534	1992/01/15/92-A1	9100398	1992/01/15/92-A1	9100398	PHILIPS	G01R 31/00	SYSTEMIC MULTIPLEXER/COMPENSATION MODULO M
N 013780	1-Jul-91	05-Mobile Communication	ROLF BECKER @PHILIPS.COM	PHILIPS	H04M 1/00	SWITCHED CAPACITOR ZERO CROSSING DETECTOR	91201679.7	US 5241535	1992/01/15/92-A1	9100334	1992/01/15/92-A1	9100334	PHILIPS	H04M 1/00	LINEAR ADJUSTABLE V-I CONVERTER IN MOS
A 050034	28-Jun-91	01-CMOS and embedded p	MARK HARTOOG, 21254 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	SCHLECHT METHOD FOR DETERMINING INSTANCE PLACEMENTS IN CIRCUIT	07723138	US 5241536	1992/01/15/92-A1	9100377	1992/01/15/92-A1	9100377	PHILIPS	H01L 23/30	CMOS DELAY CIRCUIT WITH CONTROLLABLE DELAY
A 050149	28-Jun-91	01-CMOS and embedded p	JUNJI ASHITANI, 21114 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	SYMBOLIC ROUTING GUIDANCE FOR WIRE NETWORK IN VLSI CIRCUIT	07723108	US 5241537	1992/01/15/92-A1	9100396	1992/01/15/92-A1	9100396	PHILIPS	H01L 23/30	SALICIDES AND STRAPS FROM AMORPHOUS ALLOYS
A 050139	28-Jun-91	02-Packaging & Testing	JOHN LONG, 211327 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	ELECTRONIC DEVICE PACKAGE - CARRIER ASSEMBLY READY TO USE	07723136	US 5241538	1992/01/15/92-A1	9100342	1992/01/15/92-A1	9100342	PHILIPS	H01L 23/30	BROADBAND TRANSFORMER WITH COMPENSATION WINDINGS
F 091155	28-Jun-91	04-Audio/Video	MARC DURANTON @PHILIPS.COM	PHILIPS	H03M 1/00	NON-EXECUTABLE INSTRUCTION ADDRESSES SUBPROGRAM	0100062	US 5241539	1992/01/15/92-A1	9100378	1992/01/15/92-A1	9100378	PHILIPS	H03M 1/00	METHOD AND APPARATUS FOR PROVIDING OUTPUT CONTENTORUS
A 050176	21-Jun-91	01-CMOS and embedded p	CHUN HUANG, 21294 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	METHOD OF PROVIDING POWER TO AN INTEGRATED CIRCUIT	0100062	US 5241540	1992/01/15/92-A1	9100379	1992/01/15/92-A1	9100379	PHILIPS	H01L 23/30	SEMI-CONDUCTOR CHIP PACKAGE WITH INTEGRATED CIRCUIT
N 013747	10-Jun-91	01-CMOS and embedded p	PAUL PLAS, 03284 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	SEALED POLY-SUFFERED LOGOS	0100062	US 5241541	1992/01/15/92-A1	9100380	1992/01/15/92-A1	9100380	PHILIPS	H01L 23/30	PROCESOR HAS FIFO BETWEEN ALV AND DECODER
F 091533	7-Jun-91	04-Audio/Video	MARC DURANTON @PHILIPS.COM	PHILIPS	H03M 1/00	DIGITAL OUTPUT BUFFER AND METHOD WITH SLEW RATE CONTROL	0100062	US 5241542	1992/01/15/92-A1	9100381	1992/01/15/92-A1	9100381	PHILIPS	H03M 1/00	DIGITAL OUTPUT BUFFER AND METHOD WITH SLEW RATE CONTROL
A 050042	3-Jun-91	06-Connectivity	HERMANN TENNENBOOM @PHILIPS.COM	PHILIPS	H04L 12/00	LOW BIAS VOLTAGE ACC AMPLIFIER	0100062	US 5241543	1992/01/15/92-A1	9100382	1992/01/15/92-A1	9100382	PHILIPS	H04L 12/00	EPROM WITH INCREASED CHANNEL DOPE ADJACENT DRAIN
A 050131	15-mel-91	01-CMOS and embedded p	JAMES CHEN, 00484 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	PROGRAMMABLE COMPANDOR	0100062	US 5241544	1992/01/15/92-A1	9100383	1992/01/15/92-A1	9100383	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050165	14-mel-91	07-Identification	MASROLLAH NAVID, 08007 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241545	1992/01/15/92-A1	9100384	1992/01/15/92-A1	9100384	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050145	3-mel-91	09-Discretes and Multimarks	KLAUS AXER, 09015 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241546	1992/01/15/92-A1	9100385	1992/01/15/92-A1	9100385	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050145	1-mel-91	02-Packaging & Testing	STEPHEN WONG, 03920 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241547	1992/01/15/92-A1	9100386	1992/01/15/92-A1	9100386	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050145	28-Apr-91	06-Connectivity	DAVID RACKLEY, 21403 @PF.PHILIPS.COM	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT	0100062	US 5241548	1992/01/15/92-A1	9100387	1992/01/15/92-A1	9100387	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT
A 050131	5-Apr-91	01-CMOS and embedded p	HERBERT KNEISS, 04891 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241549	1992/01/15/92-A1	9100388	1992/01/15/92-A1	9100388	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050080	4-Apr-91	01-CMOS and embedded p	ANDREW STACK, 21452 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241550	1992/01/15/92-A1	9100389	1992/01/15/92-A1	9100389	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050135	22-mel-91	01-CMOS and embedded p	TIM KISER, 21289 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241551	1992/01/15/92-A1	9100390	1992/01/15/92-A1	9100390	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
N 013673	20-mel-91	07-Identification	GARY GROSHONG, 21292 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241552	1992/01/15/92-A1	9100391	1992/01/15/92-A1	9100391	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050075	5-mel-91	09-Discretes and Multimarks	JOER KESSELS @PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241553	1992/01/15/92-A1	9100392	1992/01/15/92-A1	9100392	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
N 013690	5-mel-91	06-Connectivity	WIEL DE GUELLE @PHILIPS.COM	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT	0100062	US 5241554	1992/01/15/92-A1	9100393	1992/01/15/92-A1	9100393	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT
N 013673	5-mel-91	09-Discretes and Multimarks	EDDY HUANG, 21265 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241555	1992/01/15/92-A1	9100394	1992/01/15/92-A1	9100394	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
N 013690	26-Feb-91	01-CMOS and embedded p	JOHAN DUCHA TEAU, 03841 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241556	1992/01/15/92-A1	9100395	1992/01/15/92-A1	9100395	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050130	22-Feb-91	02-Packaging & Testing	MAARTEN ANDEL, 00049 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241557	1992/01/15/92-A1	9100396	1992/01/15/92-A1	9100396	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
N 013591	13-Feb-91	06-Audio/Video	WILLIAM HILTON, 21292 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241558	1992/01/15/92-A1	9100397	1992/01/15/92-A1	9100397	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050182	12-Feb-91	04-Connectivity	KEK CHUANG, 21152 @PF.PHILIPS.COM	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT	0100062	US 5241559	1992/01/15/92-A1	9100398	1992/01/15/92-A1	9100398	PHILIPS	H04L 12/00	RECTIFIER CIRCUIT
A 050176	5-Feb-91	09-Discretes and Multimarks	EDDY HUANG, 21265 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241560	1992/01/15/92-A1	9100399	1992/01/15/92-A1	9100399	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 021650	1-Feb-91	04-Audio/Video	WILLIAM HILTON, 21292 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241561	1992/01/15/92-A1	9100400	1992/01/15/92-A1	9100400	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050062	22-Jan-91	02-Packaging & Testing	STEVEN MERCHANT, 05092 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241562	1992/01/15/92-A1	9100401	1992/01/15/92-A1	9100401	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
O 091104	7-Jan-91	07-Identification	WILLIAM MARTIN, 21336 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241563	1992/01/15/92-A1	9100402	1992/01/15/92-A1	9100402	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050122	4-Dec-90	01-CMOS and embedded p	KLAUS AXER, 09015 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241564	1992/01/15/92-A1	9100403	1992/01/15/92-A1	9100403	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050095	21-Dec-90	04-Audio/Video	JERRY FRENKEL, 21218 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241565	1992/01/15/92-A1	9100404	1992/01/15/92-A1	9100404	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050073	20-Dec-90	04-Audio/Video	HAI PHUONG, 21390 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241566	1992/01/15/92-A1	9100405	1992/01/15/92-A1	9100405	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050120	19-Dec-90	01-CMOS and embedded p	MARGARET ARST, 08078 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241567	1992/01/15/92-A1	9100406	1992/01/15/92-A1	9100406	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050151	19-Dec-90	01-CMOS and embedded p	DALE WONG, 21481 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241568	1992/01/15/92-A1	9100407	1992/01/15/92-A1	9100407	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050171	19-Dec-90	02-Packaging & Testing	DAVID GANAPOL, 21226 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241569	1992/01/15/92-A1	9100408	1992/01/15/92-A1	9100408	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050103	18-Dec-90	09-Discretes and Multimarks	JOHN WEHLMACHER, 21405 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241570	1992/01/15/92-A1	9100409	1992/01/15/92-A1	9100409	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050092	18-Dec-90	09-Discretes and Multimarks	PATRICE MORLON, 21355 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241571	1992/01/15/92-A1	9100410	1992/01/15/92-A1	9100410	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050152	14-Dec-90	04-Audio/Video	KATHLEEN ARNOLD, 00064 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241572	1992/01/15/92-A1	9100411	1992/01/15/92-A1	9100411	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050118	14-Dec-90	04-Audio/Video	DALE WONG, 21481 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241573	1992/01/15/92-A1	9100412	1992/01/15/92-A1	9100412	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050208	5-Oct-90	04-Audio/Video	STEWART SMITH, 21445 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241574	1992/01/15/92-A1	9100413	1992/01/15/92-A1	9100413	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050537	4-Oct-90	02-Packaging & Testing	GERRIT SLAVENBURG @PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241575	1992/01/15/92-A1	9100414	1992/01/15/92-A1	9100414	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050367	24-Sep-90	09-Discretes and Multimarks	GERRIT SLAVENBURG @PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241576	1992/01/15/92-A1	9100415	1992/01/15/92-A1	9100415	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050174	22-Sep-90	09-Discretes and Multimarks	JOHN LONG, 21327 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241577	1992/01/15/92-A1	9100416	1992/01/15/92-A1	9100416	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050159	19-Sep-90	02-Packaging & Testing	JARED ZERBE, 21500 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241578	1992/01/15/92-A1	9100417	1992/01/15/92-A1	9100417	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050194	18-Sep-90	09-Discretes and Multimarks	MARTY LONG, 21329 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241579	1992/01/15/92-A1	9100418	1992/01/15/92-A1	9100418	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050194	14-Sep-90	09-Discretes and Multimarks	WILHELMUS JOSQUIN, 01428 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241580	1992/01/15/92-A1	9100419	1992/01/15/92-A1	9100419	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050152	5-Sep-90	03-Character	MICHAEL ALTHEIMER, 21107 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241581	1992/01/15/92-A1	9100420	1992/01/15/92-A1	9100420	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050108	5-Sep-90	04-Audio/Video	GERRIT SLAVENBURG @PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241582	1992/01/15/92-A1	9100421	1992/01/15/92-A1	9100421	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050090	23-Aug-90	07-Identification	ROLAND KOO, 16815 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241583	1992/01/15/92-A1	9100422	1992/01/15/92-A1	9100422	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT
A 050166	10-Aug-90	01-CMOS and embedded p	ROGER COOK, 21168 @PF.PHILIPS.COM	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT	0100062	US 5241584	1992/01/15/92-A1	9100423	1992/01/15/92-A1	9100423	PHILIPS	H01L 23/30	RECTIFIER CIRCUIT





Pub No	Inventor	Title	IPC Class	Pub No	Inventor	Title	IPC Class
N 012827	01-CMOS and embedded pr	LEO, ROOZENDAAAL, 03671@PF.PHILIPS.COM	G05F 1/02	NL	8900593		
N 012868	05-Mobile Communication	ANTONIUS MARINUS, 01902@PF.PHILIPS.COM	H04B 1/02	NL	8900809		
N 012869	01-CMOS and embedded pr	ADRIAN ALLENBAARS, 01735@PF.PHILIPS.COM	H01L 27/02	NL	8900460		
N 012747	04-Audio/Video	NOUD, SOUBEWJINS@PHILIPS.COM	H04B 7/02	NL	8900363		
N 012851	08-Discrete and Multimar	ANTOINE DELARUELLE@PHILIPS.COM	H03K 3/02	JP	89-33679		
A 001180	04-Audio/Video	ROEL OF SALTERS@PHILIPS.COM	H04B 7/02	EP	892009552, 8		
A 001181	08-Discrete and Multimar	J.H. HUIJISING@RWI.TUDELFT.NL	H03K 3/02	US	077301469		
A 050028	02-Packaging & Testing	MUHAMMAD SAMAD, 21419@PF.PHILIPS.COM	H01L 27/02	US	077305449		
N 012823	09-Discrete and Multimar	WALTER, GROENEVELD@PHILIPS.COM	H01L 27/02	NL	8900215		
N 012813	02-Packaging & Testing	MAX STAR, 02781@PF.PHILIPS.COM	H01L 27/02	NL	8900151		
A 050036	01-CMOS and embedded pr	OREGION, ASATO, 21113@PF.PHILIPS.COM	H01L 27/02	US	077297112		
N 012808	01-CMOS and embedded pr	CREGION, ASATO, 21113@PF.PHILIPS.COM	H01L 27/02	US	077297112		
A 012795	02-Packaging & Testing	BAS VERHELST, 07898@PF.PHILIPS.COM	H01L 27/02	US	077297057		
A 001177	01-CMOS and embedded pr	CORMAC, COONWELL, 005371@PF.PHILIPS.COM	H01L 27/02	NL	8900050		
N 012739	08-Discrete and Multimar	STEPHEN WONG, 03920@PF.PHILIPS.COM	H01L 27/02	NL	8900026		
A 050004	04-Audio/Video	JAMES, CHEN, 04498@PF.PHILIPS.COM	H04B 7/02	US	077286062		
D 088185	08-Discrete and Multimar	DOOK, LAMMERTS, 03699@PF.PHILIPS.COM	H03K 3/02	US	077283340		
A 001176	05-Mobile Communication	GEORG, KASPERKOVITZ, 01461@PF.PHILIPS.COM	H04B 1/02	NL	8902973		
N 012699	08-Discrete and Multimar	GEORGE, ARMSTRONG, 03908@PF.PHILIPS.COM	H03K 3/02	NL	8926538, 4		
A 021452	09-Discrete and Multimar	WAYNE, BURLESON, 21140@PF.PHILIPS.COM	H01L 27/02	DE	P 3801454, 1		
A 050020	04-Audio/Video	JAN, JONG, 01404@PF.PHILIPS.COM	H04B 7/02	US	077241528		
A 021447	09-Discrete and Multimar	DOOK, LAMMERTS, 03699@PF.PHILIPS.COM	H03K 3/02	US	077241528		
B 033477	09-Discrete and Multimar	RAJIV GUPTA, 049384@PF.PHILIPS.COM	H03K 3/02	NL	8801125		
N 012606	08-Discrete and Multimar	EDDY, HUANG, 21283@PF.PHILIPS.COM	H03K 3/02	NL	8802173		
Q 088019	05-Mobile Communication	JOSEPHUS LAARHOVEN, 03689@PF.PHILIPS.COM	H04B 1/02	US	077227376		
D 088121	01-CMOS and embedded pr	JOSEPHUS LAARHOVEN, 03689@PF.PHILIPS.COM	H01L 27/02	US	077227376		
N 012579	02-Packaging & Testing	IVON, RAALMAMERS, 03784@PF.PHILIPS.COM	H01L 27/02	NL	8801632		
N 012579	02-Packaging & Testing	HANS, GIERD, ALBERTSEN, 20177@PF.PHILIPS.COM	H01L 27/02	DE	P 38020728, 1		
Q 088016	01-CMOS and embedded pr	HANS, VOORMAN@UNIKOWN.NL	H01L 27/02	NL	8901469		
B 033360	04-Audio/Video	JOSEPHUS LAARHOVEN, 03689@PF.PHILIPS.COM	H04B 7/02	US	077177638		
B 033464	04-Audio/Video	HANS, VOORMAN@UNIKOWN.NL	H04B 7/02	US	077177638		
A 001172	04-Audio/Video	PHILIP, BIRD, 14768@PF.PHILIPS.COM	H04B 7/02	NL	8901432		
A 050023	04-Audio/Video	ROBERT, MEYER, 03611@PF.PHILIPS.COM	H04B 7/02	GB	8912958		
Q 088012	01-CMOS and embedded pr	DAVE CHAPMAN, 21157@PF.PHILIPS.COM	H01L 27/02	US	8812592, 7		
N 012514	09-Discrete and Multimar	MALCOLM, GRIFF, 01034@PF.PHILIPS.COM	H03K 3/02	US	077198950		
B 033443	09-Discrete and Multimar	ARTHUR, NOEBERCHTS, 01234@PF.PHILIPS.COM	H03K 3/02	US	077187499		
N 012514	09-Discrete and Multimar	STEVE, BUSH, 21141@PF.PHILIPS.COM	H03K 3/02	NL	8903901		
B 033443	09-Discrete and Multimar	CHARLES, DIKE, 00879@PF.PHILIPS.COM	H03K 3/02	NL	077176674		
N 012471	02-Packaging & Testing	BRAM, JONGEPIER@PHILIPS.COM	H01L 27/02	US	8986902		
N 012437	08-Discrete and Multimar	RUD, PLASSCHE, 02285@PF.PHILIPS.COM	H03K 3/02	NL	8800374		
N 012411	01-CMOS and embedded pr	JOHAN, SCHMITZ, 02987@PF.PHILIPS.COM	H01L 27/02	US	077156381		
Q 088018	01-CMOS and embedded pr	RUSSSELL, WANGER, 00796@PF.PHILIPS.COM	H01L 27/02	US	077155402		
N 012392	04-Audio/Video	LUDIK@NATLAB.RESEARCH.PHILIPS.COM	H04B 7/02	NL	8900221		
N 012392	04-Audio/Video	JAN, DIKKEW@PHILIPS.COM	H04B 7/02	NL	8900075		
N 012392	04-Audio/Video	ERNEST, FELLER, 00896@PF.PHILIPS.COM	H04B 7/02	NL	8716873		
N 012392	04-Audio/Video	LUDIK@NATLAB.RESEARCH.PHILIPS.COM	H04B 7/02	NL	8702847		
N 012392	04-Audio/Video	KEITH, SINGER, 02607@PF.PHILIPS.COM	H04B 7/02	NL	8702734		
N 012392	04-Audio/Video	PETER, G. BLANKEN@PHILIPS.COM	H04B 7/02	NL	8702671		
N 012392	04-Audio/Video	WIM, GOOTZEN, 01004@PF.PHILIPS.COM	H04B 7/02	US	077115478		
N 012392	04-Audio/Video	JAMES, CHEN, 00498@PF.PHILIPS.COM	H04B 7/02	NL	8702659		
N 012392	04-Audio/Video	YONGBUM, CHUYSAS, 21172@PF.PHILIPS.COM	H04B 7/02	US	077104187		
N 012392	04-Audio/Video	NEIL, BIRD@PHILIPS.COM	H04B 7/02	US	07703841		
N 012392	04-Audio/Video	NEIL, BIRD@PHILIPS.COM	H04B 7/02	US	8721756		
N 012392	04-Audio/Video	GEORG, KASPERKOVITZ, 01461@PF.PHILIPS.COM	H04B 7/02	GB	8721759		
N 012392	04-Audio/Video	KAH-SUNG, CHUNG, 00505@PF.PHILIPS.COM	H04B 7/02	EP	87201668, 8		
N 012392	04-Audio/Video	JANNET, BLASI, 00240@PF.PHILIPS.COM	H04B 7/02	EP	87201667		
N 012392	04-Audio/Video	DAN, HARTON, 03776@PF.PHILIPS.COM	H04B 7/02	US	077090301		
N 012392	04-Audio/Video	DAN, HARTON, 03776@PF.PHILIPS.COM	H04B 7/02	US	077098443		

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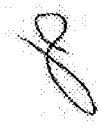
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HEWLETT PACKARD COMPANY

REF ID: A0439954

Patent No.	IPC Class	Inventor	Assignee	Date
B 093374	05-Mobile Communication	ANTONIE.MESSIOU.12097@PF.PHILIPS.COM	PHILIPS.COM	27-jun-87
A 601157	01-CMOS and embedded pr	ELWIK.DAVIS.04507@PF.PHILIPS.COM	PHILIPS.COM	20-jul-87
E 033373	04-Audio/Video	JOHN.KINGHORN@PHILIPS.COM	PHILIPS.COM	29-jul-87
D 087141	09-Discrete and Multimarke	HANS.KLOSE.00297@PF.PHILIPS.COM	PHILIPS.COM	18-jul-87
B 033372	04-Audio/Video	JOHN.KINGHORN@PHILIPS.COM	PHILIPS.COM	17-jul-87
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N 021256	04-Audio/Video	AND.SEMPEL@PHILIPS.COM	PHILIPS.COM	23-jun-87
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01-CMOS and embedded pr		GORDON.BURNS.06641@PF.PHILIPS.COM	PHILIPS.COM	15-jul-85
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01-CMOS and embedded pr		UNKNOWN.ORG	UNKNOWN.ORG	10-jul-70

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Patent No.	IPC Class	Inventor	Assignee	Date
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US 4966206	US	46775385	PHILIPS	1987
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US 4966208	DE	P3723918.8	PHILIPS	1987
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Addendum to Annex 1

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Patent No.	Priority	Portfolio Subsection	Final Inventor(s)	Patent Title	Patent Priority Appl. No.	IPC Class. Subclass.	US Patent No.	Other Patent No.
006848	1-sep-06	Mobile Communication	THOMAS REEDLE@PHILIPS.COM	Method to outsize 4H-SiC to introduce light invariance channel mobility SiC	EP 6118975.8			
006822	31-aug-06	Mobile Communication	TONY BAYERS@PHILIPS.COM	Correction of Linear Frequency Dependent Signal Path Errors	EP 6118949.3			
006818	25-aug-06	Mobile Communication	ALEXANDER DANILIN@PHILIPS.COM	ASTRA: Advanced Space-Time Reconfigurable Architecture	EP 6118507.8			
006797	22-aug-06	CMOS and embedded proces	PAUL WIELAGE@PHILIPS.COM	A Robust Method for Detecting Weak Static Random-Access Memory Cells	EP 6118335.5			
006747	18-aug-06	Connectivity	SHARAD MURARI@PHILIPS.COM	Low Pin Count External PCI Express PHY Interface	US 6088387.7			
0066521	14-aug-06	Mobile Communication	TONY KERSELAERS@PHILIPS.COM	System that provides relative constant radiated E-Field in a frequency band	EP 6118548.8			
0066524	14-aug-06	Mobile Communication	TONY KERSELAERS@PHILIPS.COM	Method to equalize the effective radiated power from a short antenna	EP 6118550.4			
006495	20-jul-06	RF Devices	J.W. WEEKAMP@PHILIPS.COM	Efficient Startup Protection for Communication Networks	EP 6117479.3			
006528	19-jul-06	Automotive	MANFRED ZINKE@PHILIPS.COM	Inductor-grounded power inverter for F-antenna	EP 6117319.1			
006303	17-jul-06	RF Devices	ZIDONG LIU@PHILIPS.COM	Decision-Directed NLMS Equalizer by Despread with a Parent Code	US 6088308.8			
005789	13-jul-06	Mobile Communication	AHMET BASTUG@PHILIPS.COM	The Sea of Fingers: design-aided method to manufacture FinFETs with steps	EP 6116368.6			
006126	4-jul-06	Mobile Communication	ANDY YUIE@PHILIPS.COM	Transmission of old GPS ephemeris to improve GPS receiver performance	EP 6116585.8			
006167	15-jun-06	Connectivity	STEVEN AERTS@PHILIPS.COM	Noise shaper for fins (processor) load/frequency control	EP 6115537			
006162	8-jun-06	Audio/Video	HENDRIEK BRODT-HULZE@PHILIPS.COM	Thermal isolation of electronic devices in submount used for LEDs lighting	EP 6114882.9			
004985	20-aug-06	Packaging & Testing	SILLES FERRU@PHILIPS.COM	Multi-bit Programmable Frequency Divider	EP 6300386.7			
001657	30-jun-05	Connectivity	WENYI SONG@PHILIPS.COM	Method to implement Pin diode components in a standard integrated pass	US 6306936.5			
US0550326	24-jun-05	CMOS and embedded proces	BERNARD Y@PHILIPS.COM	METHOD TO SYNCHRONIZE RECEIVER'S CLOCK TO ...	US 6076407.8			
US030427	24-mai-05	Discretes and Multimeter IC	HILBERT ZHANG@PHILIPS.COM	METHOD OF FABRICATING A SIGE SEMICONDUCTOR STRUCTURE	US 6057144.4			
US030291	26-aug-03	Packaging & Testing	PETER BEIKLER@PHILIPS.COM	PROPORTIONAL VARIABLE RESISTOR STRUCTURE TO ELECTRICAL US	US 6014979.0			
NL030659	12-jun-03	RF Devices	J. AMATO@PHILIPS.COM	SEAMLESS WITH HEATSINK	EP 3101729.6			
G8000026	9-mai-00	Other	ANDRE JANSMA@PHILIPS.COM	STATISTICAL DATA PRESENCE INDICATION CIRCUIT	GB 5581.3			
A 051054	24-mai-99	Other	DOMENICO FORCINO@PHILIPS.COM	JOYSTICK INTERFACES AND METHODS OF A PROVIDING POSITION	US 5931769.0			
N 015696	14-dec-95	Other	SCOTT HARROW 21253@PF.PHILIPS.COM	ESD - CLIP	EP 9524334.1			
B 034032	28-nov-95	Discretes and Multimeter IC	CHING WONG 03370@PF.PHILIPS.COM	CHANNEL LENGTH-WIDTH RATIO IN MOS TRANSISTORS	GB 9524334.1			
B 053818	28-feb-90	Mobile Communication	JOHN HUGHES@PHILIPS.COM	GPS LOCATION CENTRAL PROCESSING	GB 9004433			