

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4684305

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
CIRRUS LOGIC INTERNATIONAL SEMICONDUCTOR LTD.	06/05/2017
RECEIVING PARTY DATA	
Name:	CIRRUS LOGIC, INC.
Street Address:	800 WEST 6TH STREET
City:	AUSTIN
State/Country:	TEXAS
Postal Code:	78701
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15663411
CORRESPONDENCE DATA	
Fax Number:	(512)391-2173
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	512.236.2023
Email:	bprewitt@docket@jw.com
Correspondent Name:	JACKSON WALKER L.L.P.
Address Line 1:	100 CONGRESS AVENUE
Address Line 2:	SUITE 1100
Address Line 4:	AUSTIN, TEXAS 78701
ATTORNEY DOCKET NUMBER:	141841.00704-P1317US02
NAME OF SUBMITTER:	MINDY MAYER
SIGNATURE:	/mindy mayer/
DATE SIGNED:	11/10/2017
Total Attachments: 2	
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INTERCOMPANY ASSIGNMENT:
Transferring Certain Previously-Unassigned USA Patents

WHEREAS, Cirrus Logic, Inc. (hereafter "Cirrus"), a Delaware corporation having its principal place of business at 800 West Sixth Street, Austin, Texas 78701, United States of America, and Cirrus Logic International Semiconductor Ltd. (hereafter "CLIS"), a wholly-owned subsidiary of Cirrus organized under and pursuant to the laws of Scotland and having its principal place of business at 7B Nightingale Way, Quartermile, Edinburgh EH3 9EG, United Kingdom;

WHEREAS, this assignment is directed to USA patents and patent applications having a priority date of 29 March 2015 or earlier that were not previously assigned from CLIS to Cirrus; and

USA Assignment from CLIS to Cirrus, Conditional upon USA Grant:

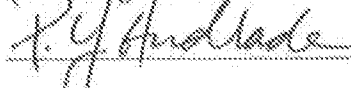
For good and valuable consideration, the receipt and sufficiency of which is acknowledged, and effective upon issuance of any patent in the United States of America from (a) the United States of America or Patent Cooperation Treaty (PCT) patent applications listed in Schedule A and any of their present or future United States of America continuations, continuations-in-part, divisionals, reissues, re-examinations, extensions or additions thereof and (b) any other present or future United States of America patent applications owned by CLIS that have an earliest priority date of 29 March 2015 or earlier (hereinafter, the issued patents of subparts (a) and (b) collectively referred to as the "USA Issued Patents"), CLIS hereby automatically sells, transfers, conveys, and assigns to Cirrus the entire right, title, and interest to the USA Issued Patents, including but not limited to: (i) all past, present, and future income, royalties, damages and payments due (including rights to damages and payments for past, present, or future infringements or misappropriations) associated with the USA Issued Patents and (ii) all rights to initiate and pursue legal and equitable remedies in any cause of action, administrative proceeding, or other procedure associated with the USA Issued Patents, including but not limited to any right to sue for infringement occurring before or after the assignment herein and any provisional right described in 35 U.S.C. §154(d) and any successor statute thereto.

If any provision of this assignment is invalid or incapable of being enforced, then such provision shall be excluded to the extent of such invalidity or unenforceability. All other provisions shall remain in full force and effect and, to the extent possible, the invalid or unenforceable provision shall be deemed replaced by a provision that is valid and enforceable and that comes closest to expressing the intention of such invalid or unenforceable provision.

Assignor:

Cirrus Logic International Semiconductor Ltd.

Signature for Cirrus Logic International Semiconductor Ltd.:



Name, Title:

Pedro Y. Andrade, Director

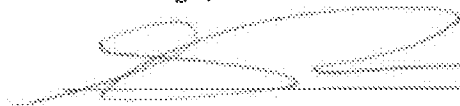
Date:

JUN 05 2017

Assignee:

Cirrus Logic, Inc.

Signature for Cirrus Logic, Inc.:



Name, Title:

Gregory Scott Thomas, VP, General Counsel & Secretary

Date:

6/5/17

Schedule A

#	Title	U.S. Serial Number	U.S. Publication
1	Digital/Analogue Conversion	14/437685	20150256925
2	Digital/Analogue Conversion	14/436648	2015/0263686
3	Detection Circuit	14/653661	2015/0358719
4	Signal Processing For MEMS Capacitive Transducers	14/787052	2016/0157017
5	Mems Device and Process	14/902641	2016/0167946
6	Activating Speech Process	15/105755	2016/0379635
7	Low Noise Amplifier for MEMS Capacitive Transducers	14/700666	2015/0318829
8	Temperature Monitoring for Loudspeakers	15/315845	
9	Packaging for MEMS Transducers	15/317004	
10	Integrated MEMS Transducer and Circuitry	15/329164	
11	Class D Amplifier Circuit	14/836006	2016/0065158
12	Analogue-to-Digital Converter	14/931332	2016/0126968
13	Analogue-to-Digital Converter	15/243305	2016/0359500
14	MEMS Devices and Processes	14/971226	2016/0176704
15	Digital Accessory Interface	14/928179	2017/0019244
16	Digital Accessory Interface Calibration	14/928295	2016/0154622
17	Voice Command Triggered Speech Enhancement	15/105882	2016/0322045