#### 12/18/2017 504693648

### PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

EPAS ID: PAT4740370

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
Chia-Ling Wang	12/18/2017
Ping-Hung Chiang	12/18/2017
Chang-Po Hsiung	12/18/2017
Chia-Wen Lu	12/18/2017
Nien-Chung Li	12/18/2017
Wen-Fang Lee	12/18/2017
Chih-Chung Wang	12/18/2017

#### RECEIVING PARTY DATA

Name:	UNITED MICROELECTRONICS CORP.
Street Address:	No.3, Li-Hsin Road 2, Science-Based Industrial Park
City:	Hsin-Chu City
State/Country:	TAIWAN

#### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	15846150

#### **CORRESPONDENCE DATA**

Fax Number: (703)997-4517

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 3027291562

Email: Patent.admin.uspto.cr@naipo.com

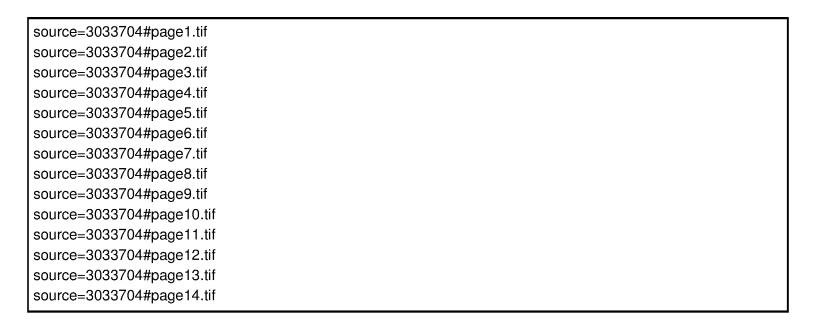
**Correspondent Name:** WINSTON HSU

Address Line 1: 5F., NO.389, FUHE RD., YONGHE DIST.,

Address Line 4: NEW TAIPEI CITY, TAIWAN

ATTORNEY DOCKET NUMBER:	NAUP3168USA
NAME OF SUBMITTER:	SIBYL YU
SIGNATURE:	/SIBYL YU/
DATE SIGNED:	12/18/2017

**Total Attachments: 14** 



#### Title of Invention:

#### TRANSISTOR WITH DUAL SPACER AND FORMING METHOD THEREOF

As the below named inventor, I here This declaration is directed to:	by declare that:			
☑ The attached application, or	Г			
☐ United States application nu	umber	filed c	n	, or
☐ PCT international application	n number	file	d on	
The above-identified application was	s made or authorized to b	e made by me.		
l believe that I am the original invent application.	tor or an original joint inve	entor of a claime	d invention in the	Ð
l hereby acknowledge that any willfu under18 U.S.C. 1001 by fine or impr				
In consideration of the payment by	UNITED MICROELE CORP.	CTRONICS	having a postal	l address of
No.3, Li-Hsin Road 2, Science	e-Based Industrial Pa	ark, Hsin-Chu	City 300, Tai	wan, R.O.C.
(referred to as "ASSIGNEE"below) to acknowledged, andfor other good ar	o I of the sum of One Dol nd valuable consideration	llar (\$ 1.00), the 1.	receipt of which	is hereby
hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica invention by the above application o substitutes, or extensions thereof, an	nd to any and all improve ation and, in and to, all Le r any continuations, conti	ements which are etters Patent to b inuation-in-part,	e disclosed in the e obtained for sa divisions, renewa	e aid als,
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I further covenant that ASSIGNEE wand documents relating to said invertions and accessible to I and will te related thereto and will promptly exe	ntion and said Letters Pa estify as to the same in a	tent and legal ed ny interference, l	uivalents as may itigation proceed	v be
representatives any and all papers, maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	plication, said invention a ecessary or desirable to d	nd said Letters F carry out the prop	Patent and said	te of signing)
Note: An application data sheet (PToinventive entity, must accompany the				

Page 1 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor:

Chia-Ling Wang

Date:

DEC 1 8 2017

Signature: Chia-Ling Wang

Page 2 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

#### Title of Invention:

### TRANSISTOR WITH DUAL SPACER AND FORMING METHOD THEREOF

As the below named inventor, I here This declaration is directed to:	by declare that:		
☑ The attached application, or	•		
☐ United States application nu	ımber	filed on	, or
☐ PCT international applicatio	n number	filed on	
The above-identified application was	s made or authorized to be made l	by me.	
I believe that I am the original invent application.	or or an original joint inventor of a	claimed invention i	n the
I hereby acknowledge that any willfu under18 U.S.C. 1001 by fine or impr	I false statement made in this decisionment of not more than five (5)	laration is punishab years, or both.	le
In consideration of the payment by	UNITED MICROELECTRON CORP.	IICS having a po	ostal address of
No.3, Li-Hsin Road 2, Science	-Based Industrial Park, Hsi	n-Chu City 300,	Taiwan, R.O.C
(referred to as "ASSIGNEE"below) to acknowledged, andfor other good are	o I of the sum of One Dollar (\$ 1.0 and valuable consideration.	0), the receipt of wh	nich is hereby
I hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica invention by the above application of substitutes, or extensions thereof, ar	nd to any and all improvements w tion and, in and to, all Letters Pat r any continuations, continuation-i	hich are disclosed ir ent to be obtained fo n-part, divisions, rer	n the or said newals.
I hereby covenant that no assignment entered into which would conflict with	nt, sale, agreement or encumbran n this assignment;	ce has been or will l	be made or
I further covenant that ASSIGNEE w and documents relating to said inver known and accessible to I and will te related thereto and will promptly exe	ntion and said Letters Patent and I estify as to the same in any interfe	legal equivalents as rence, litigation prod	may be
representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have her	plication, said invention and said L ecessary or desirable to carry out	etters Patent and sa the proposes thereo	aid f. (Date of signing)
Note: An application data sheet (PT0 inventive entity, must accompany thi	D/SB/14 or equivalent), including i s form. Use this form for <u>each ado</u>	naming the entire	

Page 3 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor: Ping-Hung Chiang

Date: DEC 1 8 2017

Signature: Prng-Hung Chrang

Page 4 of 14

F#NPO-P0002E-US1201 DSB0-106U028283

#### Title of Invention:

### TRANSISTOR WITH DUAL SPACER AND FORMING METHOD THEREOF

As the below named inventor, I here This declaration is directed to:	by declare that:		
☑ The attached application, or	r		
☐ United States application nu	ımber	_filed on	, or
☐ PCT international applicatio	n number	filed on	_
The above-identified application was	s made or authorized to be made	by me.	
I believe that I am the original invent application.	or or an original joint inventor of a	a claimed invention in the	
I hereby acknowledge that any willfu under18 U.S.C. 1001 by fine or impr	Il false statement made in this dec isonment of not more than five (5	claration is punishable ) years, or both.	
In consideration of the payment by	UNITED MICROELECTRON	NICS having a postal ad	dress of
No.3, Li-Hsin Road 2, Science	e-Based Industrial Park, Hsi	in-Chu City 300, Taiwa	ın, R.O.C
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I hereby sell, assign and transfer to a the entire right, title and interest in a invention as above-identified applica invention by the above application o substitutes, or extensions thereof, an	nd to any and all improvements w ution and, in and to, all Letters Pat r any continuations, continuation-	hich are disclosed in the ent to be obtained for said in-part, divisions, renewals.	
I hereby covenant that no assignment entered into which would conflict with	nt, sale, agreement or encumbrar h this assignment;	nce has been or will be mad	le or
I further covenant that ASSIGNEE wand documents relating to said inverknown and accessible to I and will terelated thereto and will promptly exe	ntion and said Letters Patent and estify as to the same in any interfe	legal equivalents as may be rence, litigation proceeding	е
representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said invention and said lecessary or desirable to carry out	etters Patent and said	of signing)
Note: An application data sheet (PT)			

Page 5 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

Inventor: Chang-Po Hsiung

Signature: Chang Po Hsiung

Date: DEC 1 8 2017

Page 6 of 14

#### Title of Invention:

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I hereby acknowledge that any willfu under18 U.S.C. 1001 by fine or impr	Il false statement mad isonment of not more	e in this declaration than five (5) years,	is punishable or both.	1
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I hereby covenant that no assignment entered into which would conflict with	nt, sale, agreement or h this assignment;	encumbrance has	been or will be	e made or
I further covenant that ASSIGNEE wand documents relating to said inverknown and accessible to I and will terelated thereto and will promptly exe	ntion and said Letters estify as to the same ir	Patent and legal eq n any interference, I	uivalents as n	nav be
representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said inventio ecessary or desirable t	n and said Letters F to carry out the prop	Patent and said poses thereof.	d Date of signing)
Note: An application data sheet (PT0 inventive entity, must accompany this				

Page 7 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NAI	WE OF INVENTOR(ASSIGNOR)			
Inventor:	Chia-Wen Lu	Date:	DEC 1 8 2017	
Signature:	Chia Wen Lu			

Page 8 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

#### Title of Invention:

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representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be ne IN WITNESS WHEREOF, I have he	olication, said invention ecessary or desirable t	n and said Letters I to carry out the pro	Patent and said poses thereof.	e of signing)
Note: An application data sheet (PT0 inventive entity, must accompany thi	D/SB/14 or equivalent) s form. Use this form t	), including naming for <u>each addition</u> al	the entire inventor.	

Page 9 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NA	ME OF INVENTOR (ASSIGNOR)			
Inventor:	Nien-Chung Li	Date:	DEC 1 8 2017	
Signature:	Nien Chung Li		The second secon	

Page 10 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

#### Title of Invention:

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representatives any and all papers, i maintain, issue and enforce said app equivalents thereof which may be no IN WITNESS WHEREOF, I have he	olication, said invention and said I ecessary or desirable to carry out	Letters Patent and said the proposes thereof.	of signing)		
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Page 11 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NAME OF INVENTOR(ASSIGNOR)

Inventor:

Wen-Fang Lee

Date:

Wen-Fong Loc

DEC 1 8 2017

Signature:

Page 12 of 14

#### Title of Invention:

### TRANSISTOR WITH DUAL SPACER AND FORMING METHOD THEREOF

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Page 13 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

LEGAL NAME OF INVENTOR(ASSIGNOR)					
Inventor:	Chih-Chung Wang	Date:	DEC 1 8 2017		
Signature:	Chih-Cheng, Wang				
	U A				

Page 14 of 14

NPO#NAU-P3168-USA:0 CUST#UMCD-2017-0476

**RECORDED: 12/18/2017** 

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