

## PATENT ASSIGNMENT COVER SHEET

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 Stylesheet Version v1.2

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ORI LASLO	12/18/2017
ERAN ARBEL	12/18/2017
MEBORACH BUBLIL	12/18/2017
<b>RECEIVING PARTY DATA</b>	
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<b>City:</b>	REDMOND
<b>State/Country:</b>	WASHINGTON
<b>Postal Code:</b>	98052
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	15846199
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	314-302-1451
<b>Email:</b>	docket@bjfip.com
<b>Correspondent Name:</b>	BARTA, JONES & FOLEY, P.C. (PATENT GROUP)
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<b>Address Line 2:</b>	SUITE 222
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<b>ATTORNEY DOCKET NUMBER:</b>	403441-US-NP
<b>NAME OF SUBMITTER:</b>	HOLLI L. TEMPLETON
<b>SIGNATURE:</b>	/Holli L. Templeton/
<b>DATE SIGNED:</b>	12/18/2017
<b>Total Attachments: 6</b>	
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I Ori LASLO ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "Capacitance Measuring Circuit" ("APPLICATION"), which:

☒ will be filed. ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S legal representative, Microsoft Technology Licensing, LLC., One Microsoft Way, Redmond, Washington 98052 (ASSIGNEE'S LEGAL REPRESENTATIVES), to insert here in parentheses (filed on December 18, 2017 and given Application No. 15/846,199 by the following Office United States Patent & Trademark ) this APPLICATION's Application No., filing date, and Office, when known;

☐ was filed on \_\_\_\_\_ and was given Application No. \_\_\_\_\_ by the following Office  
" \_\_\_\_\_ ";

Microsoft Technology Licensing, LLC, a Washington limited liability company, on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S LEGAL REPRESENTATIVES to insert the Application No., filing date, and Office into the following table for any APPLICATION DERIVATIVES after they are known;

Application No.	Filing Date	Office

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For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

<u>Ori LASLO</u>	<u>Dec. 18<sup>th</sup> 2017</u>
Inventor's Signature	Date
<u>Ori LASLO</u>	<u>ורי לזלו</u>
Printed Name in English	Printed Name in Native Language (if other than English)

I Eran ARBEL ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "Capacitance Measuring Circuit" ("APPLICATION"), which:

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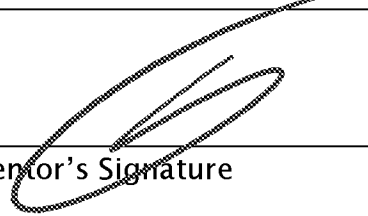
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	<u>2017 - Dec - 18</u>
Inventor's Signature	Date
<u>Eran ARBEL</u>	<u>ארבל / 18</u>
Printed Name in English	Printed Name in Native Language (if other than English)

I Meborach BUBLIL ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "Capacitance Measuring Circuit" ("APPLICATION"), which:

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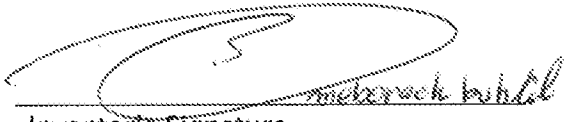
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 Inventor's Signature	<u>18/12/2017</u> Date
<u>Meborach BUBLIL</u> Printed Name in English	<u>מבורח בובליל</u> Printed Name in Native Language (if other than English)