

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4776248

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SILANNA SEMICONDUCTOR PTY LTD	01/28/2016
RECEIVING PARTY DATA	
Name:	THE SILANNA GROUP PTY LTD
Street Address:	37 BRANDL STREET
City:	EIGHT MILE PLAINS, QUEENSLAND
State/Country:	AUSTRALIA
Postal Code:	4113
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15853379
CORRESPONDENCE DATA	
Fax Number:	(858)847-0017
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	8584811883
Email:	paul@themuellerlawoffice.com
Correspondent Name:	PAUL STUART
Address Line 1:	THE MUELLER LAW OFFICE, P.C.
Address Line 2:	12707 HIGH BLUFF DRIVE, SUITE 200
Address Line 4:	SAN DIEGO, CALIFORNIA 92130
ATTORNEY DOCKET NUMBER:	SILAP017USD2
NAME OF SUBMITTER:	PAUL STUART
SIGNATURE:	/Paul Stuart/
DATE SIGNED:	01/15/2018
Total Attachments: 1	
source=20180111_SILAP017USD2_Assign_Silanna_Semi_to_Silanna_Group#page1.tif	

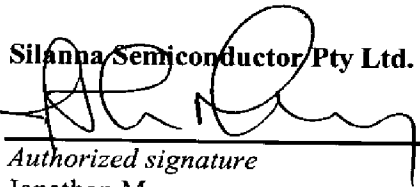
PATENT ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, **Silanna Semiconductor Pty Ltd.** ("Assignor"), hereby assigns to **The Silanna Group Pty Ltd.** ("Assignee"), all of its right, title and interest in and to the U.S. patent application listed below and the right to file non-provisionals, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues, and foreign filing of the patent application listed below, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the patent application listed below for the invention set forth in the patent application listed below and, including without limitation, any and all claims and potential claims of past, present and future infringement of such patent application and the right to sue and collect for past, present and future damages and any other causes of action under such patent application ("Assigned Patent Rights"):

Serial No.	Filing Date	Title	Inventors
14/976,337	December 21, 2015	Advanced Electronic Device Structures Using Semiconductor Structures and Superlattices	Petar Atanackovic, Matthew Godfrey

Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, letters patents, utility models or certificates of invention which may be granted upon the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

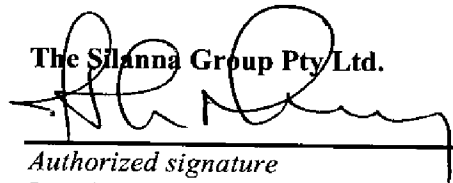
IN WITNESS WHEREOF Assignor has executed this Patent Assignment this 28th day of January, 2016.

Silanna Semiconductor Pty Ltd.


 Authorized signature
 Jonathan Murray

 Name (Printed)
 Company Secretary

 Title

The Silanna Group Pty Ltd.


 Authorized signature
 Jonathan Murray

 Name (Printed)
 Company Secretary

 Title