

PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	ASSIGNMENT
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PROPERTY NUMBERS Total: 8	
Property Type	Number
Patent Number:	6804239
Patent Number:	6888830
Patent Number:	6754223
Patent Number:	6760337
Patent Number:	7099328
Patent Number:	6822959
Patent Number:	RE42092
Patent Number:	6321331
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Total Attachments: 3

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, F. Poszat HU, L.L.C., a Delaware limited liability company with an address at 251 Little Falls Drive, Wilmington, DE 19808 ("*Assignor*"), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Assets 67 LLC, a Delaware limited liability company with an address at 251 Little Falls Drive, Wilmington, DE 19808 ("*Assignee*"), all of Assignor's right, title, and interest in and to the following (collectively, the "*Assigned Patent Rights*"):

- (a) the patents and patent applications listed in the table below (the "*Patents*");

<u>Patent/ Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6804239 (09/640231)	US	10/12/2004 (8/16/2000)	INTEGRATED CIRCUIT THAT PROCESSES COMMUNICATION PACKETS WITH CO-PROCESSOR CIRCUITRY TO CORRELATE A PACKET STREAM WITH CONTEXT INFORMATION Daniel J. Lussier; Joseph B. Tompkins; Wilson P. Snyder II;
6888830 (09/639915)	US	5/3/2005 (8/16/2000)	INTEGRATED CIRCUIT THAT PROCESSES COMMUNICATION PACKETS WITH SCHEDULER CIRCUITRY THAT EXECUTES SCHEDULING ALGORITHMS BASED ON CACHED SCHEDULING PARAMETERS Wilson P. Snyder II; Joseph B. Tompkins; Daniel J. Lussier
6754223 (09/640258)	US	6/22/2004 (8/16/2000)	INTEGRATED CIRCUIT THAT PROCESSES COMMUNICATION PACKETS WITH CO-PROCESSOR CIRCUITRY TO DETERMINE A PRIORITIZED PROCESSING ORDER FOR A CORE PROCESSOR Joseph B. Tompkins; Daniel J. Lussier; Wilson P. Snyder II

<u>Patent/ Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6760337 (09/639966)	US	7/6/2004 (8/16/2000)	INTEGRATED CIRCUIT THAT PROCESSES COMMUNICATION PACKETS WITH SCHEDULER CIRCUITRY HAVING MULTIPLE PRIORITY LEVELS Joseph B. Tompkins; Daniel J. Lussier; Wilson P. Snyder II
7099328 (09/919283)	US	8/29/2006 (7/31/2001)	METHOD FOR AUTOMATIC RESOURCE RESERVATION AND COMMUNICATION THAT FACILITATES USING MULTIPLE PROCESSING EVENTS FOR A SINGLE PROCESSING TASK Bruce G. Burns; Duane E. Galbi; Joseph B. Tompkins; Daniel J. Lussier
6822959 (09/919216)	US	11/23/2004 (7/31/2001)	ENHANCING PERFORMANCE BY PRE-FETCHING AND CACHING DATA DIRECTLY IN A COMMUNICATION PROCESSOR'S REGISTER SET Duane E. Galbi; Daniel J. Lussier; Wilson P. Snyder II
RE42092 (12/122625)	US	2/1/2011 (5/16/2008)	INTEGRATED CIRCUIT THAT PROCESSES COMMUNICATION PACKETS WITH A BUFFER MANAGEMENT ENGINE HAVING A POINTER CACHE Daniel J. Lussier; Joseph B. Tompkins; Wilson P. Snyder, II
6321331 (09/064474)	US	11/20/2001 (4/22/1998)	REAL TIME DEBUGGER INTERFACE FOR EMBEDDED SYSTEMS Subhash C. Roy; Paul Hembrook; Eugene L. Parrella; Richard Mariano

<u>Patent/ Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
CNZL99807656.2 (CN99807656.2)	CN	1/7/2009 (4/14/1999)	Real time debugger interface for embedded systems ROY S C; HEMBROOK P; PARRELLA E L

(b) any future reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents;

(c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and

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Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, take all reasonable steps necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights.

IN WITNESS WHEREOF this Assignment of Patent Rights is effective as of 15 December 2017.

ASSIGNOR:

F. Poszat HU, L.L.C.

By: 
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Title: Authorized Person