

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT4836485

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
XIANG LI	09/14/2017
ARVIND ANUMULA PARAMANANDAM	09/13/2017
PRASANNA UPADHYAYA	09/13/2017
XIAOYUE WANG	09/13/2017
RECEIVING PARTY DATA	
Name:	MARVELL SEMICONDUCTOR, INC.
Street Address:	5488 MARVELL LANE
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	15706712
CORRESPONDENCE DATA	
Fax Number:	(408)222-2755
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	408-222-2500
Email:	lgalang@marvell.com
Correspondent Name:	KELVIN VIVIAN
Address Line 1:	5488 MARVELL LANE
Address Line 4:	SANTA CLARA, CALIFORNIA 95054
ATTORNEY DOCKET NUMBER:	MP10140
NAME OF SUBMITTER:	KELVIN VIVIAN
SIGNATURE:	/Kelvin Vivian/
DATE SIGNED:	02/22/2018
Total Attachments: 3	
source=MP10140_(EXECUTED)_Assignment_Inventors_to_MSI#page1.tif	
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ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, each of us, the undersigned,

Xiang Li

Arvind Anumula Paramanandam

Prasanna Upadhyaya

Xiaoyue Wang

who have created a certain invention for which an application for United States Letters Patent was executed by each of us concurrently herewith and entitled:

**POWER-EFFICIENT SUCCESSIVE-APPROXIMATION ANALOG-TO-DIGITAL
CONVERTER USING LSB AVERAGING**

Does hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said application and to any and all inventions described in said application for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Does hereby further sell, assign and transfer to Marvell Semiconductor, Inc., its successors, assigns, and legal representatives, the full and exclusive right to United States Provisional Patent Application 62/396,472, filed on September 19, 2016, entitled, POWER-EFFICIENT METHOD AND APPARATUS TO IMPROVE SAR ADC SNR THROUGH LSBs-AVERAGING and to any and all inventions described in said provisional patent application(s) for the United States, its territorial possessions and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

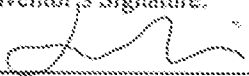
Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries, and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

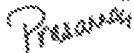
IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

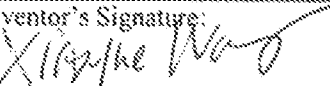
Docket#: MP10140

PATENT

Full Name of First Inventor: Xiang Li	
Inventor's Signature: 	Date: Month/Day/Year 09/14/17

Full Name of Second Inventor: Arvind Anumula Paramanandam	
Inventor's Signature:	Date: Month/Day/Year

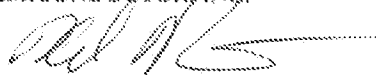
Full Name of Third Inventor: Prasanna Upadhyaya	
Inventor's Signature: 	Date: Month/Day/Year 09/13/17

Full Name of Fourth Inventor: Xiaoyue Wang	
Inventor's Signature: 	Date: Month/Day/Year 9/13/17

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By:


Kelvin Vivian
Director, Intellectual Property

Date

18 SEP 2017

Full Name of First Inventor:

Xiang Li

Inventor's Signature:

Date: Month/Day/Year

Full Name of Second Inventor:

Arvind Anumula Paramanandam

Inventor's Signature:

Date: Month/Day/Year

09/13/2017

Full Name of Third Inventor:

Prasanna Upadhyaya

Inventor's Signature:

Date: Month/Day/Year

Full Name of Fourth Inventor:

Xiaoyue Wang

Inventor's Signature:

Date: Month/Day/Year

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By:

Kelvin Vivian

Director, Intellectual Property

Date

18 SEP 2017